



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

SuperBlade SBI-7126T-T1 (Intel Xeon X5670, 2.93 GHz)

SPECint®_rate2006 = 355

SPECint_rate_base2006 = 332

CPU2006 license: 001176

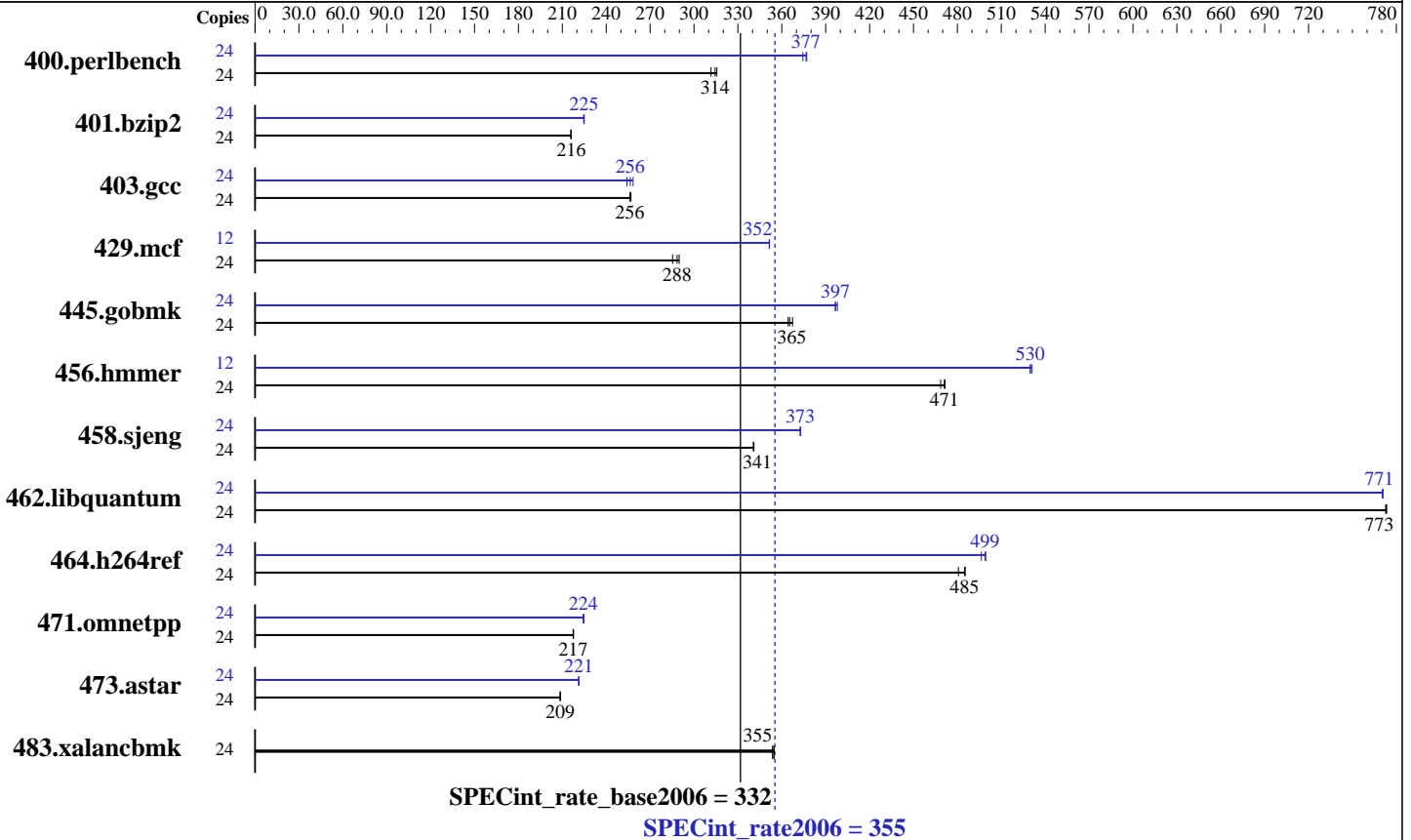
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Sep-2010

Hardware Availability: Mar-2010

Software Availability: Jan-2010



Hardware

CPU Name: Intel Xeon X5670
 CPU Characteristics: Intel Turbo Boost Technology up to 3.33 GHz
 CPU MHz: 2933
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 12 MB I+D on chip per chip
 Other Cache: None
 Memory: 24 GB (6 x 4 GB 2Rx4 PC3-10600R-9, ECC)
 Disk Subsystem: 1 x 150 GB SATA II, 10000 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 5.5, Kernel 2.6.18-194.el5
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: 1_cproc_p_11.1.064
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

SuperBlade SBI-7126T-T1 (Intel Xeon X5670, 2.93 GHz)

SPECint_rate2006 = 355

SPECint_rate_base2006 = 332

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Sep-2010
Hardware Availability: Mar-2010
Software Availability: Jan-2010

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	752	312	746	314	743	316	24	626	374	622	377	623	377
401.bzip2	24	1071	216	1072	216	1074	216	24	1030	225	1031	225	1030	225
403.gcc	24	754	256	752	257	754	256	24	748	258	760	254	753	256
429.mcf	24	767	285	755	290	759	288	12	311	351	311	352	311	352
445.gobmk	24	689	365	685	367	691	364	24	635	396	633	398	635	397
456.hammer	24	475	472	478	469	475	471	12	211	531	211	530	211	530
458.sjeng	24	853	341	852	341	852	341	24	779	373	779	373	779	373
462.libquantum	24	643	773	643	773	644	773	24	645	771	645	770	645	771
464.h264ref	24	1095	485	1105	481	1094	485	24	1070	496	1064	499	1065	499
471.omnetpp	24	690	217	689	218	690	217	24	668	225	668	224	669	224
473.astar	24	808	208	807	209	808	209	24	761	221	761	221	762	221
483.xalancbmk	24	468	354	467	355	467	355	24	468	354	467	355	467	355

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run

Platform Notes

Fan speed set to Full Speed in CMM.
As tested, the system used a Supermicro
SBE-710E-R42 enclosure.

General Notes

Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

SuperBlade SBI-7126T-T1 (Intel Xeon X5670, 2.93 GHz)

SPECint_rate2006 = 355

SPECint_rate_base2006 = 332

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: Sep-2010

Hardware Availability: Mar-2010

Software Availability: Jan-2010

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.icl11.1/libic11.1-32bit -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

462.libquantum: icc -m64

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

SuperBlade SBI-7126T-T1 (Intel Xeon X5670, 2.93 GHz)

SPECint_rate2006 = 355

SPECint_rate_base2006 = 332

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Sep-2010
Hardware Availability: Mar-2010
Software Availability: Jan-2010

Peak Portability Flags (Continued)

456.hmmcr: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -ansi-alias
401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32
403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static
429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
-ipo -no-prec-div -ansi-alias
456.hmmcr: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32
458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll4 -auto-ilp32
462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32
-opt-prefetch
464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap
473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

SuperBlade SBI-7126T-T1 (Intel Xeon X5670, 2.93 GHz)

SPECint_rate2006 = 355

SPECint_rate_base2006 = 332

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: Sep-2010

Hardware Availability: Mar-2010

Software Availability: Jan-2010

Peak Optimization Flags (Continued)

473.astar (continued):

`-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64`

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20101013.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20101013.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.

Report generated on Wed Jul 23 14:36:24 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 12 October 2010.