SPEC® CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY BX924 S2, Intel Xeon X5670, 2.93 GHz

SPECint®_rate2006 = 362

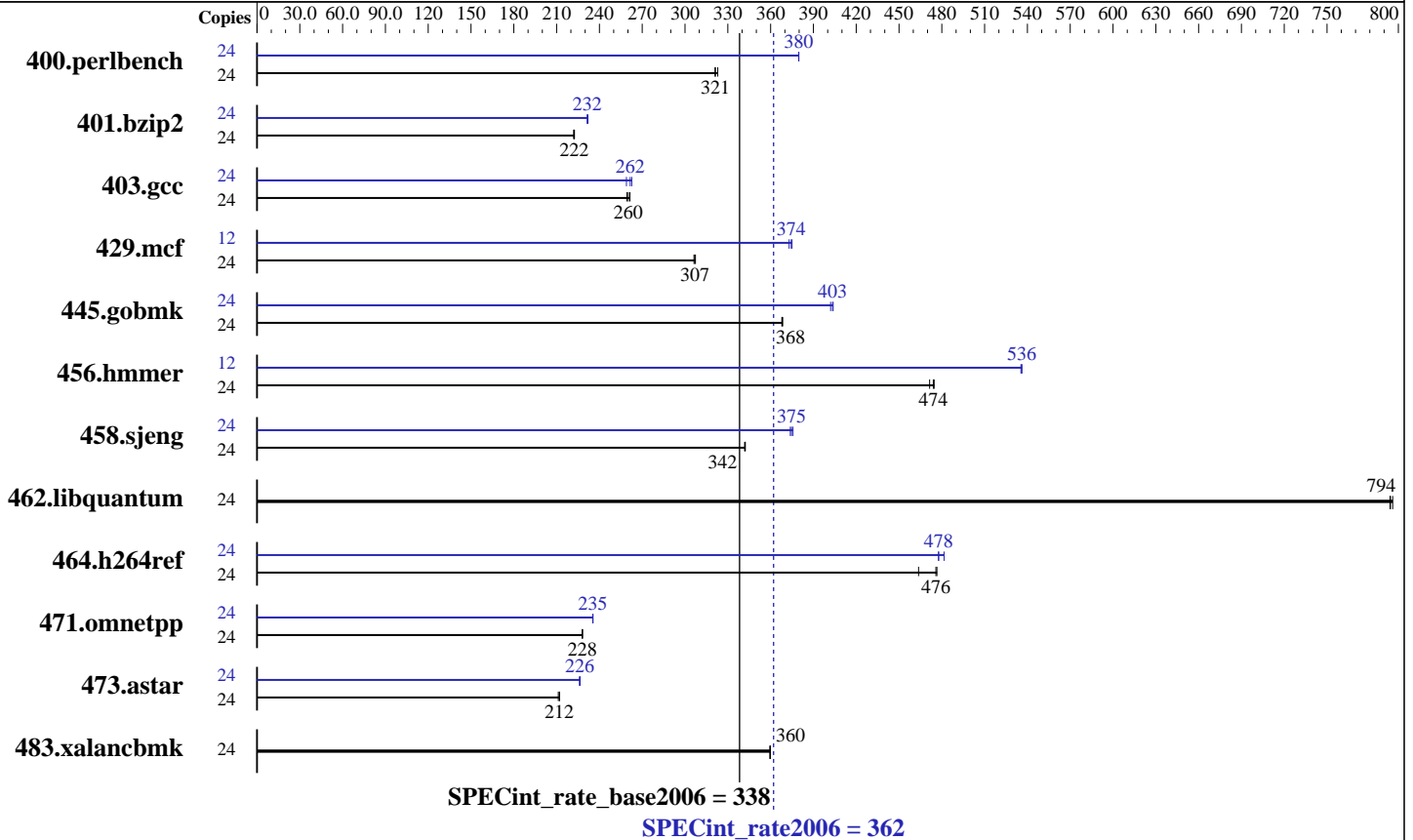SPECint_rate_base2006 = 338

CPU2006 license: 19

Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Jul-2010

Hardware Availability: Jun-2010

Software Availability: Jan-2010

SPECint_rate_base2006 = 338

SPECint_rate2006 = 362

Hardware

CPU Name: Intel Xeon X5670

CPU Characteristics: Intel Turbo Boost Technology up to 3.33 GHz

CPU MHz: 2933

FPU: Integrated

CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core

CPU(s) orderable: 1,2 chips

Primary Cache: 32 KB I + 32 KB D on chip per core

Secondary Cache: 256 KB I+D on chip per core

L3 Cache: 12 MB I+D on chip per chip

Other Cache: None

Memory: 48 GB (12x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC)

Disk Subsystem: 1 x SATA, 160 GB, 5400 RPM

Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default

Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1
Build 20091130 Package ID: l_cproc_p_11.1.064

Auto Parallel: No

File System: ext3

System State: Run level 3 (multi-user)

Base Pointers: 32-bit

Peak Pointers: 32/64-bit

Other Software: Microquill SmartHeap V8.1
Binutils 2.18.50.0.7.20080502

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY BX924 S2, Intel Xeon X5670, 2.93 GHz

SPECint_rate2006 = 362

SPECint_rate_base2006 = 338

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Jul-2010
Hardware Availability: Jun-2010
Software Availability: Jan-2010

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	730	321	726	323	**730**	**321**	24	618	380	617	380	**618**	**380**
401.bzip2	24	1044	222	1041	223	**1041**	**222**	24	998	232	**1000**	**232**	1001	231
403.gcc	24	739	261	**742**	**260**	745	259	24	**739**	**262**	735	263	746	259
429.mcf	24	715	306	**713**	**307**	712	307	12	**292**	**374**	293	373	292	375
445.gobmk	24	684	368	683	368	**684**	**368**	24	623	404	**624**	**403**	626	402
456.hmmer	24	**472**	**474**	472	475	475	471	12	209	535	**209**	**536**	209	536
458.sjeng	24	850	342	848	342	**849**	**342**	24	773	376	777	374	**775**	**375**
462.libquantum	24	626	794	**626**	**794**	625	796	24	626	794	**626**	**794**	625	796
464.h264ref	24	1114	477	1145	464	**1116**	**476**	24	**1112**	**478**	1112	478	1103	482
471.omnetpp	24	**658**	**228**	658	228	658	228	24	637	236	638	235	**637**	**235**
473.astar	24	**795**	**212**	795	212	798	211	24	744	227	745	226	**745**	**226**
483.xalancbmk	24	461	359	460	360	**460**	**360**	24	461	359	460	360	**460**	**360**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The config file option 'submit' was used.
     numactl was used to bind copies to the cores

Operating System Notes

     'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

    BIOS configuration:
    Data Reuse Optimization = Disable
    Performance/Power Setting = Traditional

General Notes

    For information about Fujitsu please visit: http://www.fujitsu.com
    Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

Base Compiler Invocation

C benchmarks:
     icc -m32

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY BX924 S2, Intel Xeon X5670, 2.93 GHz

SPECint_rate2006 =　362

SPECint_rate_base2006 =　338

CPU2006 license: 19		**Test date:**	Jul-2010
Test sponsor: Fujitsu		**Hardware Availability:**	Jun-2010
Tested by: Fujitsu		**Software Availability:**	Jan-2010

Base Compiler Invocation (Continued)

C++ benchmarks:
> `icpc -m32`

Base Portability Flags

> 400.perlbench: `-DSPEC_CPU_LINUX_IA32`
> 462.libquantum: `-DSPEC_CPU_LINUX`
> 483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
> `-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch`

C++ benchmarks:
> `-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs`
> `-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap`

Base Other Flags

C benchmarks:

> 403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
> `icc  -m32`

> 401.bzip2: `icc -m64`

> 456.hmmer: `icc -m64`

> 458.sjeng: `icc -m64`

C++ benchmarks (except as noted below):
> `icpc -m32`

> 473.astar: `icpc -m64`

Fujitsu

PRIMERGY BX924 S2, Intel Xeon X5670, 2.93 GHz

SPECint_rate2006 =   362

SPECint_rate_base2006 =   338

CPU2006 license: 19		**Test date:**	Jul-2010
Test sponsor: Fujitsu		**Hardware Availability:**	Jun-2010
Tested by: Fujitsu		**Software Availability:**	Jan-2010

Peak Portability Flags

```
   400.perlbench: -DSPEC_CPU_LINUX_IA32
       401.bzip2: -DSPEC_CPU_LP64
       456.hmmer: -DSPEC_CPU_LP64
       458.sjeng: -DSPEC_CPU_LP64
   462.libquantum: -DSPEC_CPU_LINUX
       473.astar: -DSPEC_CPU_LP64
   483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
   400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                  -prof-use(pass 2) -ansi-alias

       401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                  -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

         403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static

         429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

       445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
                  -ipo -no-prec-div -ansi-alias

       456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
                  -ansi-alias -auto-ilp32

       458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                  -prof-use(pass 2) -unroll4 -auto-ilp32

   462.libquantum: basepeak = yes

      464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                  -prof-use(pass 2) -unroll2 -ansi-alias
```

C++ benchmarks:

```
     471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                  -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
                  -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap
```

Fujitsu

PRIMERGY BX924 S2, Intel Xeon X5670, 2.93 GHz

SPECint_rate2006 =   362

SPECint_rate_base2006 = 338

CPU2006 license: 19	**Test date:** Jul-2010
Test sponsor: Fujitsu	**Hardware Availability:** Jun-2010
Tested by: Fujitsu	**Software Availability:** Jan-2010

Peak Optimization Flags (Continued)

473.astar: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
`-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2)
`-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs`
`-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64`

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.02.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.02.xml

SPEC and SPECint are registered trademarks of the Standard Performance
Evaluation Corporation. All other brand and product names appearing in
this result are trademarks or registered trademarks of their respective
holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 12:07:07 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 31 August 2010.