



SPEC[®] CINT2006 Result

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Itautec

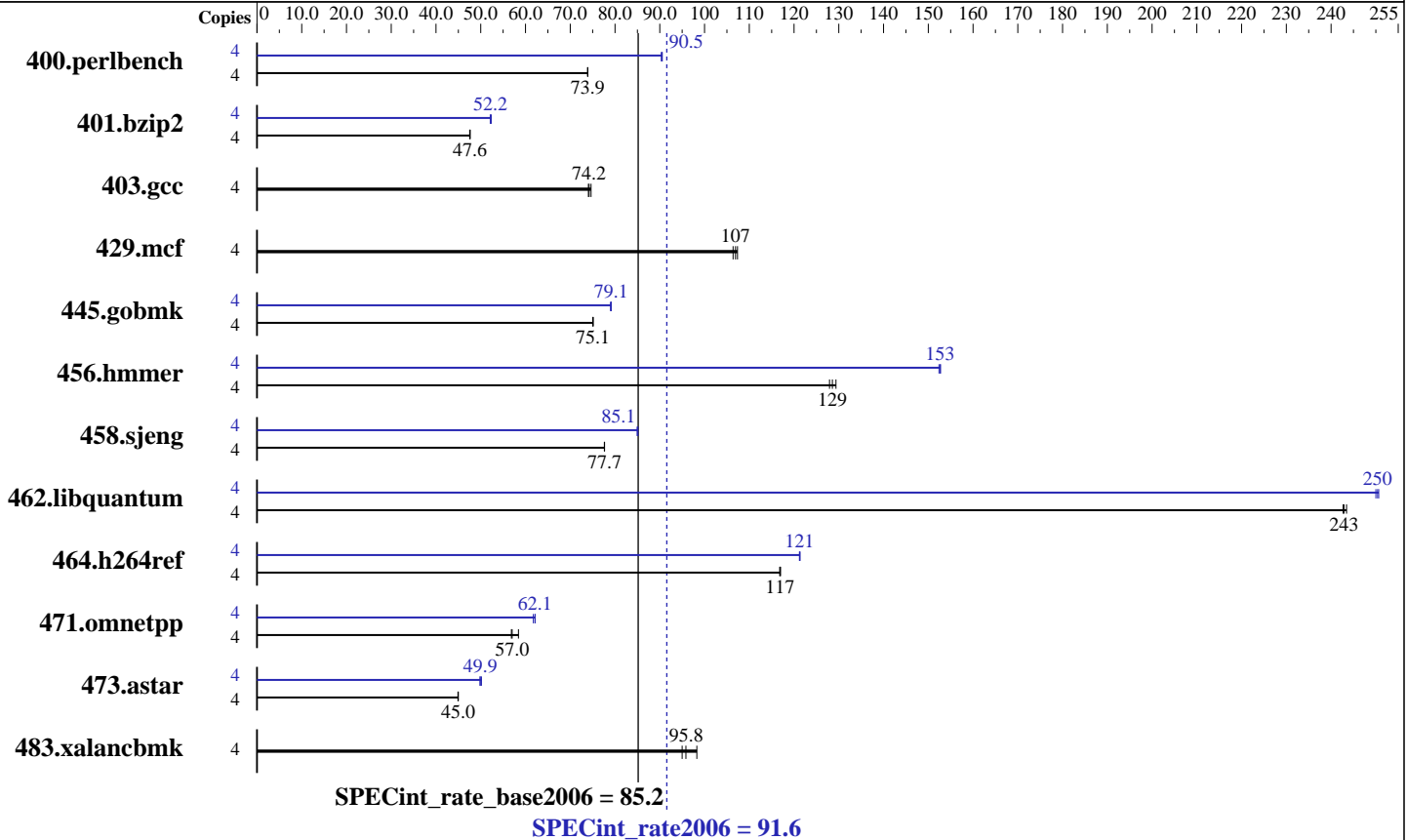
SPECint[®]_rate2006 = 91.6

Servidor Itautec LX113 (Intel Xeon X3430)

SPECint_rate_base2006 = 85.2

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Jul-2010
Hardware Availability: Apr-2010
Software Availability: Apr-2010



Hardware

CPU Name: Intel Xeon X3430
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 8 GB (4 x 2GB, DDR3-1333, Dual Rank, CL 9, ECC)
 Disk Subsystem: 1 x 160 GB SATA-2, 7200 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-smp
 Compiler: Intel C++ Professional Compiler 11.1 for Linux Build 20100414 Package ID: l_cproc_p_11.1.072
 Auto Parallel: No
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	529	73.9	529	73.9	<u>529</u>	<u>73.9</u>	4	<u>432</u>	<u>90.5</u>	433	90.3	432	90.5
401.bzip2	4	812	47.5	810	47.6	<u>811</u>	<u>47.6</u>	4	737	52.3	741	52.1	<u>739</u>	<u>52.2</u>
403.gcc	4	431	74.6	<u>434</u>	<u>74.2</u>	435	74.1	4	431	74.6	<u>434</u>	<u>74.2</u>	435	74.1
429.mcf	4	343	106	340	107	<u>341</u>	<u>107</u>	4	343	106	340	107	<u>341</u>	<u>107</u>
445.gobmk	4	559	75.1	559	75.1	<u>559</u>	<u>75.1</u>	4	<u>530</u>	<u>79.1</u>	530	79.2	531	79.0
456.hammer	4	<u>290</u>	<u>129</u>	288	129	292	128	4	<u>244</u>	<u>153</u>	245	152	244	153
458.sjeng	4	623	77.7	<u>623</u>	<u>77.7</u>	624	77.6	4	570	84.9	569	85.1	<u>569</u>	<u>85.1</u>
462.libquantum	4	342	243	<u>341</u>	<u>243</u>	340	243	4	332	250	331	251	<u>331</u>	<u>250</u>
464.h264ref	4	<u>757</u>	<u>117</u>	757	117	758	117	4	730	121	729	121	<u>730</u>	<u>121</u>
471.omnetpp	4	440	56.8	428	58.4	<u>439</u>	<u>57.0</u>	4	402	62.1	405	61.7	<u>402</u>	<u>62.1</u>
473.astar	4	625	44.9	624	45.0	<u>625</u>	<u>45.0</u>	4	<u>563</u>	<u>49.9</u>	564	49.8	560	50.2
483.xalancbmk	4	<u>288</u>	<u>95.8</u>	291	95.0	281	98.3	4	<u>288</u>	<u>95.8</u>	291	95.0	281	98.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.

General Notes

This result was measured on the Servidor Itautec LX103.
The Servidor Itautec LX103, the Servidor Itautec LX114 and the Servidor Itautec LX113 are electronically equivalent.

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/opt/sh/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32
401.bzip2: icc -m64
456.hmmer: icc -m64
458.sjeng: icc -m64
462.libquantum: icc -m64
C++ benchmarks (except as noted below):
icpc -m32
473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

456.hmmcr: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -ansi-alias
401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32
403.gcc: basepeak = yes
429.mcf: basepeak = yes
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
-ipo -no-prec-div -ansi-alias
456.hmmcr: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32
458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll4 -auto-ilp32
462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32
-opt-prefetch
464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/opt/sh/SmartHeap_8.1/lib -lsmartheap
473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs

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Peak Optimization Flags (Continued)

473.astar (continued):
-L/opt/sh/SmartHeap_8/lib -lsmartheap64

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itautec-Intel-ic11.1-linux64-revE.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itautec-Intel-ic11.1-linux64-revE.xml>

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For other inquiries, please contact webmaster@spec.org.

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