SPEC® CINT2006 Result
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Acer Incorporated

Gateway GR180 F1(Intel Xeon E5502)

SPECint®_rate2006 = 75.0

SPECint_rate_base2006 = 69.1

CPU2006 license: 97

Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Apr-2010

Hardware Availability: Jan-2010

Software Availability: Dec-2009

Benchmark	Peak	Base
400.perlbench	65.2	54.6
401.bzip2	40.5	37.6
403.gcc	60.4	61.0
429.mcf	102	102
445.gobmk	58.4	54.8
456.hmmer	108	91.4
458.sjeng	60.6	55.5
462.libquantum	296	261
464.h264ref	90.6	86.3
471.omnetpp	56.9	51.5
473.astar	43.9	37.8
483.xalancbmk		75.8

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Hardware

CPU Name:	Intel Xeon E5502
CPU Characteristics:	
CPU MHz:	1861
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	4 MB I+D on chip per chip
Other Cache:	None
Memory:	24 GB (12 x 2GB DDR3-1333 RDIMM, running at 800 MHz)
Disk Subsystem:	1 x 1000 GB SATA II, 7200 RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64) Kernel 2.6.27.19-5
Compiler:	Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064
Auto Parallel:	No
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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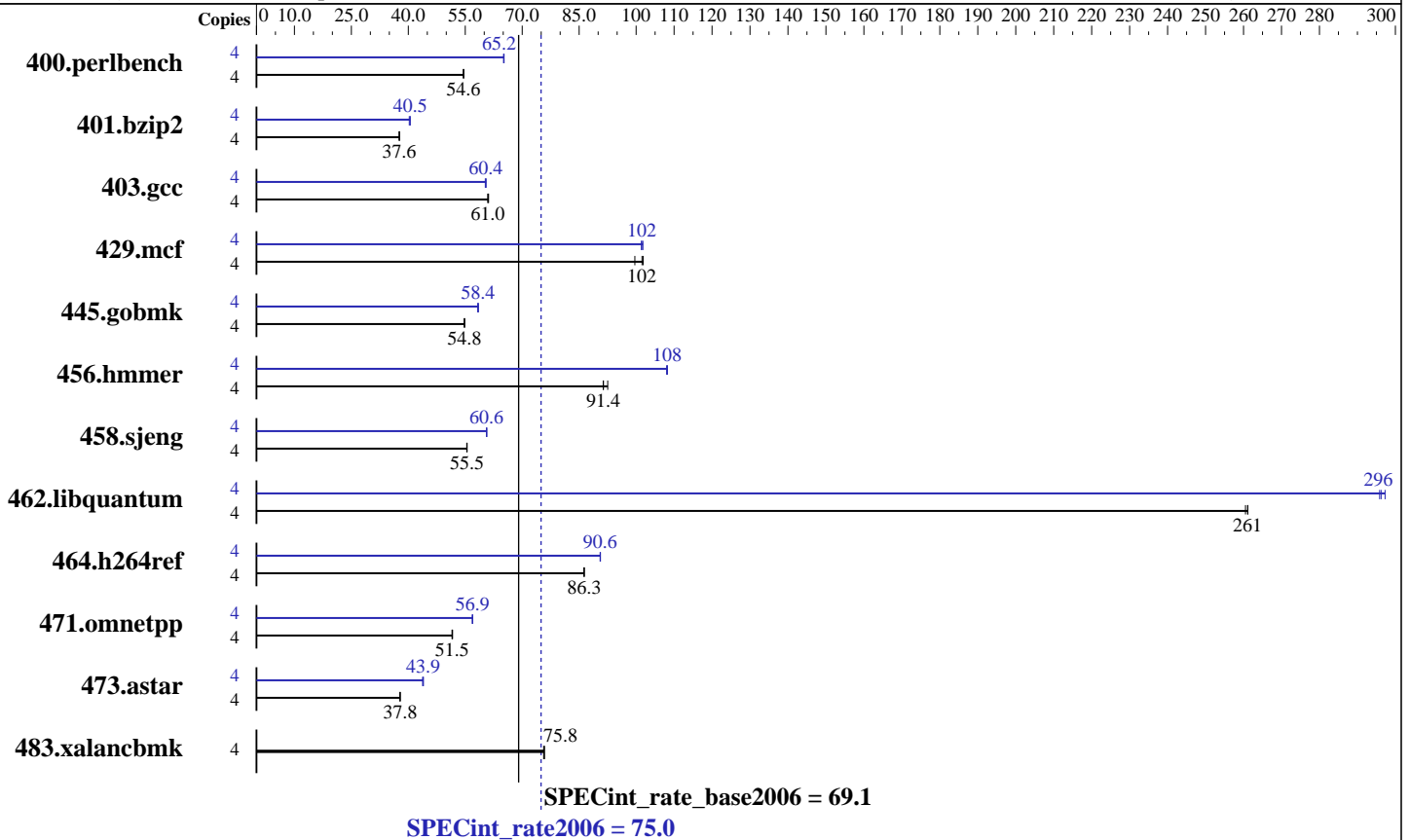SPECint_rate_base2006 = 69.1

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Results Table

Benchmark	Copies	Base Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Peak Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	717	54.5	**716**	**54.6**	716	54.6	4	599	65.2	**600**	**65.2**	600	65.1
401.bzip2	4	1027	37.6	1024	37.7	**1026**	**37.6**	4	954	40.5	**954**	**40.5**	959	40.2
403.gcc	4	528	61.0	527	61.1	**528**	**61.0**	4	534	60.3	532	60.5	**533**	**60.4**
429.mcf	4	366	99.7	**359**	**102**	358	102	4	358	102	360	101	**359**	**102**
445.gobmk	4	766	54.8	766	54.8	**766**	**54.8**	4	718	58.5	720	58.3	**719**	**58.4**
456.hmmer	4	409	91.3	**408**	**91.4**	403	92.5	4	345	108	345	108	**345**	**108**
458.sjeng	4	**873**	**55.5**	874	55.4	872	55.5	4	798	60.7	**798**	**60.6**	799	60.6
462.libquantum	4	**317**	**261**	317	261	318	261	4	**280**	**296**	279	297	280	296
464.h264ref	4	1026	86.3	1025	86.3	**1025**	**86.3**	4	978	90.5	**978**	**90.6**	977	90.6
471.omnetpp	4	**485**	**51.5**	484	51.7	485	51.5	4	**439**	**56.9**	439	56.9	440	56.8
473.astar	4	742	37.9	**742**	**37.8**	743	37.8	4	639	43.9	**639**	**43.9**	642	43.8
483.xalancbmk	4	364	75.8	365	75.6	**364**	**75.8**	4	364	75.8	365	75.6	**364**	**75.8**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The config file option 'submit' was used.
     numactl was used to bind copies to the cores

Operating System Notes

    'ulimit -s unlimited' was set for stacksize unlimited

General Notes

    This result was measured on the Gateway GR180 F1.
    The Acer AR180 F1 and Gateway GR180 F1 are electronically equivalent.

Base Compiler Invocation

C benchmarks:
      icc -m32

C++ benchmarks:
      icpc -m32

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Base Portability Flags

        400.perlbench: -DSPEC_CPU_LINUX_IA32
        462.libquantum: -DSPEC_CPU_LINUX
        483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
        -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:
        -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
        -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

Base Other Flags

C benchmarks:

        403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
        icc -m32

        401.bzip2: icc -m64

        456.hmmer: icc -m64

        458.sjeng: icc -m64

        462.libquantum: icc -m64

C++ benchmarks (except as noted below):
        icpc -m32

        473.astar: icpc -m64

Peak Portability Flags

        400.perlbench: -DSPEC_CPU_LINUX_IA32
        401.bzip2: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

```
      456.hmmer: -DSPEC_CPU_LP64
      458.sjeng: -DSPEC_CPU_LP64
   462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
       473.astar: -DSPEC_CPU_LP64
   483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
   400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                  -prof-use(pass 2) -ansi-alias

       401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                  -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

         403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static

         429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

       445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
                  -ipo -no-prec-div -ansi-alias

       456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
                  -ansi-alias -auto-ilp32

       458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                  -prof-use(pass 2) -unroll4 -auto-ilp32

   462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32
                  -opt-prefetch

      464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                  -prof-use(pass 2) -unroll2 -ansi-alias
```

C++ benchmarks:

```
      471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                  -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
                  -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

       473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                  -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
```

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Peak Optimization Flags (Continued)

473.astar (continued):
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
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