



SPEC® CINT2006 Result

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HITACHI

SPECint®_rate2006 = 350

BladeSymphony BS320 (Intel Xeon X5670)

SPECint_rate_base2006 = 326

CPU2006 license: 872

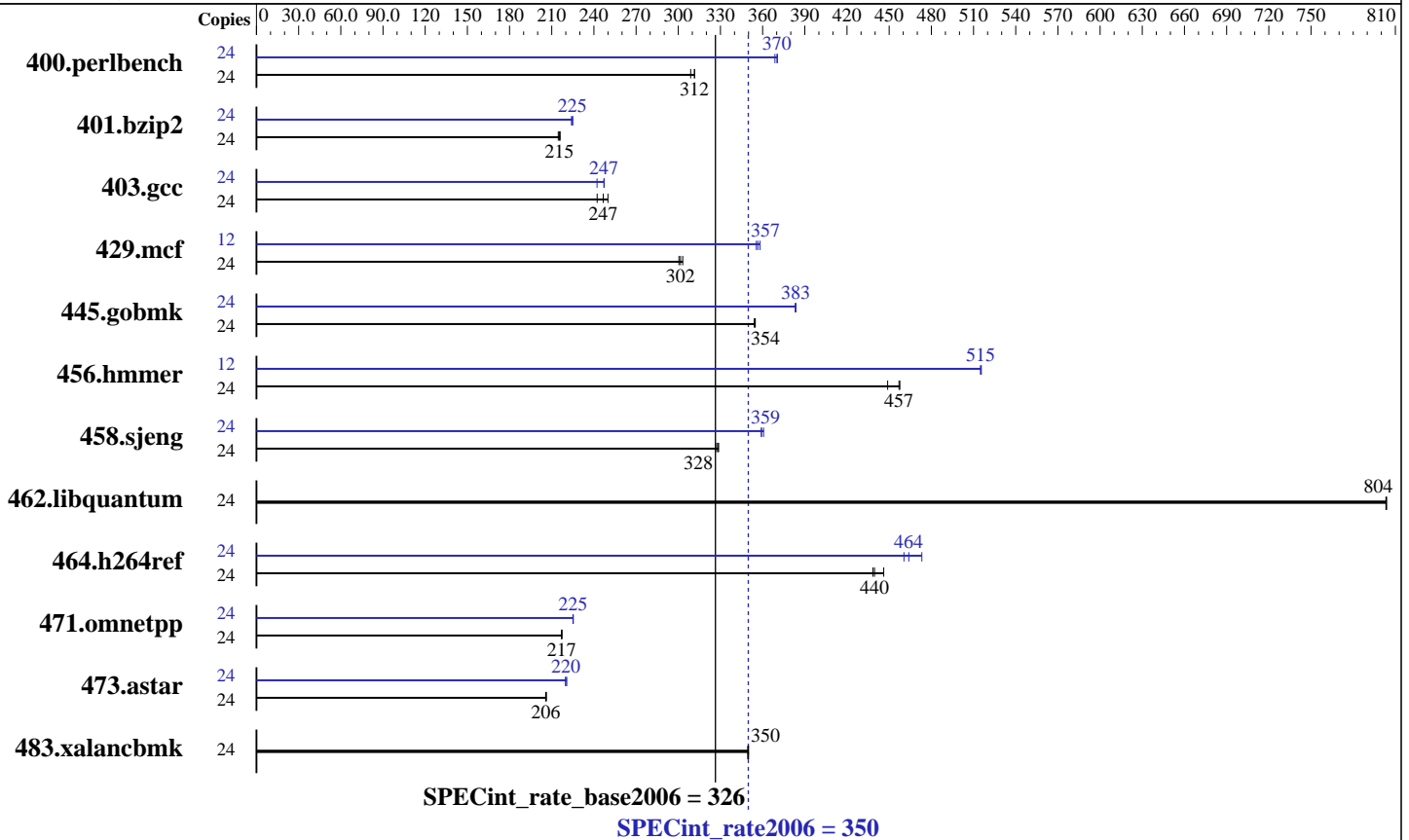
Test sponsor: HITACHI

Tested by: HITACHI

Test date: Mar-2010

Hardware Availability: Mar-2010

Software Availability: Dec-2009



Hardware

CPU Name: Intel Xeon X5670
 CPU Characteristics: Intel Turbo Boost Technology up to 3.33 GHz
 CPU MHz: 2933
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
 CPU(s) orderable: 1, 2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 12 MB I+D on chip per chip
 Other Cache: None
 Memory: 48 GB(6 x 8 GB PC3-10600R running at 1333 MHz, 2 rank)
 Disk Subsystem: 1 x 147 GB 10000 rpm SAS
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 5.4.3, Advanced Platform, Kernel 2.6.18-164.9.1.el5 on an x86_64
 Compiler: Intel C++ Compiler 11.1 for Linux Build 20091012 Package ID: l_cproc_p_11.1.059
 Auto Parallel: No
 File System: ext3
 System State: Multi-user run level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	<u>753</u>	<u>312</u>	759	309	753	312	24	636	369	633	371	<u>633</u>	<u>370</u>
401.bzip2	24	1072	216	1079	215	<u>1075</u>	<u>215</u>	24	1029	225	1034	224	<u>1031</u>	<u>225</u>
403.gcc	24	797	242	773	250	<u>783</u>	<u>247</u>	24	781	247	<u>782</u>	<u>247</u>	798	242
429.mcf	24	728	300	722	303	<u>726</u>	<u>302</u>	12	308	355	<u>307</u>	<u>357</u>	306	358
445.gobmk	24	710	355	<u>711</u>	<u>354</u>	711	354	24	657	383	656	384	<u>657</u>	<u>383</u>
456.hammer	24	499	449	<u>490</u>	<u>457</u>	489	458	12	217	516	<u>217</u>	<u>515</u>	217	515
458.sjeng	24	887	327	<u>885</u>	<u>328</u>	883	329	24	809	359	<u>808</u>	<u>359</u>	805	361
462.libquantum	24	619	804	<u>619</u>	<u>804</u>	619	803	24	619	804	<u>619</u>	<u>804</u>	619	803
464.h264ref	24	<u>1208</u>	<u>440</u>	1191	446	1212	438	24	<u>1145</u>	<u>464</u>	1122	473	1153	460
471.omnetpp	24	<u>691</u>	<u>217</u>	690	217	692	217	24	666	225	666	225	<u>666</u>	<u>225</u>
473.astar	24	<u>818</u>	<u>206</u>	819	206	817	206	24	763	221	<u>766</u>	<u>220</u>	767	220
483.xalancbmk	24	473	350	<u>474</u>	<u>350</u>	474	349	24	473	350	<u>474</u>	<u>350</u>	474	349

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
'/usr/bin/numactl' used to bind processes to CPUs

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch`

C++ benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/bsc/smartheap/lib -lsmartheap`

Base Other Flags

C benchmarks:

`403.gcc: -Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):

`icc -m32`

`401.bzip2: icc -m64`

`456.hmmer: icc -m64`

`458.sjeng: icc -m64`

C++ benchmarks (except as noted below):

`icpc -m32`

`473.astar: icpc -m64`

Peak Portability Flags

`400.perlbench: -DSPEC_CPU_LINUX_IA32`

`401.bzip2: -DSPEC_CPU_LP64`

`456.hmmer: -DSPEC_CPU_LP64`

`458.sjeng: -DSPEC_CPU_LP64`

`462.libquantum: -DSPEC_CPU_LINUX`

`473.astar: -DSPEC_CPU_LP64`

`483.xalancbmk: -DSPEC_CPU_LINUX`



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Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
 -L/home/bsc/smartheap/lib -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
 -L/home/bsc/smartheap/lib -lsmartheap64

483.xalanbmk: basepeak = yes

Peak Other Flags

C benchmarks:

Continued on next page



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Peak Other Flags (Continued)

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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