



# SPEC<sup>®</sup> CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Dell Inc.

PowerEdge R710  
(Intel Xeon X5550, 2.67 GHz)

SPECint<sup>®</sup>\_rate2006 = 252

SPECint\_rate\_base2006 = 237

CPU2006 license: 55

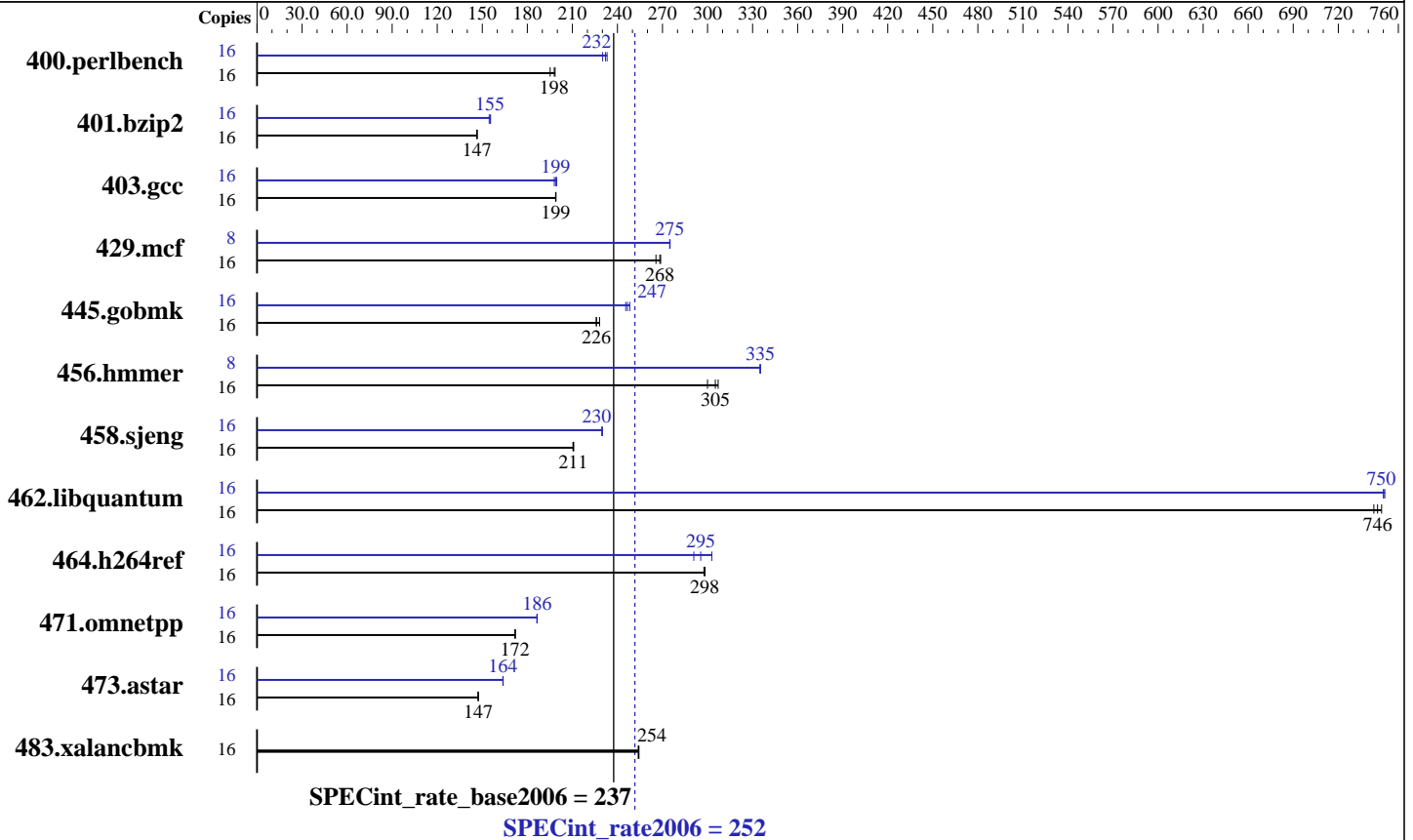
Test sponsor: Dell Inc.

Tested by: Bull SAS

Test date: Mar-2010

Hardware Availability: Mar-2009

Software Availability: Dec-2009



### Hardware

CPU Name: Intel Xeon X5550  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.06 GHz  
 CPU MHz: 2667  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 24 GB (6 x 4 GB PC3-10600R, 2 Rank, CL9-9-9, ECC)  
 Disk Subsystem: 1 x 73 GB SAS, 10000 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP2, SP2 with patch Linux kernel 20090119, Kernel 2.6.16.60-0.34-smp  
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l\_cproc\_p\_11.1.064  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECint\_rate2006 = 252

PowerEdge R710  
(Intel Xeon X5550, 2.67 GHz)

SPECint\_rate\_base2006 = 237

CPU2006 license: 55  
Test sponsor: Dell Inc.  
Tested by: Bull SAS

Test date: Mar-2010  
Hardware Availability: Mar-2009  
Software Availability: Dec-2009

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	801	195	<u>790</u>	<u>198</u>	787	199	16	671	233	<u>674</u>	<u>232</u>	679	230
401.bzip2	16	<u>1054</u>	<u>147</u>	1053	147	1055	146	16	993	156	999	155	<u>995</u>	<u>155</u>
403.gcc	16	<u>648</u>	<u>199</u>	647	199	648	199	16	645	200	651	198	<u>648</u>	<u>199</u>
429.mcf	16	549	266	543	269	<u>544</u>	<u>268</u>	8	265	275	<u>265</u>	<u>275</u>	265	275
445.gobmk	16	736	228	743	226	<u>743</u>	<u>226</u>	16	676	248	684	246	<u>680</u>	<u>247</u>
456.hammer	16	486	307	<u>489</u>	<u>305</u>	498	300	8	223	335	223	335	<u>223</u>	<u>335</u>
458.sjeng	16	<u>919</u>	<u>211</u>	918	211	920	210	16	842	230	<u>843</u>	<u>230</u>	843	230
462.libquantum	16	<u>444</u>	<u>746</u>	443	749	446	744	16	442	750	<u>442</u>	<u>750</u>	441	751
464.h264ref	16	1190	298	1187	298	<u>1187</u>	<u>298</u>	16	1169	303	<u>1198</u>	<u>295</u>	1217	291
471.omnetpp	16	582	172	<u>582</u>	<u>172</u>	582	172	16	536	186	<u>536</u>	<u>186</u>	536	187
473.astar	16	<u>763</u>	<u>147</u>	764	147	761	148	16	686	164	<u>686</u>	<u>164</u>	685	164
483.xalancbmk	16	435	254	434	254	<u>435</u>	<u>254</u>	16	435	254	434	254	<u>435</u>	<u>254</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## General Notes

The Dell PowerEdge R710 and the Bull NovaScale R460 F2 models are electronically equivalent. The results have been measured on a Bull NovaScale R460 F2 model.

## Base Compiler Invocation

C benchmarks:  
icc -m32

C++ benchmarks:  
icpc -m32



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**Dell Inc.**

PowerEdge R710  
(Intel Xeon X5550, 2.67 GHz)

**SPECint\_rate2006 = 252**

**SPECint\_rate\_base2006 = 237**

**CPU2006 license:** 55  
**Test sponsor:** Dell Inc.  
**Tested by:** Bull SAS

**Test date:** Mar-2010  
**Hardware Availability:** Mar-2009  
**Software Availability:** Dec-2009

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.icl1.1/libic11.1-32bit -lsmarheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

462.libquantum: icc -m64

C++ benchmarks (except as noted below):  
icpc -m32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge R710  
(Intel Xeon X5550, 2.67 GHz)

SPECint\_rate2006 = 252

SPECint\_rate\_base2006 = 237

CPU2006 license: 55  
Test sponsor: Dell Inc.  
Tested by: Bull SAS

Test date: Mar-2010  
Hardware Availability: Mar-2009  
Software Availability: Dec-2009

## Peak Portability Flags (Continued)

456.hmmcr: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -ansi-alias  
401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32  
403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static  
429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch  
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
-ipo -no-prec-div -ansi-alias  
456.hmmcr: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32  
458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll4 -auto-ilp32  
462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32  
-opt-prefetch  
464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap  
473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**Dell Inc.**

PowerEdge R710  
(Intel Xeon X5550, 2.67 GHz)

**SPECint\_rate2006 = 252**

**SPECint\_rate\_base2006 = 237**

**CPU2006 license:** 55

**Test sponsor:** Dell Inc.

**Tested by:** Bull SAS

**Test date:** Mar-2010

**Hardware Availability:** Mar-2009

**Software Availability:** Dec-2009

## Peak Optimization Flags (Continued)

473.astar (continued):

`-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64`

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.  
Report generated on Wed Jul 23 09:39:33 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 14 April 2010.