



# SPEC® CINT2006 Result

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Itautec

SPECint®\_rate2006 = 371

Servidor Itautec MX223 (Intel Xeon X5680)

SPECint\_rate\_base2006 = 344

CPU2006 license: 9001

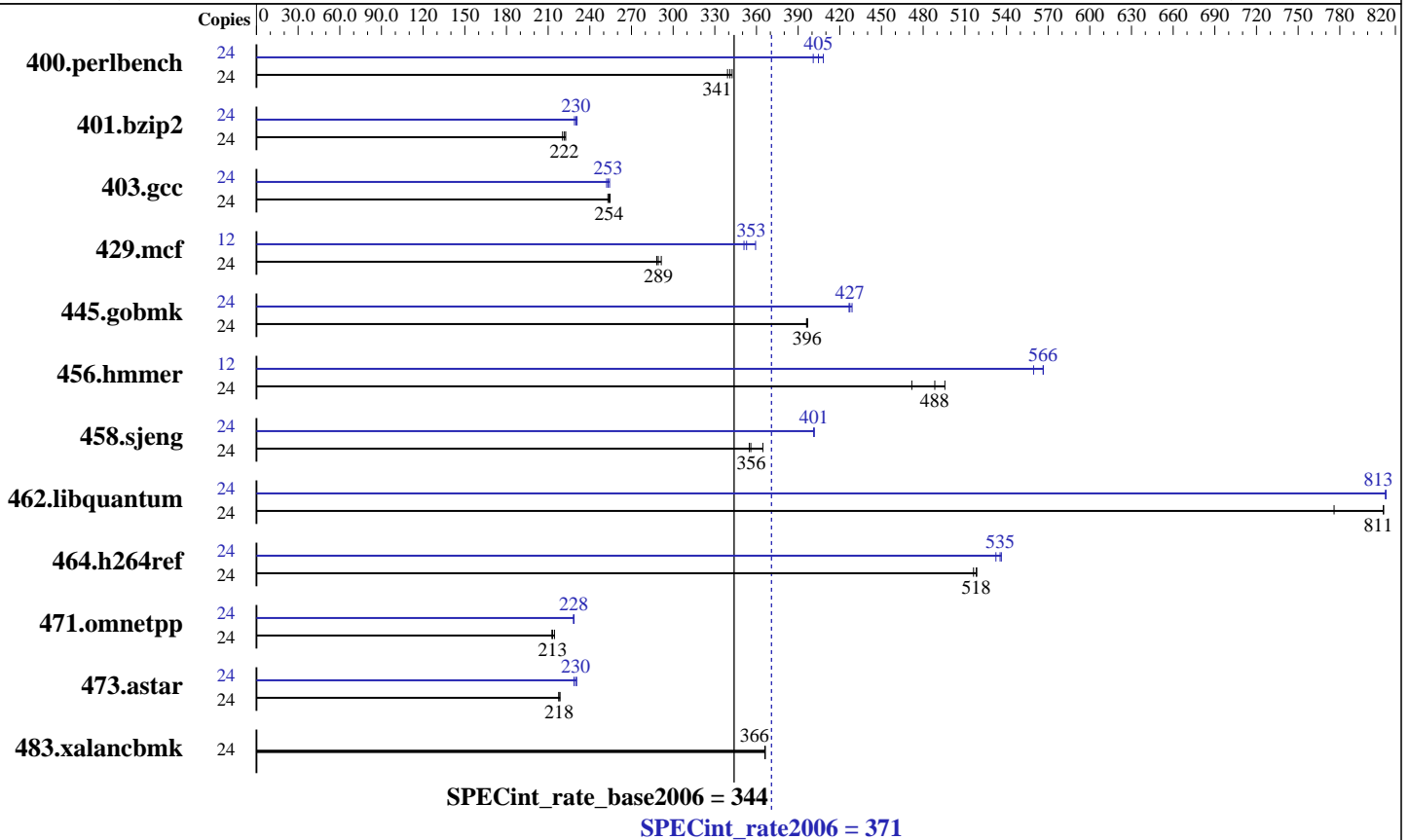
Test date: Mar-2010

Test sponsor: Itautec

Hardware Availability: Apr-2010

Tested by: Itautec

Software Availability: Feb-2010



## Hardware

CPU Name: Intel Xeon X5680  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz  
 CPU MHz: 3330  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1, 2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 12 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 24 GB (6 x 4GB DDR3-1333, CL 9, ECC)  
 Disk Subsystem: 1 x 160 GB SATA-2, 7200RPM  
 Other Hardware: None

## Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64), Kernel 2.6.27.19-5-smp  
 Compiler: Intel C++ Compiler 11.1 for Linux Build 20100203 Package ID: l\_cproc\_p\_11.1.069  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	<b>688</b>	<b>341</b>	685	342	691	339	24	585	401	<b>580</b>	<b>405</b>	574	408
401.bzip2	24	1051	220	<b>1045</b>	<b>222</b>	1040	223	24	1003	231	<b>1007</b>	<b>230</b>	1012	229
403.gcc	24	759	255	<b>761</b>	<b>254</b>	764	253	24	760	254	<b>763</b>	<b>253</b>	766	252
429.mcf	24	759	288	<b>757</b>	<b>289</b>	751	291	12	<b>310</b>	<b>353</b>	305	359	312	351
445.gobmk	24	636	396	634	397	<b>635</b>	<b>396</b>	24	587	429	<b>589</b>	<b>427</b>	590	427
456.hammer	24	452	496	<b>458</b>	<b>488</b>	475	472	12	200	560	198	567	<b>198</b>	<b>566</b>
458.sjeng	24	797	365	819	355	<b>816</b>	<b>356</b>	24	724	401	723	402	<b>724</b>	<b>401</b>
462.libquantum	24	641	776	613	812	<b>613</b>	<b>811</b>	24	612	813	<b>612</b>	<b>813</b>	612	813
464.h264ref	24	1024	519	<b>1025</b>	<b>518</b>	1029	516	24	990	536	998	532	<b>992</b>	<b>535</b>
471.omnetpp	24	<b>703</b>	<b>213</b>	705	213	699	215	24	656	229	657	228	<b>657</b>	<b>228</b>
473.astar	24	<b>774</b>	<b>218</b>	774	218	771	219	24	737	229	731	231	<b>733</b>	<b>230</b>
483.xalancbmk	24	452	366	452	366	<b>452</b>	<b>366</b>	24	452	366	452	366	<b>452</b>	<b>366</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.

## General Notes

This result was measured on the Servidor Itaotec MX203.  
The Servidor Itaotec MX203 and the Servidor Itaotec MX223 are electronically equivalent.

## Base Compiler Invocation

C benchmarks:  
icc -m32

C++ benchmarks:  
icpc -m32



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/home/richard/sh/SmartHeap\_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

462.libquantum: icc -m64

C++ benchmarks (except as noted below):  
icpc -m32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

456.hmmcr: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -ansi-alias  
401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32  
403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static  
429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch  
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
-ipo -no-prec-div -ansi-alias  
456.hmmcr: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32  
458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll4 -auto-ilp32  
462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32  
-opt-prefetch  
464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/home/richard/sh/SmartHeap\_8.1/lib -lsmartheap  
473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs

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## Peak Optimization Flags (Continued)

473.astar (continued):  
-L/home/richard/sh/SmartHeap\_8.1/lib -lsmartheap64

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itautec-Intel-ic11.1-linux64-revE.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itautec-Intel-ic11.1-linux64-revE.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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