



# SPEC® CINT2006 Result

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## Acer Incorporated

SPECint®\_rate2006 = 92.9

## Acer AT310 F1 (Intel Xeon X3430)

SPECint\_rate\_base2006 = 86.0

CPU2006 license: 97

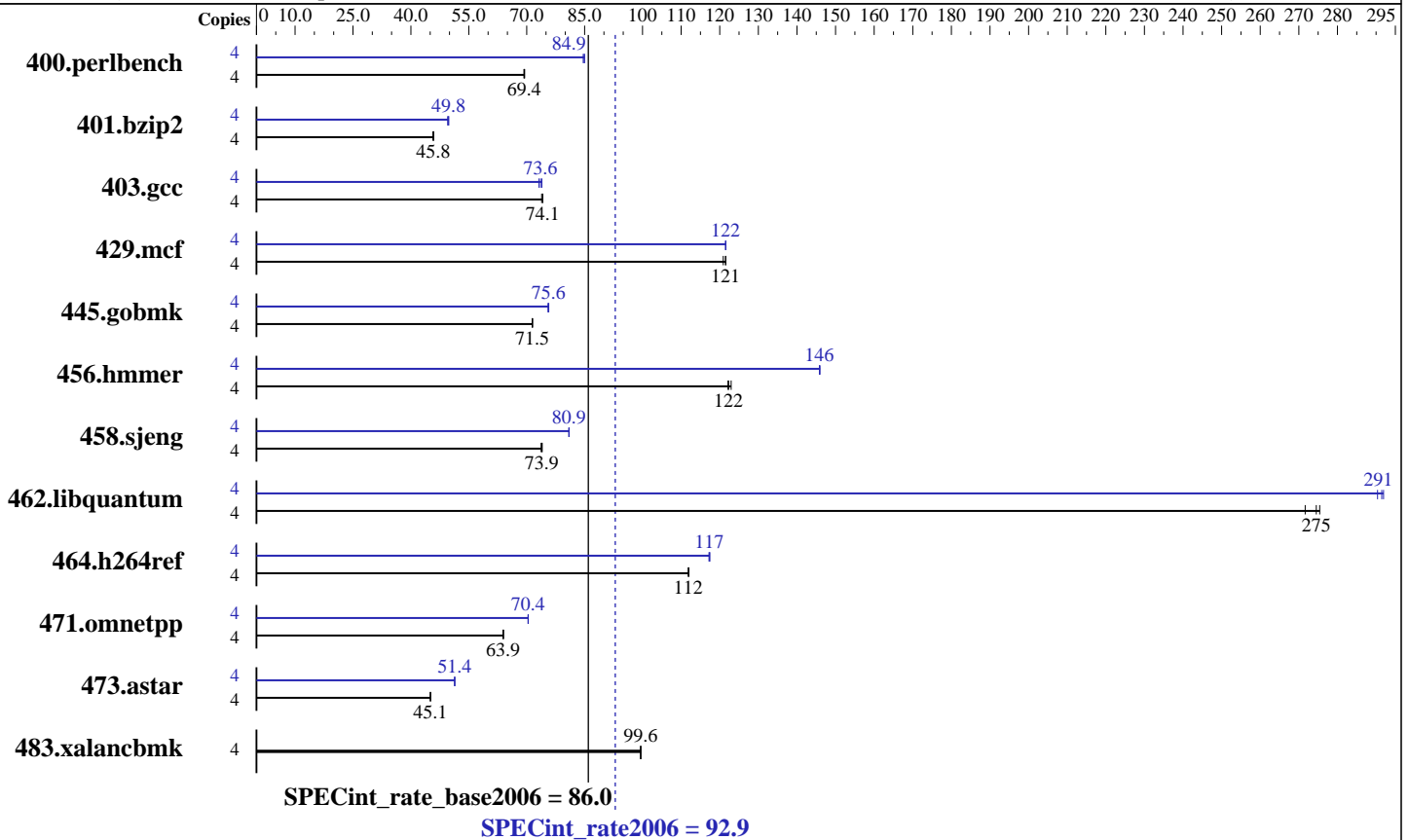
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Mar-2010

Hardware Availability: Mar-2010

Software Availability: Jan-2010



### Hardware

CPU Name: Intel Xeon X3430  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz  
 CPU MHz: 2400  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 8 GB (4 x 2048 MB Registered DDR3-1333)  
 Disk Subsystem: 1 x 1000 GB 7200RPM SATA HDD  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Sserver 11 (x86\_64)  
 Kernel 2.6.27.19-5  
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1  
 Build 20091130 Package ID: i\_cproc\_p\_11.1.064  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Multi-user, level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1  
 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	564	69.3	<b><u>563</u></b>	<b><u>69.4</u></b>	563	69.5	4	462	84.6	<b><u>460</u></b>	<b><u>84.9</u></b>	460	85.0
401.bzip2	4	844	45.7	841	45.9	<b><u>842</u></b>	<b><u>45.8</u></b>	4	<b><u>776</u></b>	<b><u>49.8</u></b>	780	49.5	775	49.8
403.gcc	4	<b><u>434</u></b>	<b><u>74.1</u></b>	436	73.9	434	74.1	4	<b><u>438</u></b>	<b><u>73.6</u></b>	440	73.2	435	73.9
429.mcf	4	302	121	300	122	<b><u>300</u></b>	<b><u>121</u></b>	4	300	122	<b><u>300</u></b>	<b><u>122</u></b>	300	122
445.gobmk	4	587	71.5	587	71.5	<b><u>587</u></b>	<b><u>71.5</u></b>	4	554	75.7	556	75.5	<b><u>555</u></b>	<b><u>75.6</u></b>
456.hammer	4	306	122	304	123	<b><u>305</u></b>	<b><u>122</u></b>	4	256	146	<b><u>256</u></b>	<b><u>146</u></b>	256	146
458.sjeng	4	654	74.0	<b><u>655</u></b>	<b><u>73.9</u></b>	656	73.7	4	598	80.9	598	81.0	<b><u>598</u></b>	<b><u>80.9</u></b>
462.libquantum	4	301	275	<b><u>302</u></b>	<b><u>275</u></b>	305	272	4	<b><u>284</u></b>	<b><u>291</u></b>	284	292	285	290
464.h264ref	4	791	112	<b><u>791</u></b>	<b><u>112</u></b>	790	112	4	755	117	754	117	<b><u>754</u></b>	<b><u>117</u></b>
471.omnetpp	4	391	64.0	391	63.9	<b><u>391</u></b>	<b><u>63.9</u></b>	4	355	70.3	355	70.4	<b><u>355</u></b>	<b><u>70.4</u></b>
473.astar	4	623	45.1	<b><u>623</u></b>	<b><u>45.1</u></b>	623	45.1	4	<b><u>547</u></b>	<b><u>51.4</u></b>	547	51.4	547	51.3
483.xalancbmk	4	<b><u>277</u></b>	<b><u>99.6</u></b>	277	99.5	277	99.6	4	<b><u>277</u></b>	<b><u>99.6</u></b>	277	99.5	277	99.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was set for stacksize unlimited

## General Notes

This result was measured on the Gateway GT310 F1.  
These 4 systems are electronically equivalent:  
Acer AR320 F1, Acer AT310 F1, Gateway GR320 F1, and Gateway GT310 F1.

## Base Compiler Invocation

C benchmarks:  
icc -m32

C++ benchmarks:  
icpc -m32



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.icl1.1/libic11.1-32bit -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

462.libquantum: icc -m64

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

456.hmmcr: -DSPEC\_CPU\_LP64  
 458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
 473.astar: -DSPEC\_CPU\_LP64  
 483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
 -ipo -no-prec-div -ansi-alias

456.hmmcr: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32  
 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
 -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs

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## Peak Optimization Flags (Continued)

473.astar (continued):

-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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