



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

Express5800/T110b
(Intel Pentium G6950)

SPECint®_rate2006 = 42.5

SPECint_rate_base2006 = 39.0

CPU2006 license: 9006

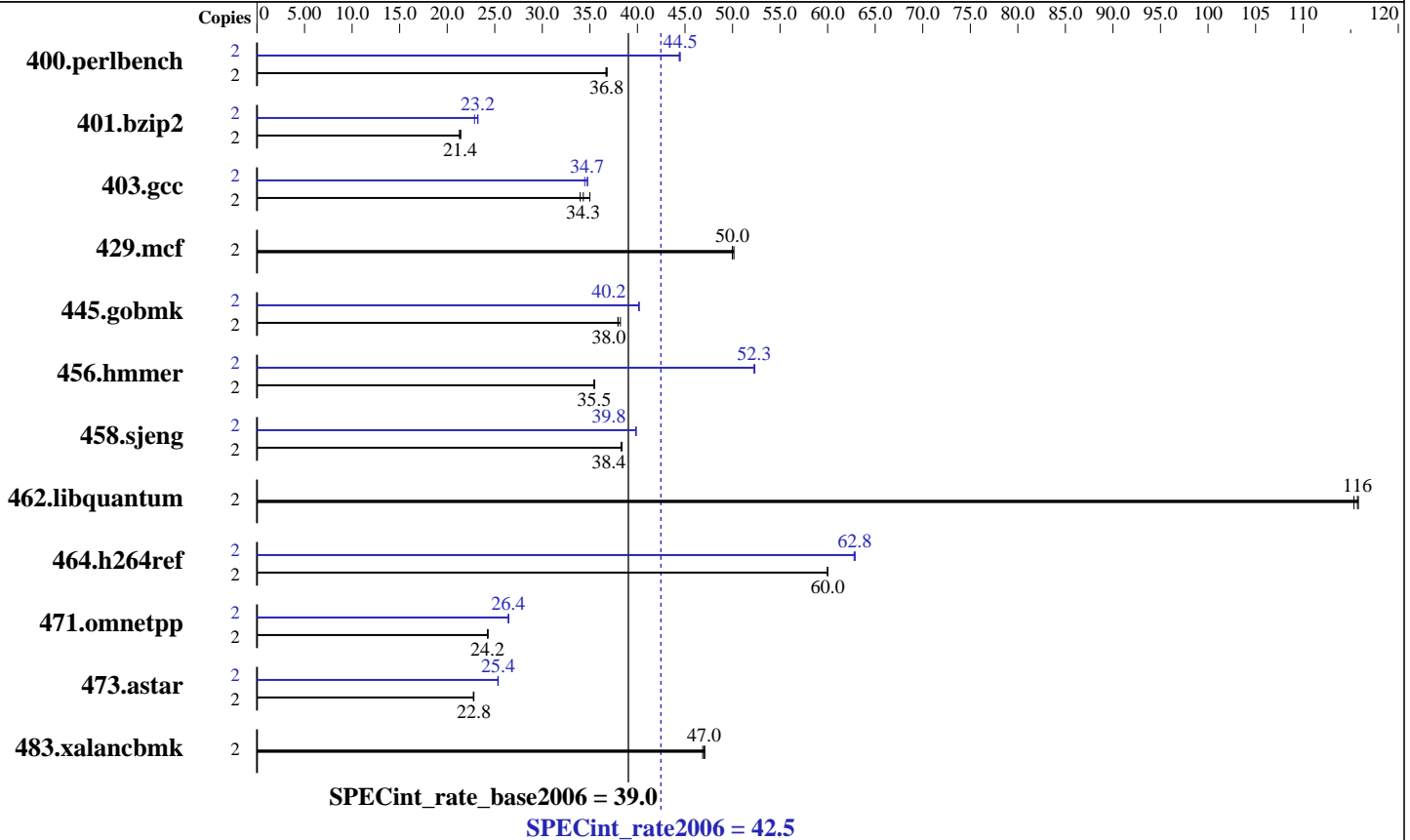
Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Feb-2010

Hardware Availability: Jan-2010

Software Availability: Nov-2009



Hardware

CPU Name: Intel Pentium G6950
 CPU Characteristics:
 CPU MHz: 2800
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 3 MB I+D on chip per chip
 Other Cache: None
 Memory: 8 GB (4 x 2 GB PC3-10600E running at 1066 MHz)
 Disk Subsystem: 1x160 GB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091012 Package ID: l_cproc_p_11.1.059
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	531	36.8	<u>532</u>	<u>36.8</u>	532	36.7	2	439	44.5	440	44.4	<u>439</u>	<u>44.5</u>
401.bzip2	2	907	21.3	901	21.4	<u>903</u>	<u>21.4</u>	2	<u>831</u>	<u>23.2</u>	831	23.2	844	22.9
403.gcc	2	460	35.0	<u>469</u>	<u>34.3</u>	474	34.0	2	463	34.8	<u>464</u>	<u>34.7</u>	467	34.5
429.mcf	2	365	50.0	364	50.1	<u>365</u>	<u>50.0</u>	2	365	50.0	364	50.1	<u>365</u>	<u>50.0</u>
445.gobmk	2	<u>552</u>	<u>38.0</u>	553	37.9	549	38.2	2	522	40.2	523	40.1	<u>522</u>	<u>40.2</u>
456.hammer	2	527	35.4	<u>526</u>	<u>35.5</u>	526	35.5	2	356	52.3	<u>357</u>	<u>52.3</u>	357	52.3
458.sjeng	2	631	38.4	632	38.3	<u>631</u>	<u>38.4</u>	2	607	39.8	607	39.8	<u>607</u>	<u>39.8</u>
462.libquantum	2	<u>358</u>	<u>116</u>	359	115	358	116	2	<u>358</u>	<u>116</u>	359	115	358	116
464.h264ref	2	738	60.0	737	60.0	<u>738</u>	<u>60.0</u>	2	704	62.9	<u>704</u>	<u>62.8</u>	705	62.8
471.omnetpp	2	514	24.3	<u>516</u>	<u>24.2</u>	516	24.2	2	472	26.5	473	26.4	<u>473</u>	<u>26.4</u>
473.astar	2	<u>616</u>	<u>22.8</u>	617	22.7	616	22.8	2	554	25.3	554	25.4	<u>554</u>	<u>25.4</u>
483.xalancbmk	2	293	47.1	<u>293</u>	<u>47.0</u>	294	46.9	2	293	47.1	<u>293</u>	<u>47.0</u>	294	46.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
taskset has been used to bind processes to cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

Default BIOS settings were used.

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

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Base Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSSE3 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSSE3 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/opt/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -opt-prefetch -ansi-alias

403.gcc: -xSSSE3 -ipo -O3 -no-prec-div -static -inline-alloc
-opt-malloc-options=3

429.mcf: basepeak = yes

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -O2 -ipo
-no-prec-div -ansi-alias

456.hmmcr: -xSSSE3 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -unroll4

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias
-opt-ra-region-strategy=routine -Wl,-z,muldefs
-L/opt/SmartHeap_8.1/lib64 -lsmartheap64

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revE.20100302.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revE.20100302.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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