SPEC® CINT2006 Result
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Fujitsu

CELSIUS W280, Intel Core i5-750

$SPECint®\_rate2006 =$ 99.2

$SPECint\_rate\_base2006 =$ 92.4

CPU2006 license: 19	**Test date:** Jan-2010
Test sponsor: Fujitsu	**Hardware Availability:** Jan-2010
Tested by: Fujitsu	**Software Availability:** Oct-2009

Copies

Benchmark	Copies	Peak	Base
400.perlbench	4	93.3	75.4
401.bzip2	4	54.3	50.0
403.gcc	4	79.2	79.4
429.mcf	4	127	127
445.gobmk	4	83.3	78.8
456.hmmer	4	160	135
458.sjeng	4	89.3	81.4
462.libquantum	4	277	
464.h264ref	4	129	124
471.omnetpp	4	71.9	66.7
473.astar	4	55.2	48.9
483.xalancbmk	4	105	

SPECint_rate_base2006 = 92.4

SPECint_rate2006 = 99.2

Hardware

CPU Name:	Intel Core i5-750
CPU Characteristics:	Intel Turbo Boost Technology up to 3.2 GHz
CPU MHz:	2667
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	8 GB (2x4 GB PC3-10600U, 2 rank, CL9)
Disk Subsystem:	1 x SATA II, 400 GB, 7200 rpm
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64), kernel 2.6.27.19-5-default
Compiler:	Intel C++ Compiler for IA32 and Intel 64, Version 11.1 Build 20091012 Package ID: l_cproc_p_11.1.059
Auto Parallel:	No
File System:	ext3
System State:	Multi-User Run Level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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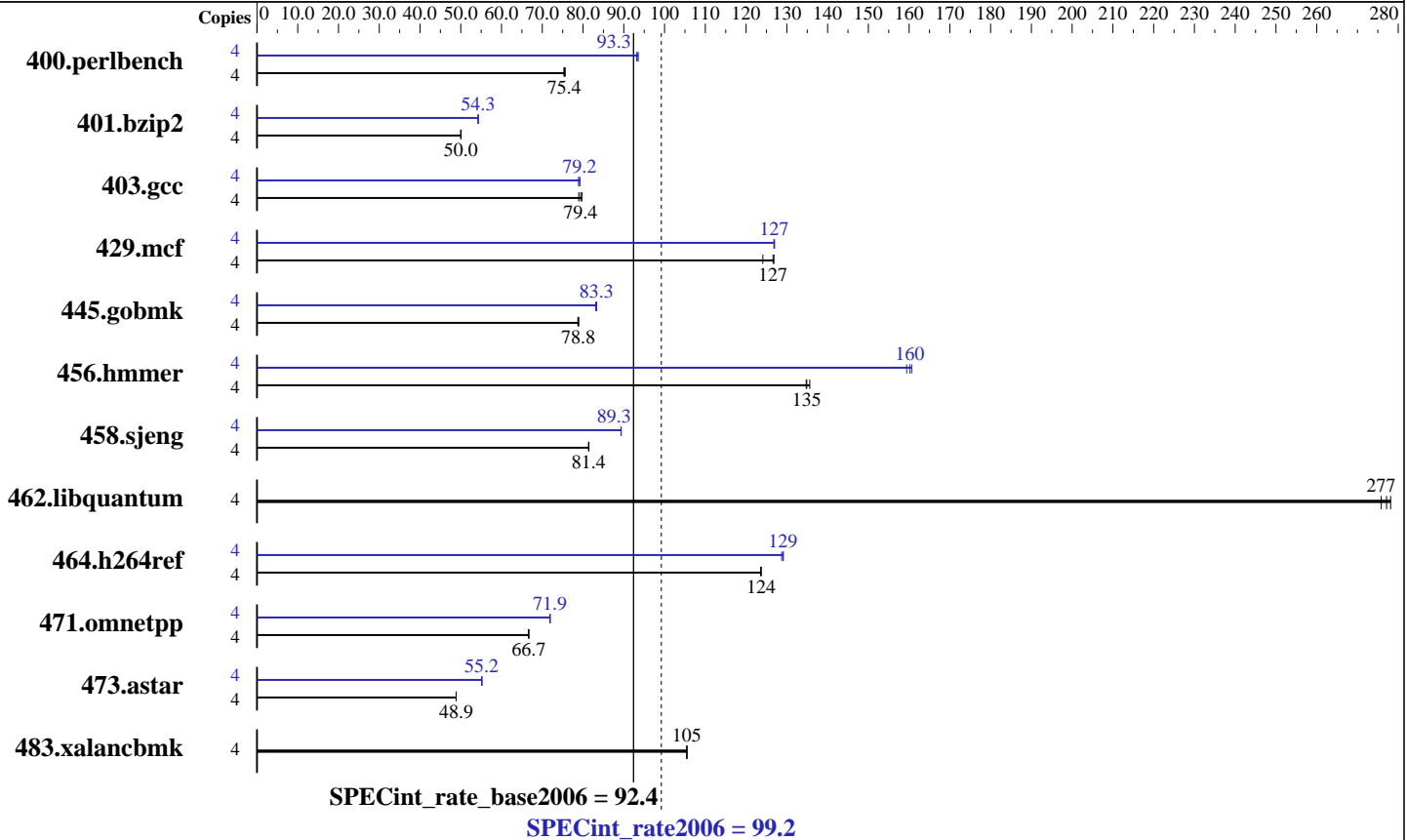
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Results Table

Benchmark	Base Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Peak Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	**518**	**75.4**	519	75.3	517	75.6	4	420	93.1	**419**	**93.3**	418	93.6
401.bzip2	4	771	50.1	**772**	**50.0**	772	50.0	4	712	54.2	711	54.3	**711**	**54.3**
403.gcc	4	404	79.7	**406**	**79.4**	408	78.9	4	**407**	**79.2**	408	78.9	406	79.2
429.mcf	4	294	124	**288**	**127**	288	127	4	**287**	**127**	288	127	287	127
445.gobmk	4	**532**	**78.8**	532	78.9	533	78.7	4	503	83.4	**504**	**83.3**	505	83.1
456.hmmer	4	277	135	**277**	**135**	275	136	4	**233**	**160**	234	159	232	161
458.sjeng	4	594	81.4	**595**	**81.4**	595	81.4	4	542	89.3	542	89.3	**542**	**89.3**
462.libquantum	4	**299**	**277**	300	276	298	278	4	**299**	**277**	300	276	298	278
464.h264ref	4	716	124	**716**	**124**	716	124	4	**686**	**129**	685	129	688	129
471.omnetpp	4	**375**	**66.7**	375	66.7	375	66.6	4	348	71.9	**348**	**71.9**	348	71.9
473.astar	4	575	48.9	**575**	**48.9**	575	48.9	4	**509**	**55.2**	509	55.1	509	55.2
483.xalancbmk	4	**262**	**105**	262	105	262	105	4	**262**	**105**	262	105	262	105

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The config file option 'submit' was used.
     numactl was used to bind copies to the cores

Operating System Notes

    'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Base Compiler Invocation

C benchmarks:
    icc -m32

C++ benchmarks:
    icpc -m32

Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

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Base Optimization Flags

C benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
```

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc  -m32
```

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks (except as noted below):
```
icpc -m32
```

473.astar: `icpc -m64`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-DSPEC_CPU_LINUX`

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Peak Optimization Flags

C benchmarks:

    400.perlbench: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
    `-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)`
    `-prof-use(pass 2) -ansi-alias`

    401.bzip2: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
    `-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)`
    `-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32`

    403.gcc: `-xSSE4.2 -ipo -O3 -no-prec-div -static`

    429.mcf: `-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch`

    445.gobmk: `-xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2`
    `-ipo -no-prec-div -ansi-alias`

    456.hmmer: `-xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2`
    `-ansi-alias -auto-ilp32`

    458.sjeng: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
    `-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)`
    `-prof-use(pass 2) -unroll4 -auto-ilp32`

    462.libquantum: `basepeak = yes`

    464.h264ref: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
    `-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)`
    `-prof-use(pass 2) -unroll2 -ansi-alias`

C++ benchmarks:

    471.omnetpp: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
    `-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
    `-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs`
    `-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap`

    473.astar: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
    `-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
    `-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs`
    `-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64`

    483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

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Peak Other Flags (Continued)

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revD.20100119.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revD.20100119.xml

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For other inquiries, please contact webmaster@spec.org.

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