



SPEC® CINT2006 Result

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Dell Inc.

SPECint®_rate2006 = 61.1

PowerEdge T310 (Intel Core i3-530, 2.93 GHz)

SPECint_rate_base2006 = 56.7

CPU2006 license: 55

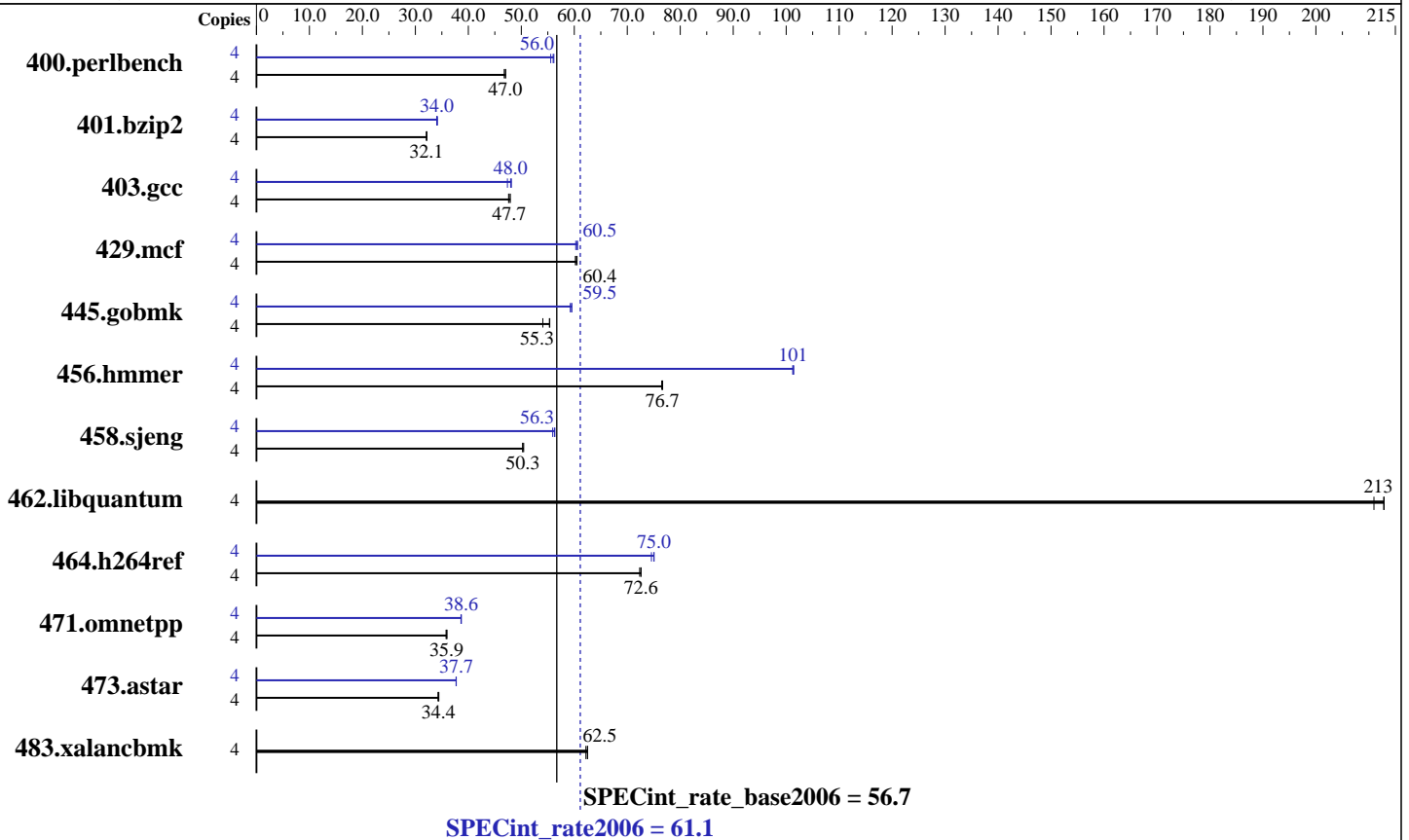
Test date: Jan-2010

Test sponsor: Dell Inc.

Hardware Availability: Jan-2010

Tested by: Dell Inc.

Software Availability: Dec-2009



Hardware

CPU Name: Intel Core i3-530
 CPU Characteristics:
 CPU MHz: 2933
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 4 MB I+D on chip per chip
 Other Cache: None
 Memory: 8 GB (4 x 2 GB DDR3-1333 DR UDIMM)
 Disk Subsystem: 1 x 160 GB 7200 RPM SATA
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 5.3, Kernel 2.6.18-128.el5
 Compiler: Intel C++ Compiler Professional Edition 11.1 for Linux Build 20091012 Package ID: 1_cproc_p_11.1.059
 Auto Parallel: No
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	832	47.0	836	46.8	831	47.0	4	703	55.6	696	56.1	698	56.0
401.bzip2	4	1200	32.2	1203	32.1	1203	32.1	4	1129	34.2	1134	34.0	1134	34.0
403.gcc	4	675	47.7	677	47.6	672	47.9	4	671	48.0	680	47.4	669	48.1
429.mcf	4	603	60.5	606	60.2	604	60.4	4	603	60.5	605	60.3	602	60.6
445.gobmk	4	776	54.1	758	55.4	759	55.3	4	705	59.5	708	59.2	705	59.5
456.hammer	4	487	76.7	488	76.5	487	76.7	4	368	101	368	101	369	101
458.sjeng	4	963	50.3	963	50.3	959	50.5	4	859	56.3	860	56.3	865	55.9
462.libquantum	4	389	213	389	213	393	211	4	389	213	389	213	393	211
464.h264ref	4	1219	72.6	1224	72.3	1220	72.6	4	1187	74.6	1179	75.1	1180	75.0
471.omnetpp	4	695	36.0	696	35.9	699	35.8	4	646	38.7	648	38.6	647	38.6
473.astar	4	819	34.3	816	34.4	817	34.4	4	745	37.7	744	37.7	745	37.7
483.xalancbmk	4	442	62.5	442	62.5	444	62.2	4	442	62.5	442	62.5	444	62.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

BIOS Settings:
Power Management = Maximum Performance (Default = Active Power Controller)

General Notes

The Dell PowerEdge T310 and the Bull NovaScale T820 F2 models are electronically equivalent.
This result was measured on a Dell PowerEdge T310.

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

401.bzip2: icc -m64

456.hmmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

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Peak Optimization Flags (Continued)

483.xalanbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revA.20100216.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revA.20100216.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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