



SPEC[®] CINT2006 Result

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Fujitsu

SPECint[®]_rate2006 = 127

CELSIUS W380, Intel Xeon X3470

SPECint_rate_base2006 = 120

CPU2006 license: 19

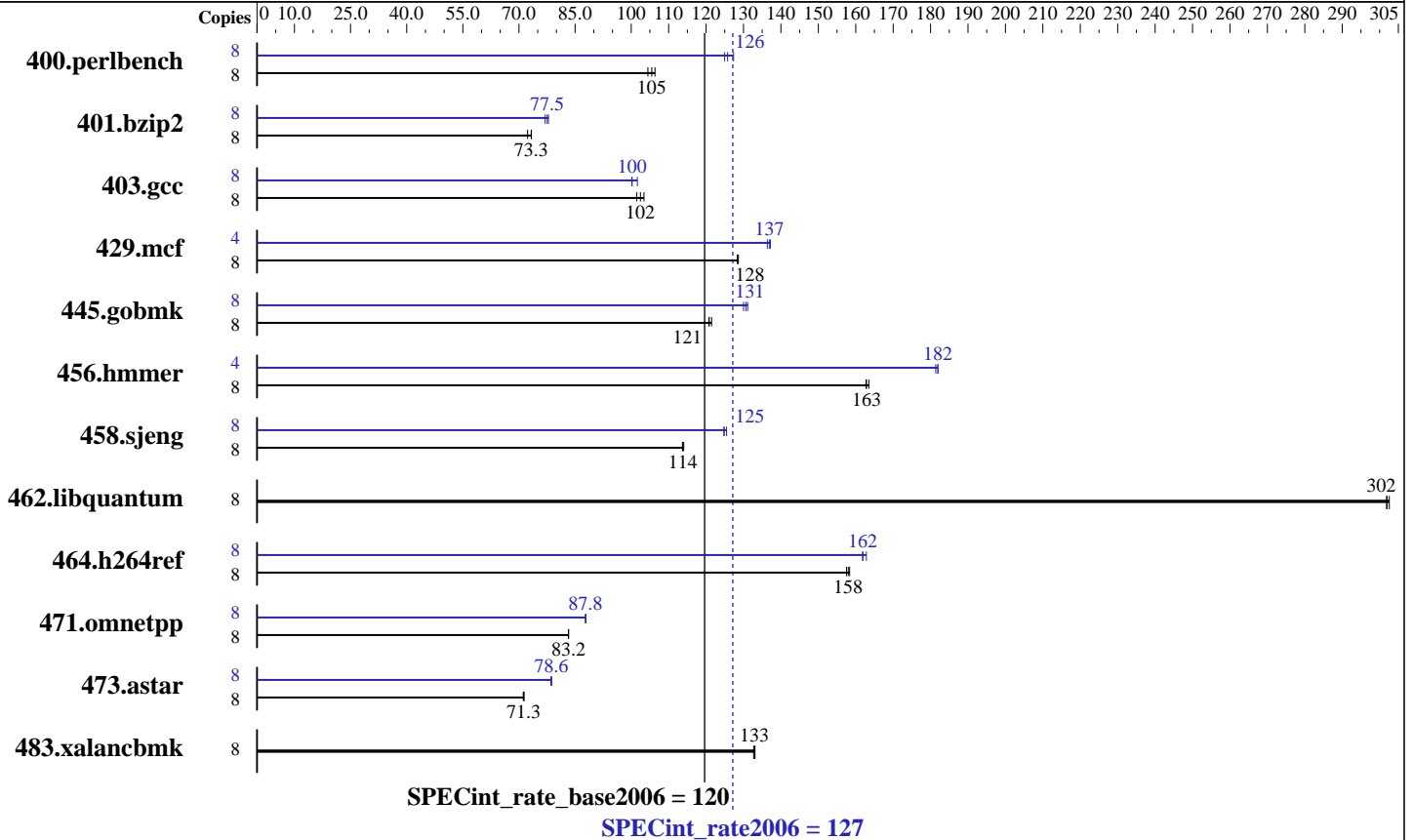
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Dec-2009

Hardware Availability: Mar-2010

Software Availability: Oct-2009



Hardware

CPU Name: Intel Xeon X3470
 CPU Characteristics: Intel Turbo Boost Technology up to 3.6 GHz
 CPU MHz: 2933
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 8 GB (2x4 GB PC3 10600E, 2 rank, CL9, ECC)
 Disk Subsystem: 1 x SATA II, 400 GB, 7200 rpm
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64), kernel 2.6.27.19-5-default
 Compiler: Intel C++ Compiler for IA32 and Intel 64, Version 11.1 Build 20091012 Package ID: 1_cproc_p_11.1.059
 Auto Parallel: No
 File System: ext3
 System State: Multi-User Run Level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	735	106	748	104	<u>741</u>	<u>105</u>	8	<u>621</u>	<u>126</u>	625	125	614	127
401.bzip2	8	<u>1054</u>	<u>73.3</u>	1068	72.3	1053	73.3	8	1003	77.0	991	77.9	<u>996</u>	<u>77.5</u>
403.gcc	8	623	103	635	101	<u>629</u>	<u>102</u>	8	634	102	643	100	<u>642</u>	<u>100</u>
429.mcf	8	569	128	567	129	<u>568</u>	<u>128</u>	4	268	136	<u>266</u>	<u>137</u>	266	137
445.gobmk	8	695	121	<u>694</u>	<u>121</u>	691	121	8	645	130	<u>642</u>	<u>131</u>	640	131
456.hammer	8	<u>458</u>	<u>163</u>	459	163	456	164	4	206	181	205	182	<u>205</u>	<u>182</u>
458.sjeng	8	852	114	849	114	<u>851</u>	<u>114</u>	8	776	125	771	125	<u>775</u>	<u>125</u>
462.libquantum	8	<u>549</u>	<u>302</u>	549	302	548	303	8	<u>549</u>	<u>302</u>	549	302	548	303
464.h264ref	8	<u>1121</u>	<u>158</u>	1124	158	1118	158	8	1087	163	1094	162	<u>1093</u>	<u>162</u>
471.omnetpp	8	601	83.2	<u>601</u>	<u>83.2</u>	601	83.3	8	<u>569</u>	<u>87.8</u>	570	87.8	569	87.9
473.astar	8	<u>788</u>	<u>71.3</u>	787	71.4	789	71.1	8	713	78.7	715	78.5	<u>714</u>	<u>78.6</u>
483.xalancbmk	8	<u>415</u>	<u>133</u>	416	133	415	133	8	<u>415</u>	<u>133</u>	416	133	415	133

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.icl1.1/libicl1.1-32bit -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

401.bzip2: -DSPEC_CPU_LP64

456.hmmer: -DSPEC_CPU_LP64

458.sjeng: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LINUX

473.astar: -DSPEC_CPU_LP64

483.xalancbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

483.xalanbmk: basepeak = yes

Peak Other Flags

C benchmarks:

Continued on next page



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Peak Other Flags (Continued)

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revD.20100119.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revD.20100119.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 06:23:42 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 19 January 2010.