# SPEC® CINT2006 Result
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## Fujitsu

PRIMERGY BX920 S1, Intel Xeon X5550, 2.67 GHz

SPECint®_rate2006 = 122

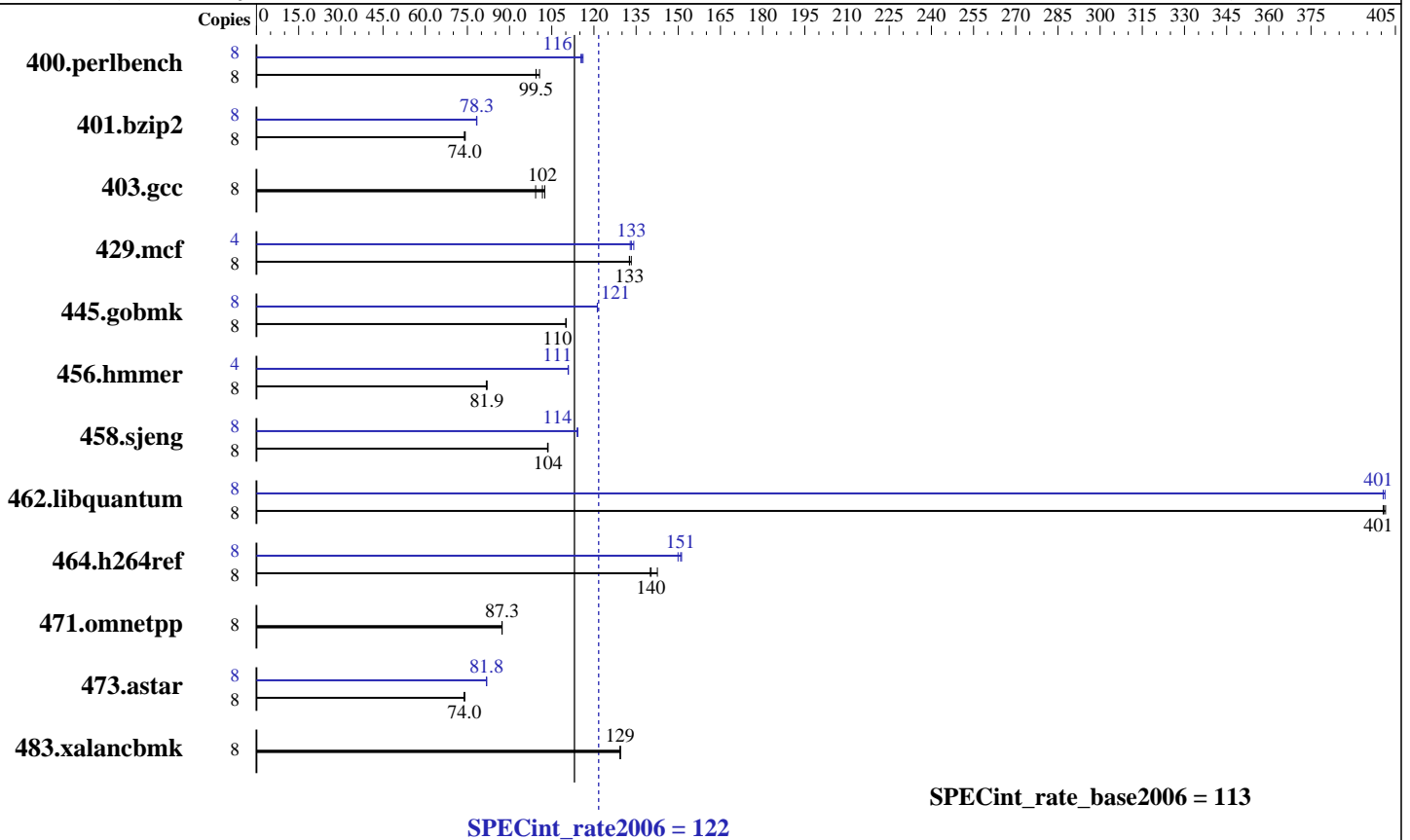SPECint_rate_base2006 = 113

**CPU2006 license:** 19
**Test sponsor:** Fujitsu
**Tested by:** Fujitsu

**Test date:** Oct-2009
**Hardware Availability:** Jun-2009
**Software Availability:** Feb-2009



SPECint_rate_base2006 = 113

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## Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon X5550 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 3.07 GHz |
| CPU MHz: | 2667 |
| FPU: | Integrated |
| CPU(s) enabled: | 4 cores, 1 chip, 4 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 8 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 24 GB (3x8 GB PC3-10600R, 2 rank, CL9-9-9, ECC) |
| Disk Subsystem: | 1 x SATA, 250 GB, 7200 RPM |
| Other Hardware: | None |

## Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp |
| Compiler: | Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080 |
| Auto Parallel: | No |
| File System: | ext3 |
| System State: | Multi-User Run Level 3 |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502 |

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## Results Table

| Benchmark | Base | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 8 | 776 | 101 | 786 | 99.4 | **786** | **99.5** | 8 | 677 | 115 | 673 | 116 | **675** | **116** |
| 401.bzip2 | 8 | 1040 | 74.2 | **1043** | **74.0** | 1044 | 74.0 | 8 | 985 | 78.4 | **986** | **78.3** | 987 | 78.3 |
| 403.gcc | 8 | 649 | 99.3 | 628 | 103 | **633** | **102** | 8 | 649 | 99.3 | 628 | 103 | **633** | **102** |
| 429.mcf | 8 | 550 | 133 | 547 | 133 | **550** | **133** | 4 | **274** | **133** | 274 | 133 | 272 | 134 |
| 445.gobmk | 8 | 762 | 110 | 763 | 110 | **762** | **110** | 8 | 692 | 121 | **692** | **121** | 692 | 121 |
| 456.hmmer | 8 | 911 | 82.0 | 913 | 81.7 | **911** | **81.9** | 4 | **337** | **111** | 336 | 111 | 337 | 111 |
| 458.sjeng | 8 | **934** | **104** | 934 | 104 | 935 | 104 | 8 | **848** | **114** | 849 | 114 | 847 | 114 |
| 462.libquantum | 8 | **413** | **401** | 414 | 401 | 413 | 401 | 8 | **414** | **401** | 414 | 401 | 413 | 401 |
| 464.h264ref | 8 | 1264 | 140 | 1242 | 143 | **1262** | **140** | 8 | 1171 | 151 | **1174** | **151** | 1181 | 150 |
| 471.omnetpp | 8 | **573** | **87.3** | 572 | 87.4 | 573 | 87.3 | 8 | **573** | **87.3** | 572 | 87.4 | 573 | 87.3 |
| 473.astar | 8 | **759** | **74.0** | 759 | 74.0 | 761 | 73.8 | 8 | 686 | 81.9 | **687** | **81.8** | 687 | 81.8 |
| 483.xalancbmk | 8 | **427** | **129** | 427 | 129 | 427 | 129 | 8 | **427** | **129** | 427 | 129 | 427 | 129 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Base Compiler Invocation

C benchmarks:
        icc

C++ benchmarks:
        icpc

## Base Portability Flags

        400.perlbench: -DSPEC_CPU_LINUX_IA32
        462.libquantum: -DSPEC_CPU_LINUX
        483.xalancbmk: -DSPEC_CPU_LINUX

## Base Optimization Flags

C benchmarks:
        -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
        -opt-malloc-options=3 -opt-prefetch

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# Base Optimization Flags (Continued)

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap
```

# Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

# Peak Compiler Invocation

C benchmarks (except as noted below):

```
icc
```

```
401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

```
456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

```
458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

C++ benchmarks (except as noted below):

```
icpc
```

```
473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc
```

# Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
   401.bzip2: -DSPEC_CPU_LP64
   456.hmmer: -DSPEC_CPU_LP64
   458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
   473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX
```

# Peak Optimization Flags

C benchmarks:

```
400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -ansi-alias -opt-prefetch
```

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# Peak Optimization Flags (Continued)

401.bzip2: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)`
`-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32`

403.gcc: `basepeak = yes`

429.mcf: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)`
`-prof-use(pass 2) -opt-prefetch`

445.gobmk: `-xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2`
`-ipo -no-prec-div -ansi-alias`

456.hmmer: `-xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2`
`-ansi-alias -auto-ilp32`

458.sjeng: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)`
`-prof-use(pass 2) -unroll4 -auto-ilp32`

462.libquantum: `-xSSE4.2 -ipo -O3 -no-prec-div -static`
`-opt-malloc-options=3 -opt-prefetch`

464.h264ref: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)`
`-prof-use(pass 2) -unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `basepeak = yes`

473.astar: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32`
`-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64`

483.xalancbmk: `basepeak = yes`

# Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

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The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090901.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090901.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 04:55:21 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 10 November 2009.