



SPEC® CINT2006 Result

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NTT System S. A.
NTT Business W 907G

SPECint®_rate2006 = 28.3
SPECint_rate_base2006 = 27.4

CPU2006 license: 9013

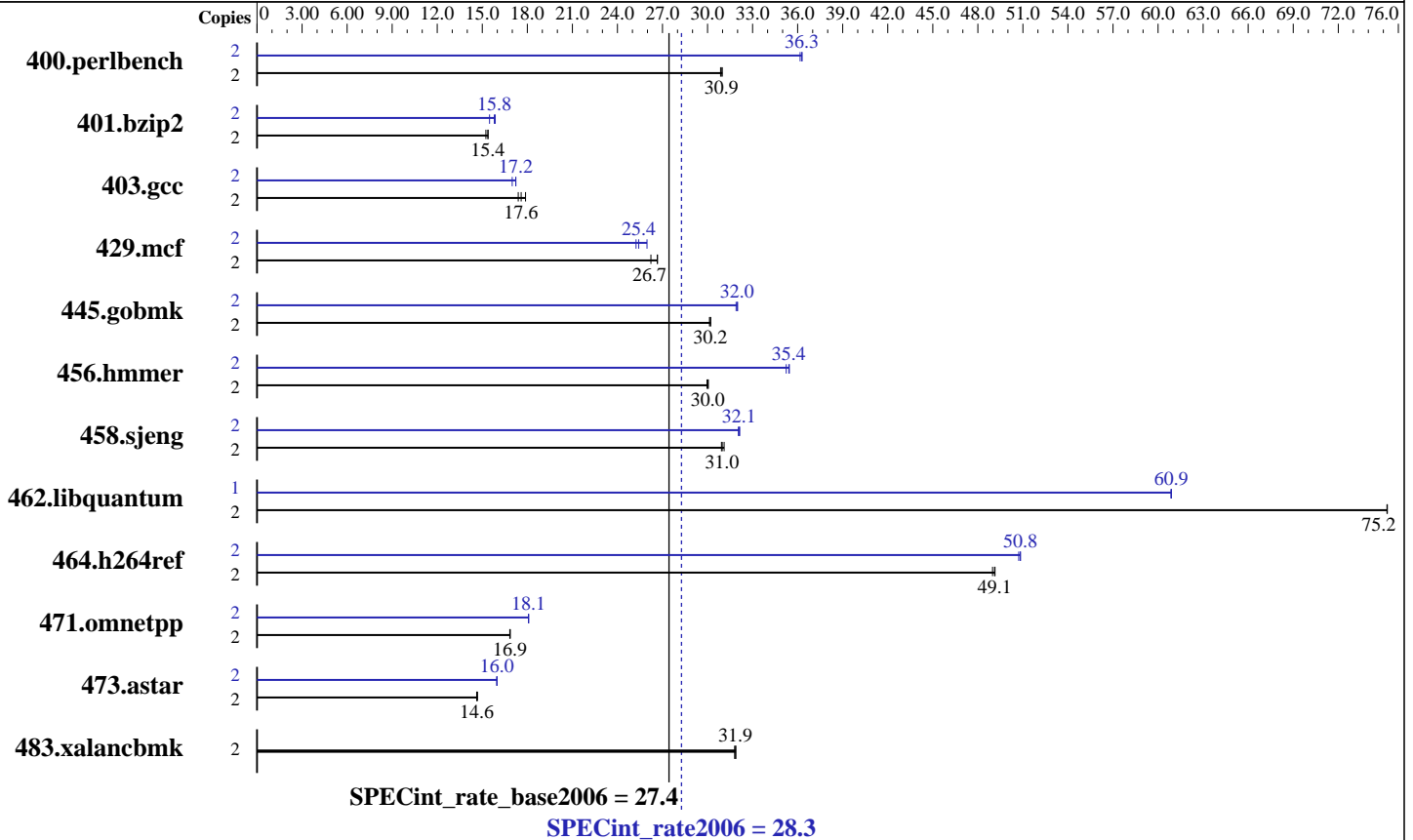
Test sponsor: NTT System S. A.

Tested by: NTT System S. A.

Test date: Jul-2009

Hardware Availability: Jul-2009

Software Availability: Nov-2009



Hardware

CPU Name: Intel Celeron E3200
 CPU Characteristics:
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 1 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 4 GB (2x2GB) DDR2 800Mhz
 Disk Subsystem: 250 GB SATA, 7200RPM
 Other Hardware: None

Software

Operating System: SuSe Linux SLES10 SP2, Kernel 2.6.16.60-0.21-smp
 Compiler: Intel C++ Compiler 11.0 for Linux
 Build 20080930 Package ID: l_cproc_p_11.0.066
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1
 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	<u>632</u>	<u>30.9</u>	633	30.9	631	31.0	2	540	36.2	<u>539</u>	<u>36.3</u>	538	36.3
401.bzip2	2	1254	15.4	<u>1256</u>	<u>15.4</u>	1266	15.2	2	<u>1222</u>	<u>15.8</u>	1217	15.9	1246	15.5
403.gcc	2	900	17.9	926	17.4	<u>915</u>	<u>17.6</u>	2	934	17.2	947	17.0	<u>934</u>	<u>17.2</u>
429.mcf	2	684	26.7	<u>684</u>	<u>26.7</u>	695	26.2	2	<u>718</u>	<u>25.4</u>	723	25.2	702	26.0
445.gobmk	2	696	30.1	<u>695</u>	<u>30.2</u>	695	30.2	2	656	32.0	658	31.9	<u>656</u>	<u>32.0</u>
456.hammer	2	<u>622</u>	<u>30.0</u>	623	30.0	621	30.1	2	526	35.4	530	35.2	<u>527</u>	<u>35.4</u>
458.sjeng	2	778	31.1	<u>781</u>	<u>31.0</u>	783	30.9	2	755	32.1	<u>754</u>	<u>32.1</u>	753	32.1
462.libquantum	2	551	75.3	551	75.2	<u>551</u>	<u>75.2</u>	1	340	60.9	<u>340</u>	<u>60.9</u>	340	60.9
464.h264ref	2	901	49.1	<u>901</u>	<u>49.1</u>	904	49.0	2	<u>871</u>	<u>50.8</u>	873	50.7	871	50.8
471.omnetpp	2	<u>742</u>	<u>16.9</u>	742	16.9	743	16.8	2	<u>691</u>	<u>18.1</u>	691	18.1	691	18.1
473.astar	2	956	14.7	<u>959</u>	<u>14.6</u>	960	14.6	2	<u>879</u>	<u>16.0</u>	878	16.0	879	16.0
483.xalanbmk	2	<u>433</u>	<u>31.9</u>	433	31.9	434	31.8	2	<u>433</u>	<u>31.9</u>	433	31.9	434	31.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

General Notes

numactl was used to bind processes to cores
OMP_NUM_THREADS set to number of processors
KMP_AFFINITY set to "physical,0"
KMP_STACKSIZE set to 64M

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalanbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

-xSSSE3 -ipo -O3 -no-prec-div -static -inline-alloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSSE3 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/066/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/066/bin/intel64/icc

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

401.bzip2: -DSPEC_CPU_LP64

456.hmmer: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LINUX

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -ansi-alias -opt-prefetch

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Peak Optimization Flags (Continued)

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -opt-prefetch -ansi-alias

403.gcc: -xSSSE3 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3

429.mcf: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -xSSSE3 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -unroll4

462.libquantum: -xSSSE3 -ipo -O3 -no-prec-div -static
-opt-malloc-options=3 -parallel -par-runtime-control
-opt-prefetch

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmarheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revF.html>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090710.00.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revF.xml>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090710.00.xml>

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For other inquiries, please contact webmaster@spec.org.

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