SPEC® CINT2006 Result

GIGA-BYTE Technology Co. Ltd.
(Test Sponsor: Intel Corporation)

Gigabyte MA78GM-S2H Motherboard (AMD Athlon X2 7550)

$SPECint®\_rate2006 = 24.8$

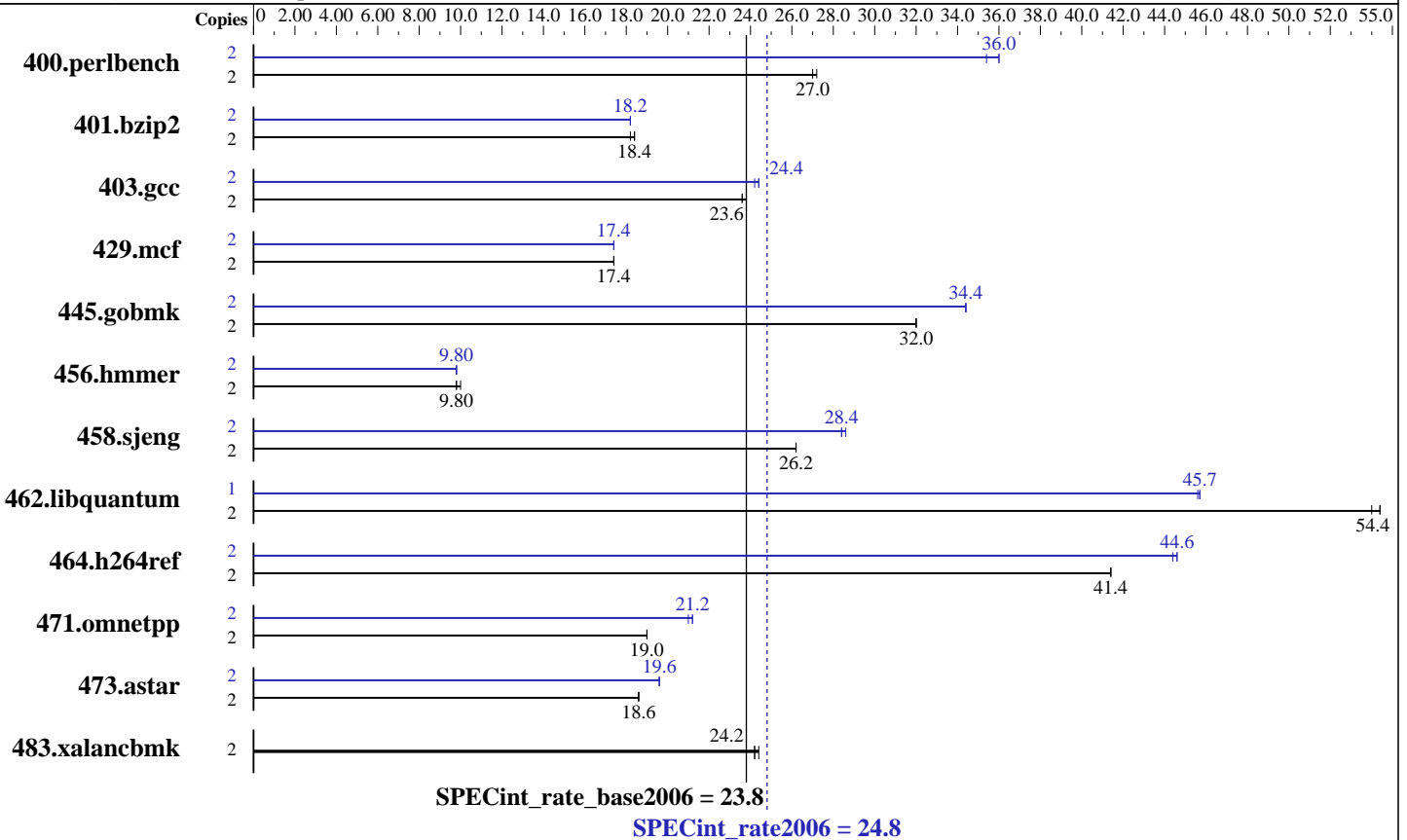$SPECint\_rate\_base2006 = 23.8$

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: May-2009
Hardware Availability: Dec-2008
Software Availability: Nov-2008

Copies 0 2.00 4.00 6.00 8.00 10.0 12.0 14.0 16.0 18.0 20.0 22.0 24.0 26.0 28.0 30.0 32.0 34.0 36.0 38.0 40.0 42.0 44.0 46.0 48.0 50.0 52.0 55.0

Benchmark	Copies	Value
400.perlbench	2	36.0
400.perlbench	2	27.0
401.bzip2	2	18.2
401.bzip2	2	18.4
403.gcc	2	24.4
403.gcc	2	23.6
429.mcf	2	17.4
429.mcf	2	17.4
445.gobmk	2	34.4
445.gobmk	2	32.0
456.hmmer	2	9.80
456.hmmer	2	9.80
458.sjeng	2	28.4
458.sjeng	2	26.2
462.libquantum	1	45.7
462.libquantum	2	54.4
464.h264ref	2	44.6
464.h264ref	2	41.4
471.omnetpp	2	21.2
471.omnetpp	2	19.0
473.astar	2	19.6
473.astar	2	18.6
483.xalancbmk	2	24.2

SPECint_rate_base2006 = 23.8

SPECint_rate2006 = 24.8

Hardware

CPU Name:	AMD Athlon X2 7550
CPU Characteristics:	
CPU MHz:	2500
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	64 KB I + 64 KB D on chip per core
Secondary Cache:	512 KB I+D on chip per core
L3 Cache:	2 MB I+D on chip per chip
Other Cache:	None
Memory:	4 GB (4x1GB DDR2-800 CL5)
Disk Subsystem:	Seagate 320 GB SATA, 7200RPM
Other Hardware:	None

Software

Operating System:	Windows Vista Ultimate w/ SP1 (64-bit)
Compiler:	Intel C++ Compiler Professional 11.0 for IA32 Build 20080930 Package ID: w_cproc_p_11.0.054 Microsoft Visual Studio 2008 (for libraries)
Auto Parallel:	Yes
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	SmartHeap Library Version 8.1 from http://www.microquill.com/

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Results Table

Benchmark	Copies	Base Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Peak Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	**724**	**27.0**	721	27.2	725	27.0	2	551	35.4	542	36.0	**544**	**36.0**
401.bzip2	2	1046	18.4	**1050**	**18.4**	1058	18.2	2	**1060**	**18.2**	1060	18.2	1061	18.2
403.gcc	2	**682**	**23.6**	682	23.6	677	23.8	2	657	24.4	**661**	**24.4**	665	24.2
429.mcf	2	**1048**	**17.4**	1049	17.4	1048	17.4	2	1043	17.4	**1047**	**17.4**	1048	17.4
445.gobmk	2	**656**	**32.0**	656	32.0	657	32.0	2	609	34.4	609	34.4	**609**	**34.4**
456.hmmer	2	1889	9.80	**1889**	**9.80**	1883	10.0	2	**1914**	**9.80**	1910	9.80	1915	9.80
458.sjeng	2	**924**	**26.2**	924	26.2	923	26.2	2	**850**	**28.4**	849	28.6	850	28.4
462.libquantum	2	**763**	**54.4**	761	54.4	767	54.0	1	**454**	**45.7**	454	45.6	453	45.7
464.h264ref	2	1067	41.4	**1068**	**41.4**	1068	41.4	2	995	44.4	994	44.6	**994**	**44.6**
471.omnetpp	2	657	19.0	657	19.0	**657**	**19.0**	2	593	21.0	**592**	**21.2**	592	21.2
473.astar	2	**755**	**18.6**	756	18.6	755	18.6	2	719	19.6	**719**	**19.6**	720	19.6
483.xalancbmk	2	570	24.2	**568**	**24.2**	568	24.4	2	570	24.2	**568**	**24.2**	568	24.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

General Notes

Tested systems can be used with Shin-G ATX case, Antec NeoPower 480W power supply
Binaries were built on Windows Vista Ultimate (32-bit)
OMP_NUM_THREADS set to number of logical processors as seen by the OS
KMP_AFFINITY set to physical,0
submit disabled for 462.libquantum peak

Base Compiler Invocation

C benchmarks:
    icl -Qvc9 -Qc99

C++ benchmarks:
    icl -Qvc9

Base Portability Flags

        403.gcc: -DSPEC_CPU_WIN32
        464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

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Base Portability Flags (Continued)

483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

Base Optimization Flags

C benchmarks:
`/arch:SSE2 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000`

C++ benchmarks:
`/arch:SSE2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features`
`/F512000000 shlw32m.lib        -link /FORCE:MULTIPLE`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks:
`icl -Qvc9 -Qc99`

C++ benchmarks:
`icl -Qvc9`

Peak Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`
483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

Peak Optimization Flags

C benchmarks:

400.perlbench: `/arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)`
`-Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch`
`/F512000000 shlw32m.lib        -link /FORCE:MULTIPLE`

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Peak Optimization Flags (Continued)

401.bzip2: `/arch:SSE2`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2)
`-Qipo -O3 -Qprec-div- -Qopt-prefetch -Qansi-alias`
`/F512000000`

403.gcc: `/arch:SSE2`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2)
`-Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000`

429.mcf: `/arch:SSE2 -Qipo -O3 -Qprec-div- -Qopt-prefetch`
`/F512000000`

445.gobmk: `/arch:SSE2 -Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo`
`-O2 -Qprec-div- -Qansi-alias /F512000000`

456.hmmer: `/arch:SSE2 -Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo`
`-O3 -Qprec-div- -Qunroll2 -Qansi-alias /F512000000`

458.sjeng: `/arch:SSE2`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2)
`-Qipo -O3 -Qprec-div- -Qunroll4 /F512000000`

462.libquantum: `/arch:SSE2 -Qipo -O3 -Qprec-div- -Qopt-prefetch`
`-Qparallel -Qpar-runtime-control -Qvec-guard-write`
`/F512000000`

464.h264ref: `Same as 456.hmmer`

C++ benchmarks:

471.omnetpp: `/arch:SSE2`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2)
`-Qipo -O3 -Qprec-div- -Qansi-alias`
`-Qopt-ra-region-strategy=block /F512000000 shlw32m.lib`
`-link /FORCE:MULTIPLE`

473.astar: `/arch:SSE2`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2)
`-Qipo -O3 -Qprec-div- -Qansi-alias`
`-Qopt-ra-region-strategy=routine /F512000000 shlw32m.lib`
`-link /FORCE:MULTIPLE`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

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Test sponsor:	Intel Corporation	**Hardware Availability:**	Dec-2008
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The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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