



# SPEC<sup>®</sup> CINT2006 Result

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## Fujitsu

PRIMERGY RX100 S5, Intel Core 2 Duo E7400, 2.80 GHz

SPECint<sup>®</sup>\_rate2006 = 40.0

SPECint\_rate\_base2006 = 37.3

CPU2006 license: 19

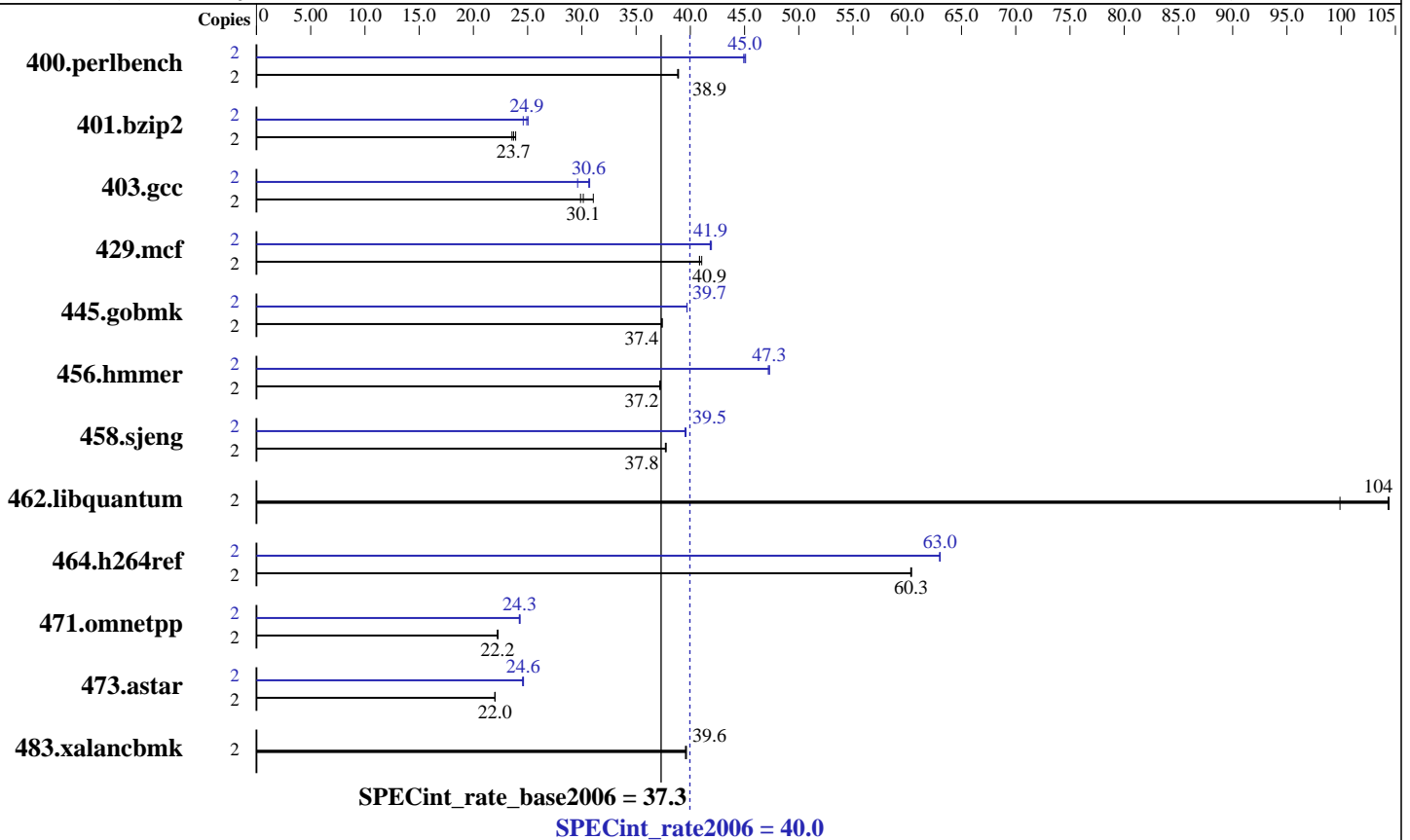
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Apr-2009

Hardware Availability: Apr-2009

Software Availability: Nov-2008



### Hardware

CPU Name: Intel Core 2 Duo E7400  
 CPU Characteristics: 1067 MHz system bus  
 CPU MHz: 2800  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 3 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 8 GB (4x2 GB PC2-6400E, 2 rank, CL6-6-6, ECC)  
 Disk Subsystem: 1x SATA, 250 GB, 7200 rpm  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP2, Kernel 2.6.16.60-0.21-smp  
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20080930 Package ID: l\_cproc\_p\_11.0.066  
 Auto Parallel: No  
 File System: ext3  
 System State: Multi-User Run Level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	503	38.8	502	38.9	<u>502</u>	<u>38.9</u>	2	435	44.9	433	45.1	<u>434</u>	<u>45.0</u>
401.bzip2	2	808	23.9	<u>814</u>	<u>23.7</u>	819	23.6	2	771	25.0	785	24.6	<u>774</u>	<u>24.9</u>
403.gcc	2	<u>534</u>	<u>30.1</u>	539	29.9	518	31.1	2	524	30.7	<u>526</u>	<u>30.6</u>	544	29.6
429.mcf	2	<u>446</u>	<u>40.9</u>	447	40.8	444	41.1	2	435	41.9	<u>436</u>	<u>41.9</u>	436	41.8
445.gobmk	2	561	37.4	561	37.4	<u>561</u>	<u>37.4</u>	2	528	39.7	<u>529</u>	<u>39.7</u>	529	39.7
456.hammer	2	501	37.2	<u>502</u>	<u>37.2</u>	502	37.2	2	<u>395</u>	<u>47.3</u>	395	47.2	395	47.3
458.sjeng	2	641	37.8	<u>641</u>	<u>37.8</u>	642	37.7	2	612	39.5	<u>612</u>	<u>39.5</u>	611	39.6
462.libquantum	2	397	104	415	99.9	<u>397</u>	<u>104</u>	2	397	104	415	99.9	<u>397</u>	<u>104</u>
464.h264ref	2	733	60.4	734	60.3	<u>733</u>	<u>60.3</u>	2	702	63.0	<u>703</u>	<u>63.0</u>	703	63.0
471.omnetpp	2	<u>562</u>	<u>22.2</u>	563	22.2	561	22.3	2	516	24.2	514	24.3	<u>515</u>	<u>24.3</u>
473.astar	2	638	22.0	639	22.0	<u>638</u>	<u>22.0</u>	2	572	24.5	<u>571</u>	<u>24.6</u>	570	24.6
483.xalancbmk	2	348	39.6	349	39.6	<u>349</u>	<u>39.6</u>	2	348	39.6	349	39.6	<u>349</u>	<u>39.6</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
taskset has been used to bind processes to cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Platform Notes

BIOS configuration:  
Adjacent Sector Prefetch = Disable

## General Notes

For information about Fujitsu please visit:  
<http://www.fujitsu.com>

## Base Compiler Invocation

C benchmarks:  
icc

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## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-alloc  
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:  
-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc

401.bzip2: /opt/intel/Compiler/11.0/066/bin/intel64/icc  
456.hmmer: /opt/intel/Compiler/11.0/066/bin/intel64/icc

C++ benchmarks:  
icpc



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## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalanbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -ansi-alias -opt-prefetch  
401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -opt-prefetch -ansi-alias  
403.gcc: -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-alloc  
-opt-malloc-options=3  
429.mcf: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -opt-prefetch  
445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo  
-no-prec-div -ansi-alias  
456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias  
458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -unroll4  
462.libquantum: basepeak = yes  
464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap  
473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap  
483.xalanbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.04.html>  
<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090710.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.04.xml>  
<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090710.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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