



SPEC® CINT2006 Result

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ACTION S.A.

SPECint®_rate2006 = 43.0

ACTINA SOLAR 210 X2 (Intel Xeon E5405, 2.0 GHz)

SPECint_rate_base2006 = 39.5

CPU2006 license: 9008

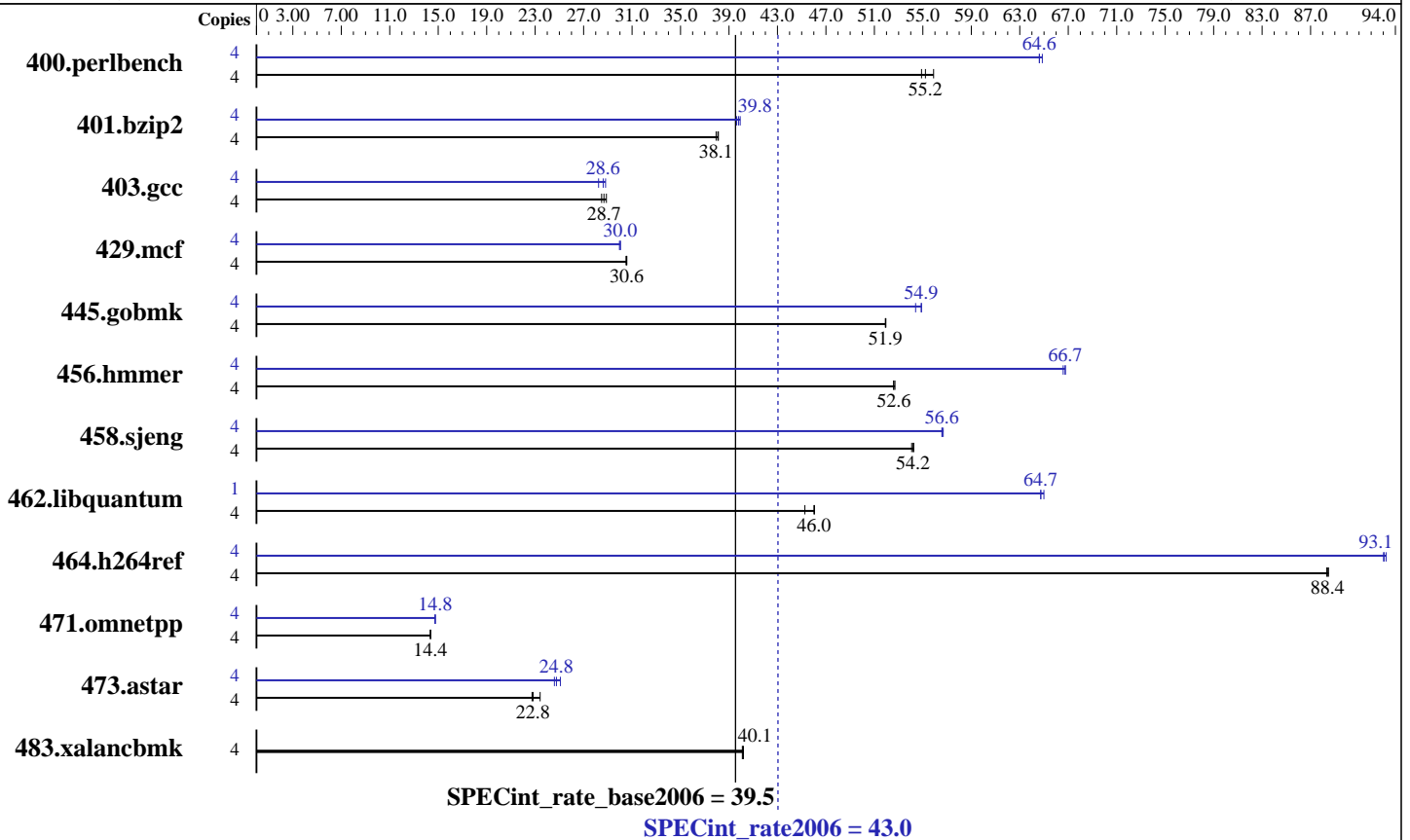
Test date: Mar-2009

Test sponsor: ACTION S.A.

Hardware Availability: Sep-2008

Tested by: ACTION S.A.

Software Availability: Nov-2008



Hardware

CPU Name: Intel Xeon E5405
 CPU Characteristics: 1333 MHz System Bus
 CPU MHz: 2000
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (4x 2 GB, PC2-5300, CL 5-5-5, FB ECC)
 Disk Subsystem: 2x 146 GB SAS, 2.5", 10000 RPM
 Other Hardware: None

Software

Operating System: SuSe Linux Enterprise Server 10 (x86_64) with SP2, kernel 2.6.16.60-0.21-smp
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20080930 Package ID: l_cproc_p_11.0.066
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	708	<u>55.2</u>	712	54.9	699	55.9	4	605	<u>64.6</u>	603	64.9	605	64.6
401.bzip2	4	1018	37.9	1012	38.1	1014	<u>38.1</u>	4	974	39.6	967	39.9	970	<u>39.8</u>
403.gcc	4	1130	28.5	1115	28.9	1123	<u>28.7</u>	4	1125	<u>28.6</u>	1118	28.8	1140	28.2
429.mcf	4	1193	30.6	1194	<u>30.6</u>	1196	30.5	4	1215	30.0	1215	<u>30.0</u>	1218	30.0
445.gobmk	4	808	<u>51.9</u>	808	51.9	808	51.9	4	771	54.4	764	54.9	765	<u>54.9</u>
456.hammer	4	710	52.6	708	52.7	709	<u>52.6</u>	4	559	66.8	561	66.6	559	<u>66.7</u>
458.sjeng	4	893	<u>54.2</u>	895	54.1	892	54.2	4	855	<u>56.6</u>	854	56.7	856	56.6
462.libquantum	4	1831	45.3	1801	<u>46.0</u>	1799	46.1	1	320	64.7	319	65.0	320	<u>64.7</u>
464.h264ref	4	1000	88.5	1001	<u>88.4</u>	1002	88.3	4	951	<u>93.1</u>	949	93.2	952	93.0
471.omnetpp	4	1740	14.4	1740	<u>14.4</u>	1742	14.4	4	1697	14.7	1693	14.8	1695	<u>14.8</u>
473.astar	4	1230	<u>22.8</u>	1199	23.4	1235	22.7	4	1119	25.1	1142	24.6	1134	<u>24.8</u>
483.xalancbmk	4	688	40.1	687	40.2	688	<u>40.1</u>	4	688	40.1	687	40.2	688	<u>40.1</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

General Notes

```
'taskset' was used to bind processes to cores except
for 462.libquantum peak
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of processors
KMP_AFFINITY set to "physical,0"
KMP_STACKSIZE set to 64M
```

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

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Base Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/066/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/066/bin/intel64/icc

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-prefetch -ansi-alias

403.gcc: -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3

429.mcf: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4

462.libquantum: -xSSE4.1 -ipo -O3 -no-prec-div -static
-opt-malloc-options=3 -parallel -par-runtime-control
-opt-prefetch

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmarheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.01.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.01.xml>

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For other inquiries, please contact webmaster@spec.org.

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