



SPEC® CINT2006 Result

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NEC Corporation

SPECint®_rate2006 = 37.0

Express5800/R110a-1H
(Intel Core 2 Duo T9400)

SPECint_rate_base2006 = 34.6

CPU2006 license: 9006

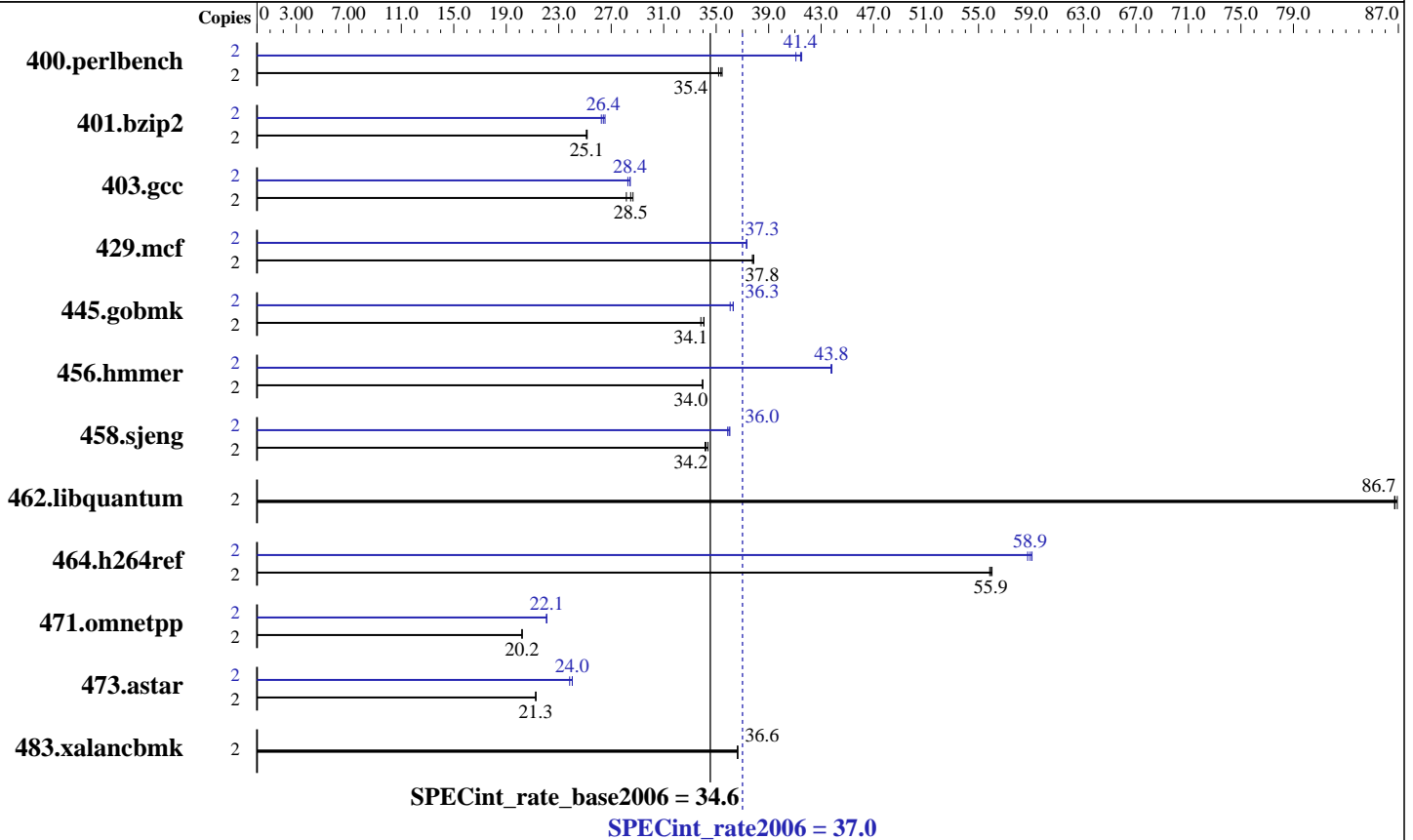
Test date: Feb-2009

Test sponsor: NEC Corporation

Hardware Availability: Jan-2009

Tested by: NEC Corporation

Software Availability: Nov-2008



Hardware

CPU Name: Intel Core 2 Duo T9400
 CPU Characteristics: 1066 MHz system bus
 CPU MHz: 2533
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (4x2 GB PC2-5300P, 1 rank, CL5-5-5, ECC)
 Disk Subsystem: 1x160 GB SATAII, 7200 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20081105 Package ID: l_cproc_p_11.0.074
 Auto Parallel: No
 File System: ext2
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap Library 8.1 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	555	35.2	551	35.5	<u>553</u>	<u>35.4</u>	2	<u>472</u>	<u>41.4</u>	471	41.5	476	41.1
401.bzip2	2	769	25.1	767	25.2	<u>767</u>	<u>25.1</u>	2	735	26.2	728	26.5	<u>731</u>	<u>26.4</u>
403.gcc	2	<u>565</u>	<u>28.5</u>	572	28.2	562	28.7	2	566	28.4	<u>566</u>	<u>28.4</u>	570	28.3
429.mcf	2	483	37.8	<u>482</u>	<u>37.8</u>	482	37.9	2	489	37.3	489	37.3	<u>489</u>	<u>37.3</u>
445.gobmk	2	<u>616</u>	<u>34.1</u>	616	34.1	620	33.8	2	<u>578</u>	<u>36.3</u>	578	36.3	582	36.1
456.hammer	2	549	34.0	549	34.0	<u>549</u>	<u>34.0</u>	2	426	43.8	<u>426</u>	<u>43.8</u>	426	43.8
458.sjeng	2	704	34.4	709	34.2	<u>707</u>	<u>34.2</u>	2	<u>672</u>	<u>36.0</u>	675	35.9	671	36.0
462.libquantum	2	478	86.7	<u>478</u>	<u>86.7</u>	477	86.9	2	478	86.7	<u>478</u>	<u>86.7</u>	477	86.9
464.h264ref	2	790	56.0	792	55.8	<u>791</u>	<u>55.9</u>	2	<u>751</u>	<u>58.9</u>	749	59.1	753	58.7
471.omnetpp	2	619	20.2	<u>619</u>	<u>20.2</u>	619	20.2	2	566	22.1	566	22.1	<u>566</u>	<u>22.1</u>
473.astar	2	660	21.3	<u>661</u>	<u>21.3</u>	661	21.2	2	589	23.8	<u>584</u>	<u>24.0</u>	584	24.0
483.xalancbmk	2	<u>377</u>	<u>36.6</u>	377	36.6	377	36.6	2	<u>377</u>	<u>36.6</u>	377	36.6	377	36.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
taskset was used to bind processes to cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

Bios settings:
Hardware Prefetcher: Enabled
Adjacent Cache Line Prefetch: Enabled

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/opt/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/074/bin/intel64/icc
-L/opt/intel/Compiler/11.0/074/ipp/em64t/lib
-I/opt/intel/Compiler/11.0/074/ipp/em64t/include

456.hmmer: /opt/intel/Compiler/11.0/074/bin/intel64/icc
-L/opt/intel/Compiler/11.0/074/ipp/em64t/lib
-I/opt/intel/Compiler/11.0/074/ipp/em64t/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-prefetch -ansi-alias

403.gcc: -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3

429.mcf: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap

483.xalancbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revE.html>

<http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revE.xml>

<http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revB.xml>

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For other inquiries, please contact webmaster@spec.org.

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