



# SPEC® CINT2006 Result

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## Itautec

## SPECint®\_rate2006 = Not Run

### Servidor Itautec MX201 (Intel Xeon E5440)

## SPECint\_rate\_base2006 = 125

CPU2006 license: 9001

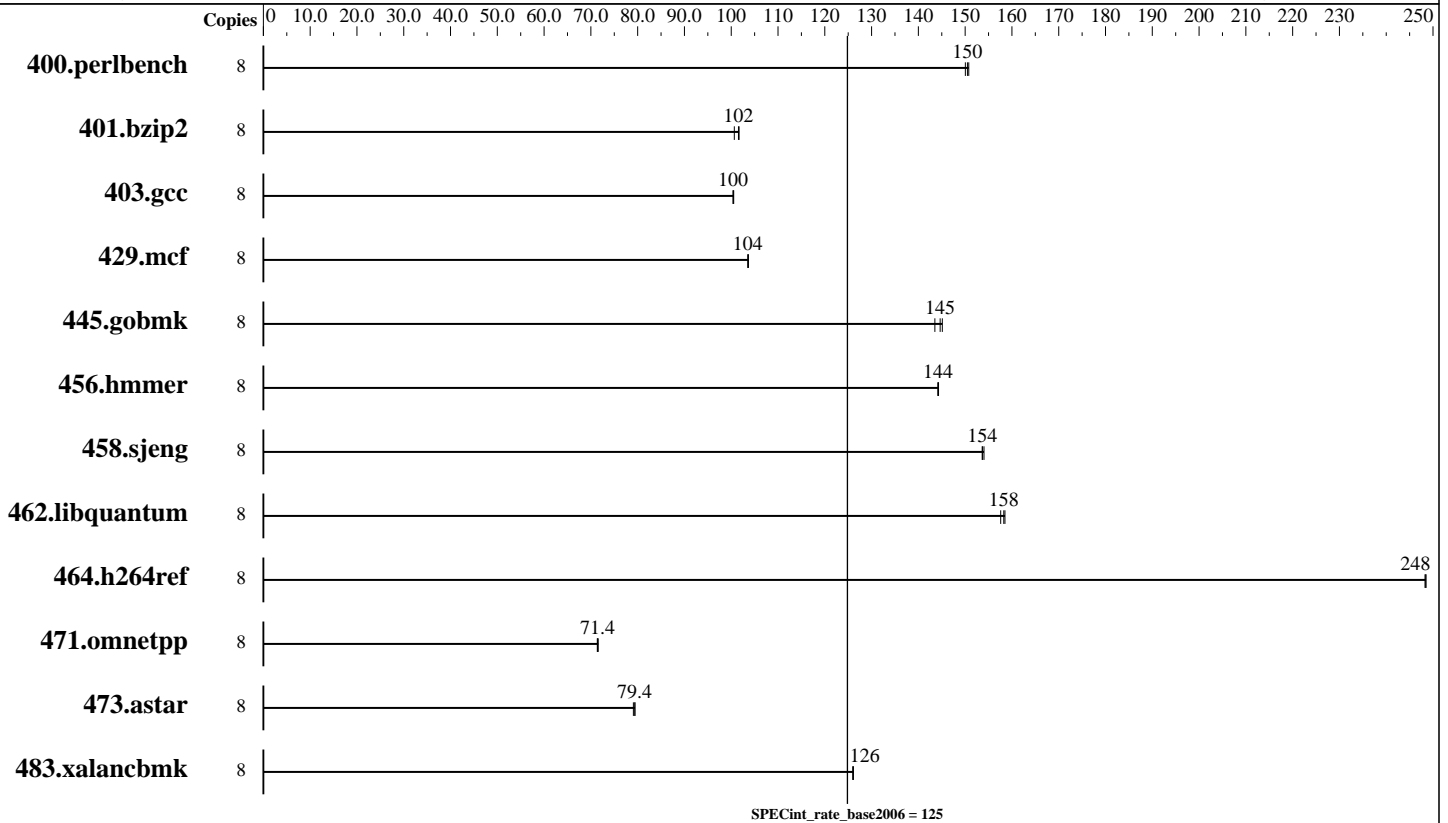
Test sponsor: Itautec

Tested by: Itautec

Test date: Jan-2009

Hardware Availability: Dec-2007

Software Availability: Nov-2008



### Hardware

CPU Name: Intel Xeon E5440  
 CPU Characteristics:  
 CPU MHz: 2830  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (4 \* 4 GB PC2-5300 FBDIMM, CL-5-5-5, ECC)  
 Disk Subsystem: 1 x SAS, 36GB, 15000RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP2, Kernel 2.6.16-60.0.21-smp  
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20081105 Package ID: l\_cproc\_b\_11.0.074  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: Not Applicable  
 Other Software: Binutils 2.18.50.0.7.20080502  
 Microquill SmartHeap V8.1



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	521	150	<u>519</u>	<u>150</u>	518	151							
401.bzip2	8	<u>760</u>	<u>102</u>	767	101	759	102							
403.gcc	8	641	100	641	101	<u>641</u>	<u>100</u>							
429.mcf	8	704	104	<u>704</u>	<u>104</u>	704	104							
445.gobmk	8	585	144	<u>580</u>	<u>145</u>	578	145							
456.hammer	8	518	144	<u>517</u>	<u>144</u>	517	144							
458.sjeng	8	<u>630</u>	<u>154</u>	630	154	628	154							
462.libquantum	8	1052	158	<u>1047</u>	<u>158</u>	1046	159							
464.h264ref	8	713	248	<u>713</u>	<u>248</u>	712	248							
471.omnetpp	8	<u>700</u>	<u>71.4</u>	699	71.6	701	71.4							
473.astar	8	707	79.4	<u>707</u>	<u>79.4</u>	710	79.1							
483.xalancbmk	8	<u>438</u>	<u>126</u>	438	126	438	126							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.  
'/usr/bin/taskset' used to bind benchmark copies to processors.

## Platform Notes

BIOS Settings:  
Hardware Prefetch Disabled

## General Notes

This result was measured on the Servidor Itautec MX221.  
The Servidor Itautec MX201 and the Servidor Itautec MX221 are electronically equivalent.

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3 -opt-prefetch  
C++ benchmarks:  
-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/opt/sh/SmartHeap\_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.html>  
<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.xml>  
<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revE.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.  
Report generated on Tue Jul 22 23:13:24 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 17 February 2009.