



SPEC[®] CINT2006 Result

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Unisys Corporation

Unisys ES7000 Model 7600R, Intel Xeon X7460, 2.66GHz

SPECint[®]_rate2006 = 1050

SPECint_rate_base2006 = 999

CPU2006 license: 15

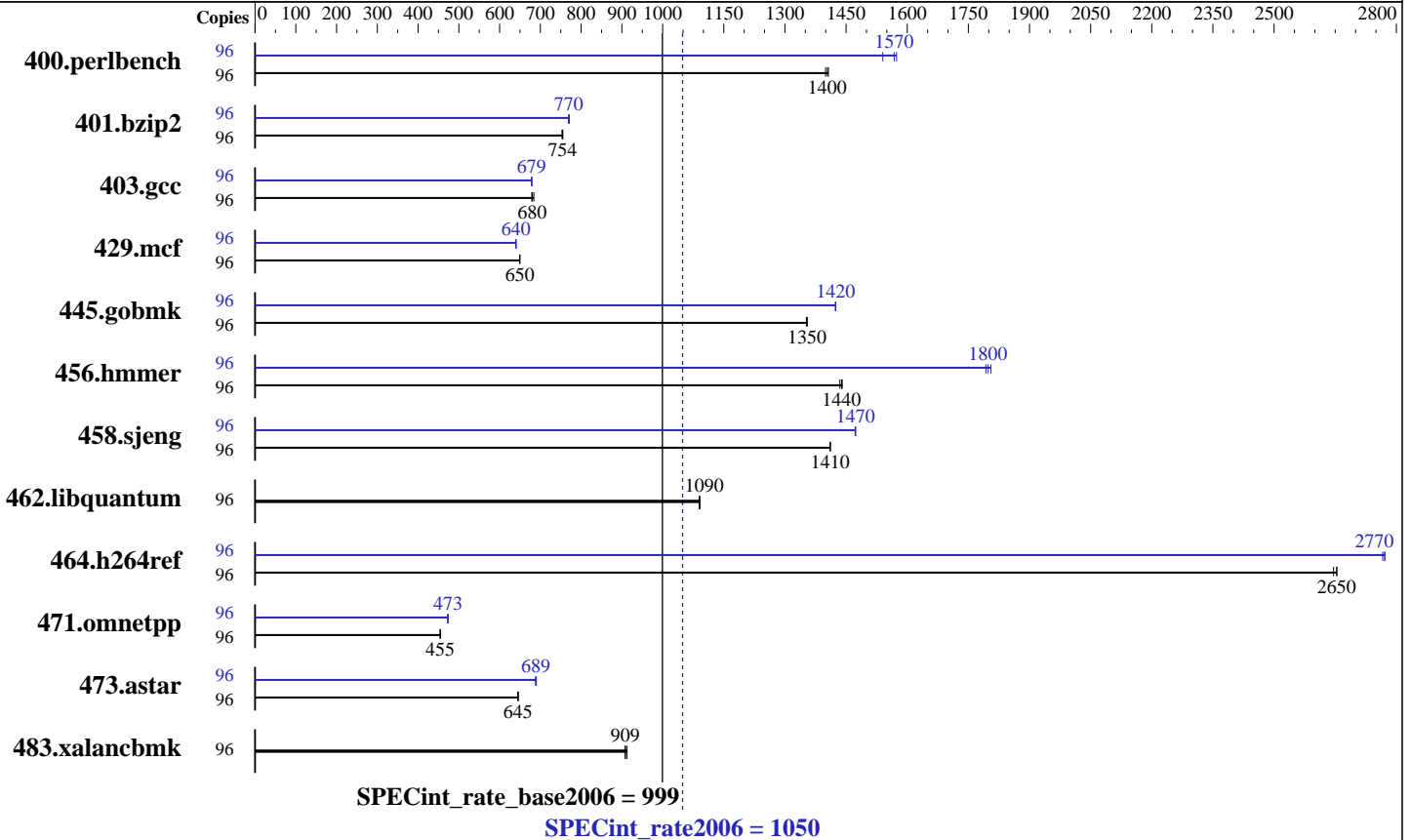
Test sponsor: Unisys Corporation

Tested by: Unisys Corporation

Test date: Dec-2008

Hardware Availability: Nov-2008

Software Availability: Jan-2009



Hardware

CPU Name: Intel Xeon X7460
 CPU Characteristics:
 CPU MHz: 2667
 FPU: Integrated
 CPU(s) enabled: 96 cores, 16 chips, 6 cores/chip
 CPU(s) orderable: 4,8,12,16 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 9 MB I+D on chip per chip, 3 MB shared / 2 cores
 L3 Cache: 16 MB I+D on chip per chip
 Other Cache: None
 Memory: 128 GB (32x4GB,2 rank, PC2-5300F-555-11-E0)
 Disk Subsystem: 2 x 146 GB SAS,15000 RPM
 Other Hardware: None

Software

Operating System: SuSe Linux Enterprise Server 10 (x86_64) SP2(kernel 2.6.16-60.0.21-smp)
 Compiler: Intel C++ Compiler 11.0 for Linux (Build 20080730) Package ID: l_cproc_b_11.0.042
 Auto Parallel: No
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	96	670	1400	668	1400	667	1410	96	609	1540	596	1570	598	1570
401.bzip2	96	1228	754	1227	755	1230	753	96	1204	769	1203	770	1202	771
403.gcc	96	1138	679	1136	680	1130	684	96	1139	678	1139	679	1138	679
429.mcf	96	1349	649	1348	650	1348	650	96	1368	640	1367	640	1368	640
445.gobmk	96	744	1350	743	1350	745	1350	96	707	1420	707	1420	707	1420
456.hammer	96	622	1440	624	1430	622	1440	96	496	1810	500	1790	498	1800
458.sjeng	96	823	1410	823	1410	823	1410	96	789	1470	788	1470	788	1470
462.libquantum	96	1824	1090	1825	1090	1823	1090	96	1824	1090	1825	1090	1823	1090
464.h264ref	96	801	2650	801	2650	803	2650	96	768	2770	766	2770	767	2770
471.omnetpp	96	1320	455	1321	454	1320	455	96	1268	473	1268	473	1268	473
473.astar	96	1045	645	1045	645	1044	646	96	979	688	977	690	978	689
483.xalancbmk	96	730	908	726	913	729	909	96	730	908	726	913	729	909

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
taskset was used to bind processes to cores.

Operating System Notes

'ulimit -s unlimited' allows stack to grow to system limit.
/proc/sys/vm/zone_reclaim_mode=1 to enable zone_reclaim.

Base Compiler Invocation

C benchmarks:
icc
C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX

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Base Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/066/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/066/bin/intel64/icc

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

401.bzip2: -DSPEC_CPU_LP64

456.hmmer: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LINUX

483.xalancbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-prefetch -ansi-alias

403.gcc: -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3

429.mcf: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.01.html>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.12.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.01.xml>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.12.xml>

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