



SPEC® CINT2006 Result

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BOXX Technologies, Inc.

SPECint®_rate2006 = 179

3DBOXX WORKSTATION 8450SE Special Edition

SPECint_rate_base2006 = 164

CPU2006 license: 3314

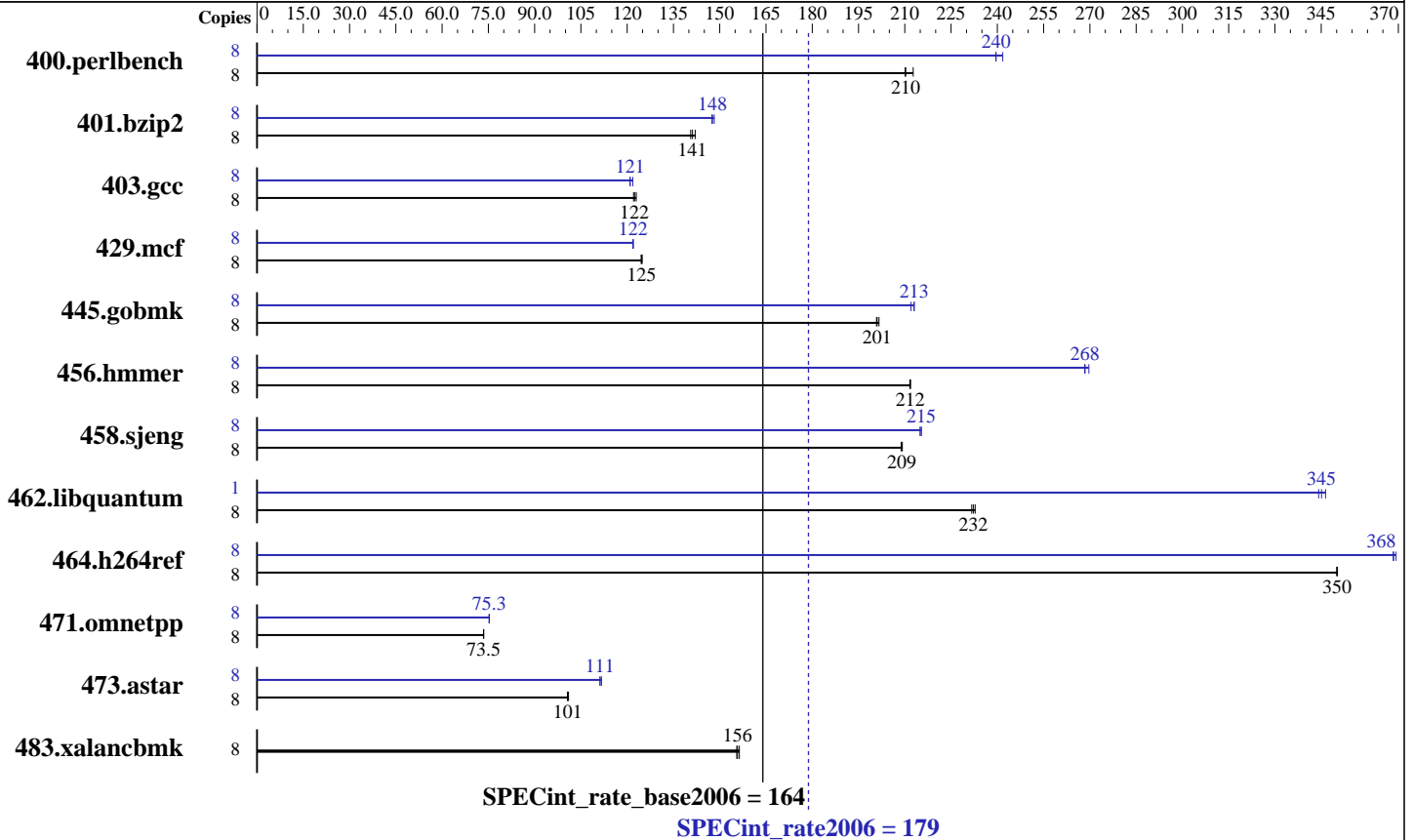
Test date: Nov-2008

Test sponsor: BOXX Technologies, Inc.

Hardware Availability: May-2008

Tested by: BOXX Technologies, Inc.

Software Availability: Nov-2008



Hardware

CPU Name: Intel Core 2 Extreme QX9775
 CPU Characteristics: 1600 MHz Bus speed, CPU frequency set to 4.0GHz
 CPU MHz: 4000
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (4 x 4GB ECC PC2-6400,CL5,FBDIMM)
 Disk Subsystem: 300 GB SATA, 10000RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16-60-0.21-smp
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: I_cproc_b_11.0.042,
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|----------------|--------|------------|------------|------------|------------|------------|-------------|--------|------------|------------|-------------|------------|------------|-------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 8 | 372 | 210 | <u>372</u> | <u>210</u> | 367 | 213 | 8 | <u>326</u> | <u>240</u> | 323 | 242 | 326 | 240 |
| 401.bzip2 | 8 | <u>547</u> | <u>141</u> | 549 | 141 | 543 | 142 | 8 | 524 | 147 | <u>523</u> | <u>148</u> | 521 | 148 |
| 403.gcc | 8 | 524 | 123 | 528 | 122 | <u>526</u> | <u>122</u> | 8 | 529 | 122 | <u>532</u> | <u>121</u> | 532 | 121 |
| 429.mcf | 8 | <u>585</u> | <u>125</u> | 584 | 125 | 586 | 125 | 8 | 599 | 122 | 598 | 122 | <u>599</u> | <u>122</u> |
| 445.gobmk | 8 | 418 | 201 | <u>417</u> | <u>201</u> | 416 | 202 | 8 | 394 | 213 | 396 | 212 | <u>394</u> | <u>213</u> |
| 456.hammer | 8 | 353 | 212 | <u>352</u> | <u>212</u> | 352 | 212 | 8 | 277 | 270 | <u>278</u> | <u>268</u> | 278 | 268 |
| 458.sjeng | 8 | 463 | 209 | 464 | 209 | <u>463</u> | <u>209</u> | 8 | 450 | 215 | 450 | 215 | <u>450</u> | <u>215</u> |
| 462.libquantum | 8 | 715 | 232 | 712 | 233 | <u>714</u> | <u>232</u> | 1 | 59.8 | 346 | <u>60.0</u> | <u>345</u> | 60.2 | 344 |
| 464.h264ref | 8 | <u>506</u> | <u>350</u> | 506 | 350 | 505 | 350 | 8 | 479 | 369 | <u>480</u> | <u>368</u> | 481 | 368 |
| 471.omnetpp | 8 | 680 | 73.5 | 681 | 73.4 | <u>681</u> | <u>73.5</u> | 8 | 664 | 75.3 | 664 | 75.3 | <u>664</u> | <u>75.3</u> |
| 473.astar | 8 | 556 | 101 | <u>557</u> | <u>101</u> | 558 | 101 | 8 | <u>505</u> | <u>111</u> | 505 | 111 | 503 | 112 |
| 483.xalancbmk | 8 | 355 | 156 | 353 | 156 | <u>354</u> | <u>156</u> | 8 | 355 | 156 | 353 | 156 | <u>354</u> | <u>156</u> |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
taskset was used to bind processes to cores except
for 462.libquantum peak

General Notes

OMP_NUM_THREADS set to number of processors
KMP_AFFINITY set to "physical,0"
KMP_STACKSIZE set to 64M

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX

Continued on next page



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Base Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/042/bin/intel64/icc
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include

456.hmmer: /opt/intel/Compiler/11.0/042/bin/intel64/icc
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbenc: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-prefetch -ansi-alias

403.gcc: -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3

429.mcf: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4

462.libquantum: -xSSE4.1 -ipo -O3 -no-prec-div -static
-opt-malloc-options=3 -parallel -par-runtime-control
-opt-prefetch

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmarheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.13.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.13.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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