



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Fujitsu Siemens Computers

## SPECint®\_rate2006 = Not Run

### PRIMERGY RX300 S4, Intel Xeon L5430, 2.67 GHz

## SPECint\_rate\_base2006 = 122

CPU2006 license: 22

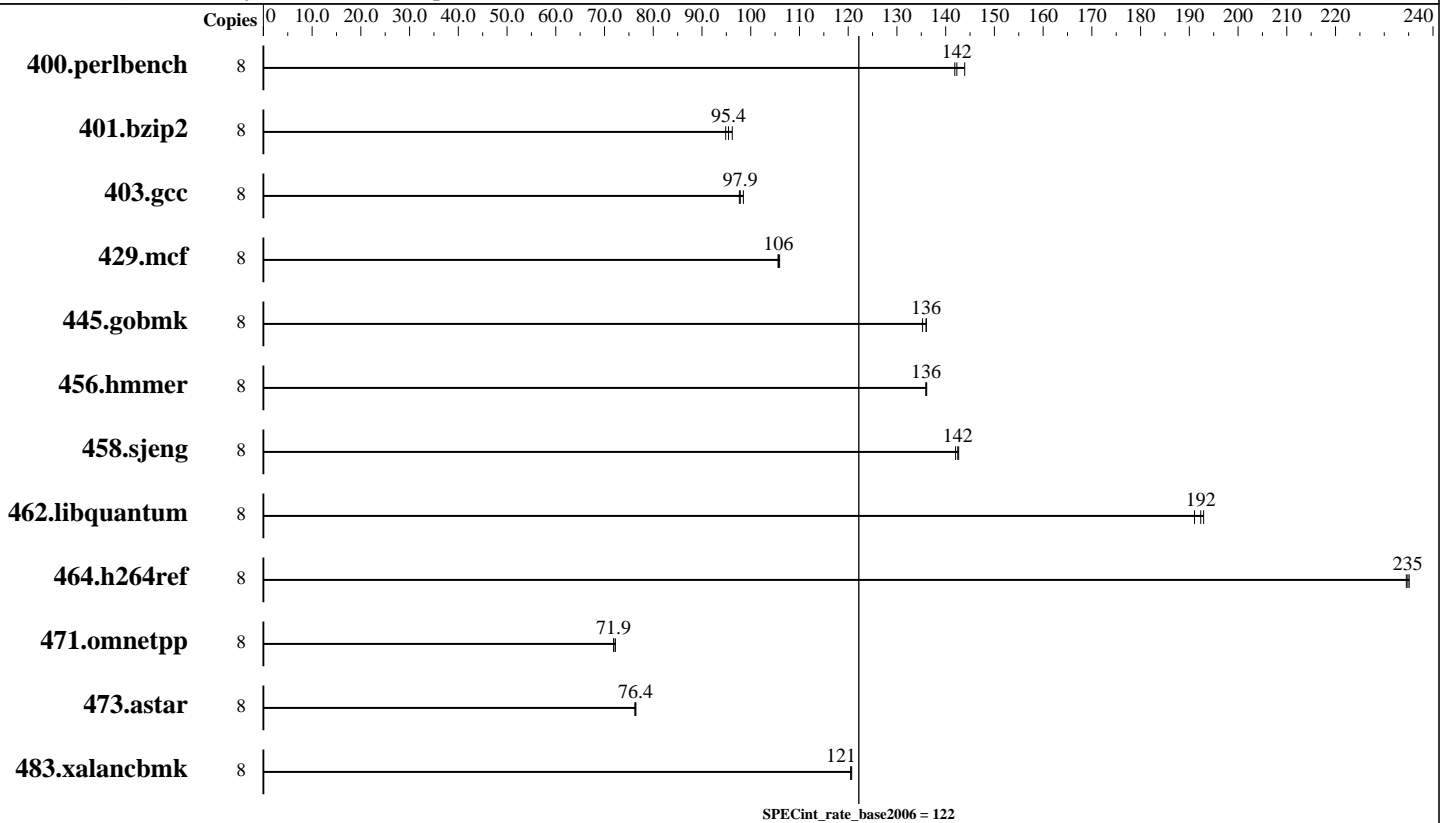
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Nov-2008

Hardware Availability: Oct-2008

Software Availability: Nov-2008



### Hardware

CPU Name: Intel Xeon L5430  
 CPU Characteristics: 1333 MHz system bus  
 CPU MHz: 2667  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8x2 GB PC2-5300F, 2 rank, CL5-5-5, ECC)  
 Disk Subsystem: 1x SAS 70 GB, 10000 rpm  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP2, Kernel 2.6.16.60-0.21-smp  
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l\_cproc\_b\_11.0.042  
 Auto Parallel: No  
 File System: ext3  
 System State: Multi-User Run Level 3  
 Base Pointers: 32-bit  
 Peak Pointers: Not Applicable  
 Other Software: Microquill SmartHeap Library, Version 8.1 Binutils 2.18.50.0.7.20080502



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Fujitsu Siemens Computers

SPECint\_rate2006 = Not Run

PRIMERGY RX300 S4, Intel Xeon L5430, 2.67 GHz

SPECint\_rate\_base2006 = 122

CPU2006 license: 22

Test date: Nov-2008

Test sponsor: Fujitsu Siemens Computers

Hardware Availability: Oct-2008

Tested by: Fujitsu Siemens Computers

Software Availability: Nov-2008

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	551	142	543	144	<u>549</u>	<u>142</u>							
401.bzip2	8	814	94.9	802	96.2	<u>809</u>	<u>95.4</u>							
403.gcc	8	<b>658</b>	<b>97.9</b>	654	98.5	659	97.7							
429.mcf	8	691	106	<b>690</b>	<b>106</b>	689	106							
445.gobmk	8	617	136	620	135	<b>617</b>	<b>136</b>							
456.hammer	8	549	136	<b>549</b>	<b>136</b>	548	136							
458.sjeng	8	678	143	682	142	<b>679</b>	<b>142</b>							
462.libquantum	8	868	191	<b>862</b>	<b>192</b>	859	193							
464.h264ref	8	753	235	755	235	<b>754</b>	<b>235</b>							
471.omnetpp	8	<b>695</b>	<b>71.9</b>	696	71.9	692	72.2							
473.astar	8	737	76.2	735	76.4	<b>735</b>	<b>76.4</b>							
483.xalancbmk	8	457	121	<b>458</b>	<b>121</b>	458	121							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
taskset has been used to bind processes to cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Platform Notes

BIOS configuration:  
Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable  
Memory Throttling = Enable

## General Notes

For information about Fujitsu Siemens Computers please see:  
<http://www.fujitsu-siemens.com>

## Base Compiler Invocation

C benchmarks:  
icc

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu Siemens Computers

SPECint\_rate2006 = Not Run

PRIMERGY RX300 S4, Intel Xeon L5430, 2.67 GHz

SPECint\_rate\_base2006 = 122

CPU2006 license: 22

Test date: Nov-2008

Test sponsor: Fujitsu Siemens Computers

Hardware Availability: Oct-2008

Tested by: Fujitsu Siemens Computers

Software Availability: Nov-2008

## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-alloc  
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:  
-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.16.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.16.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.  
Report generated on Tue Jul 22 22:03:38 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 26 November 2008.