SPEC® CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge 1950 III (Intel Xeon E5405, 2.00 GHz)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 =   103
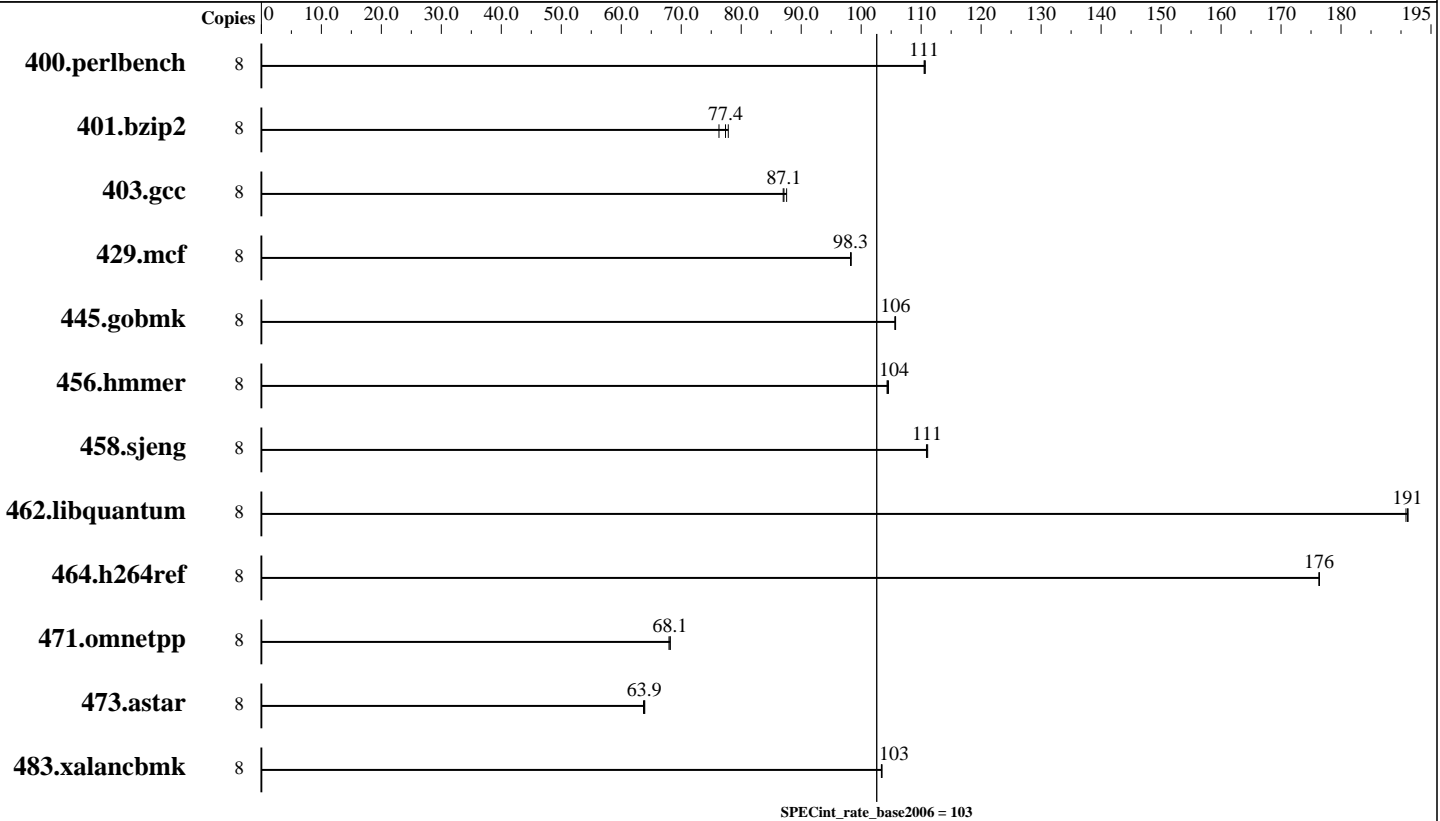
CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Oct-2008
Hardware Availability: Sep-2008
Software Availability: Nov-2008

Copies	Benchmark	Result
8	400.perlbench	111
8	401.bzip2	77.4
8	403.gcc	87.1
8	429.mcf	98.3
8	445.gobmk	106
8	456.hmmer	104
8	458.sjeng	111
8	462.libquantum	191
8	464.h264ref	176
8	471.omnetpp	68.1
8	473.astar	63.9
8	483.xalancbmk	103

SPECint_rate_base2006 = 103

Hardware

CPU Name: Intel Xeon E5405
CPU Characteristics:
CPU MHz: 2000
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache: None
Other Cache: None
Memory: 16 GB (8 x 2 GB DDR2-667 FB-DIMM)
Disk Subsystem: 1 x 73 GB 10000 RPM SAS
Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16-60.0.21-smp
Compiler: Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.042
Auto Parallel: No
File System: ReiserFS
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: Not Applicable
Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge 1950 III (Intel Xeon E5405, 2.00 GHz)

SPECint_rate2006 =   Not Run

SPECint_rate_base2006 =   103

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Oct-2008
Hardware Availability: Sep-2008
Software Availability: Nov-2008

Results Table

Benchmark	Copies	Base Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Peak Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	707	111	706	111	**707**	**111**							
401.bzip2	8	1012	76.3	**998**	**77.4**	992	77.8							
403.gcc	8	**739**	**87.1**	735	87.6	740	87.0							
429.mcf	8	**742**	**98.3**	743	98.2	742	98.3							
445.gobmk	8	795	106	794	106	**794**	**106**							
456.hmmer	8	715	104	714	105	**715**	**104**							
458.sjeng	8	**872**	**111**	873	111	871	111							
462.libquantum	8	869	191	**867**	**191**	867	191							
464.h264ref	8	1004	176	**1004**	**176**	1004	176							
471.omnetpp	8	736	68.0	**734**	**68.1**	734	68.2							
473.astar	8	879	63.9	882	63.7	**879**	**63.9**							
483.xalancbmk	8	533	103	534	103	**534**	**103**							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run

Platform Notes

BIOS Settings:
Hardware Prefetcher = Disabled (Default = Enabled)
Adjacent Cache Line Prefetch = Disabled (Default = Enabled)

General Notes

taskset was used to bind processes to cores except
for 462.libquantum peak

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge 1950 III (Intel Xeon E5405, 2.00 GHz)

SPECint_rate2006 =   Not Run

SPECint_rate_base2006 =   103

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Oct-2008
Hardware Availability: Sep-2008
Software Availability: Nov-2008

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch
```

C++ benchmarks:
```
-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.html
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.02.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.xml
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.02.xml

SPEC and SPECint are registered trademarks of the Standard Performance
Evaluation Corporation. All other brand and product names appearing in
this result are trademarks or registered trademarks of their respective
holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Tue Jul 22 20:24:30 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 11 November 2008.