



SPEC® CINT2006 Result

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Acer Incorporated

SPECint®_rate2006 = 92.6

Acer Altos R920 (Intel Xeon E7420)

SPECint_rate_base2006 = 88.5

CPU2006 license: 97

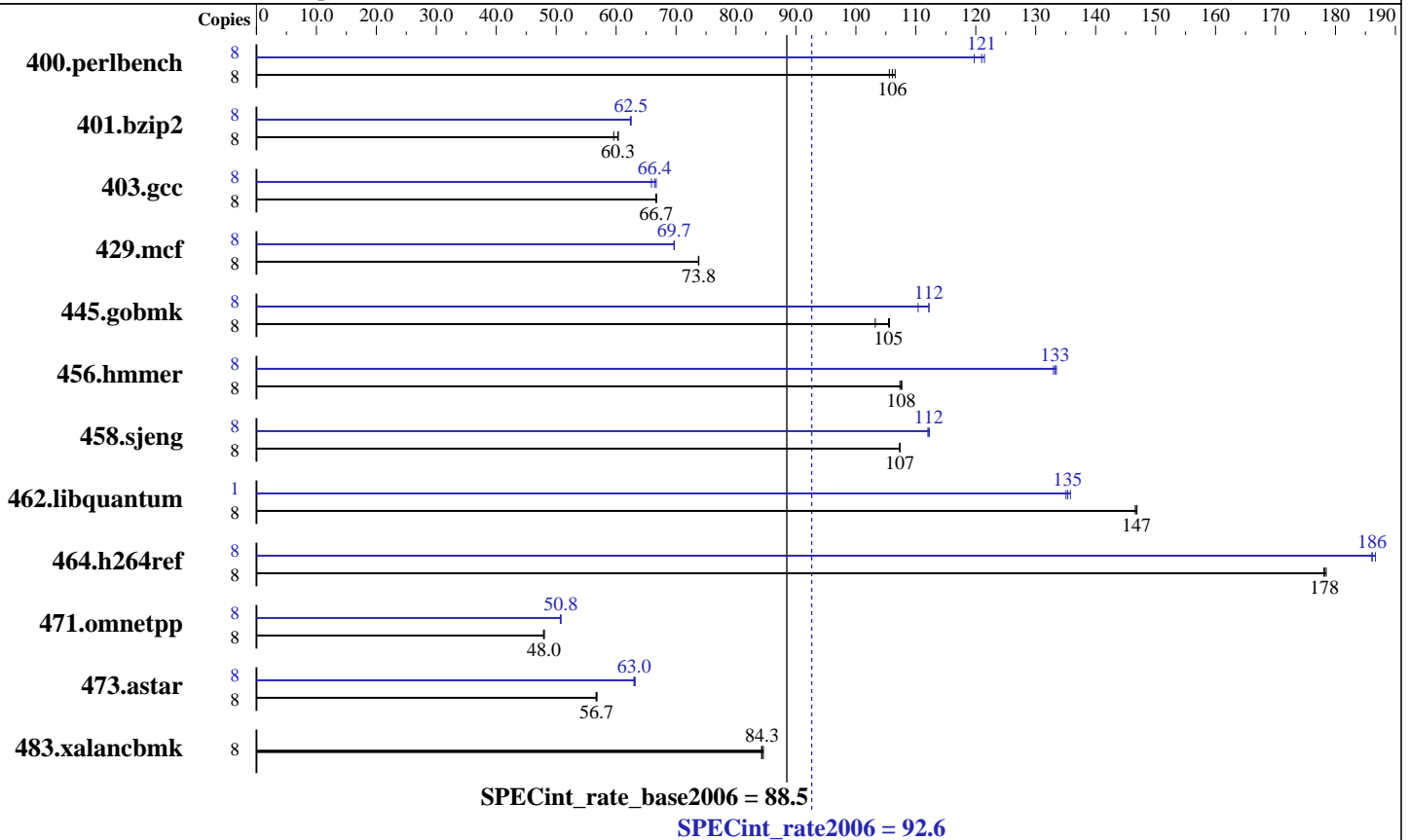
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Oct-2008

Hardware Availability: Sep-2008

Software Availability: Nov-2008



Hardware

CPU Name: Intel Xeon E7420
 CPU Characteristics: 1066MHz system bus
 CPU MHz: 2133
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1, 2, 4 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip, 3 MB shared / 2 cores
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 32 GB (16 x 2048MB ECC FB-DIMM DDR2-667 CL5)
 Disk Subsystem: 1 x 73GB 10000RPM SAS HDD
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.042
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Multi-user, level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	<u>737</u>	<u>106</u>	734	107	740	106	8	653	120	<u>646</u>	<u>121</u>	644	121
401.bzip2	8	1295	59.6	1279	60.4	<u>1280</u>	<u>60.3</u>	8	1235	62.5	1238	62.4	<u>1236</u>	<u>62.5</u>
403.gcc	8	<u>966</u>	<u>66.7</u>	965	66.7	966	66.7	8	977	65.9	<u>969</u>	<u>66.4</u>	966	66.7
429.mcf	8	989	73.8	990	73.7	<u>989</u>	<u>73.8</u>	8	1047	69.7	<u>1047</u>	<u>69.7</u>	1046	69.7
445.gobmk	8	795	106	813	103	<u>796</u>	<u>105</u>	8	760	110	<u>748</u>	<u>112</u>	748	112
456.hammer	8	695	107	<u>694</u>	<u>108</u>	693	108	8	<u>560</u>	<u>133</u>	559	133	561	133
458.sjeng	8	902	107	<u>902</u>	<u>107</u>	902	107	8	864	112	862	112	<u>863</u>	<u>112</u>
462.libquantum	8	1131	147	1128	147	<u>1129</u>	<u>147</u>	1	153	135	<u>153</u>	<u>135</u>	153	136
464.h264ref	8	<u>994</u>	<u>178</u>	992	178	994	178	8	948	187	<u>951</u>	<u>186</u>	951	186
471.omnetpp	8	1042	48.0	<u>1042</u>	<u>48.0</u>	1044	47.9	8	985	50.8	<u>985</u>	<u>50.8</u>	986	50.7
473.astar	8	<u>990</u>	<u>56.7</u>	989	56.8	991	56.7	8	889	63.2	892	63.0	<u>891</u>	<u>63.0</u>
483.xalancbmk	8	655	84.3	653	84.6	<u>655</u>	<u>84.3</u>	8	655	84.3	653	84.6	<u>655</u>	<u>84.3</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
Taskset was used to bind processes to cores except for
462.libquantum in peak.

Platform Notes

BIOS Settings
Adjacent Cache Line Prefetch set to OFF
Hardware Prefetch set to OFF
High Bandwidth Option set to OFF

General Notes

OMP_NUM_THREADS set to number of processors
KMP_AFFINITY set to "physical,0"
KMP_STACKSIZE set to 64M
"ulimit -s unlimited" was set for stacksize unlimited

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/042/bin/intel64/icc
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include

456.hmmer: /opt/intel/Compiler/11.0/042/bin/intel64/icc
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-prefetch -ansi-alias

403.gcc: -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3

429.mcf: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4

462.libquantum: -xSSE4.1 -ipo -O3 -no-prec-div -static
-opt-malloc-options=3 -parallel -par-runtime-control
-opt-prefetch

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

483.xalancbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.13.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.13.xml>

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For other inquiries, please contact webmaster@spec.org.

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