SPEC® CINT2006 Result

IBM Corporation

SPECint®_rate2006 =   Not Run

IBM System x3850 M2 (Intel Xeon E7330)

SPECint_rate_base2006 = 75.8

CPU2006 license: 11
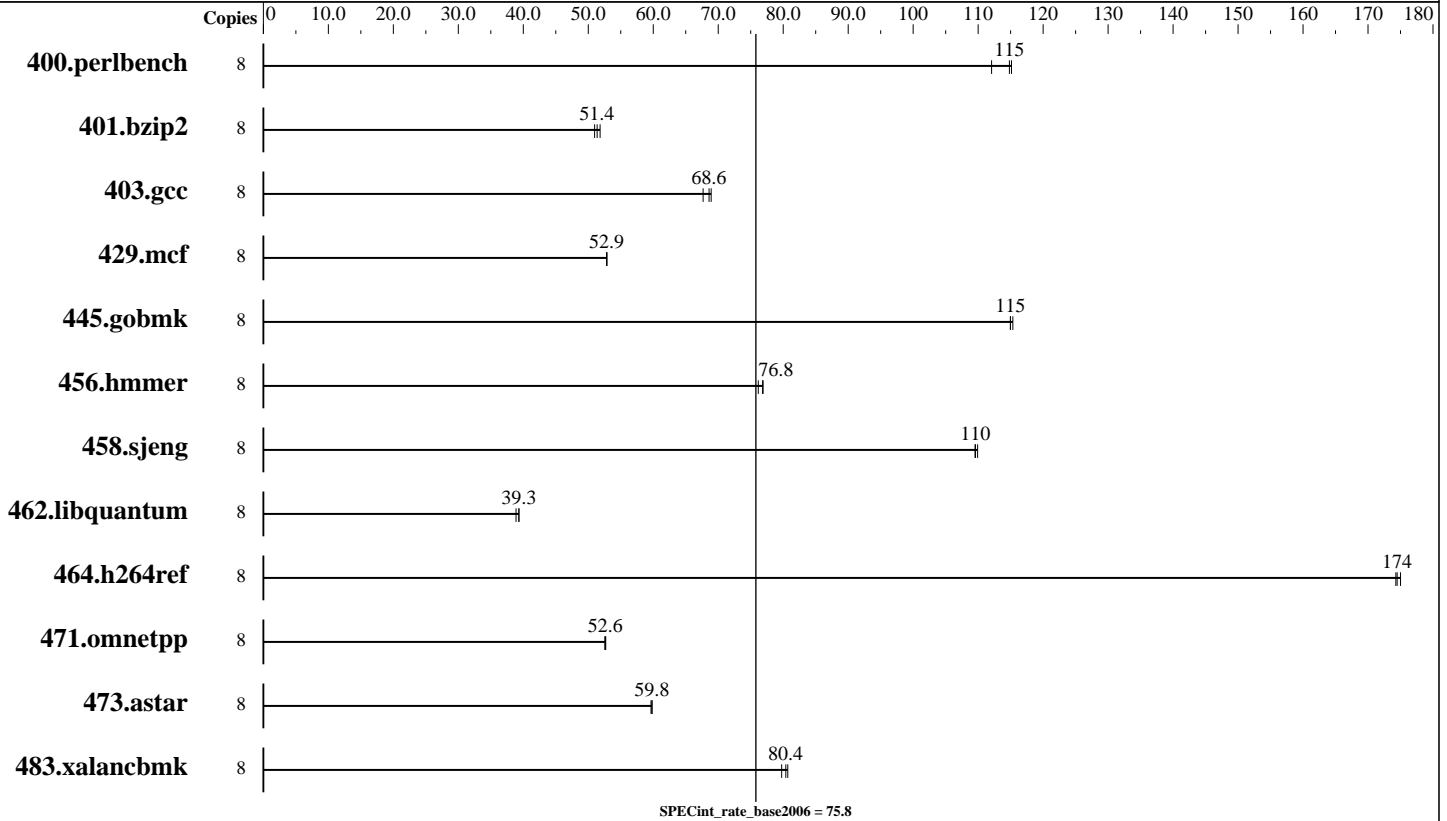**Test sponsor:** IBM Corporation
Tested by: IBM Corporation

Test date: Sep-2008
Hardware Availability: Nov-2007
Software Availability: May-2008

Benchmark	Copies	Result
400.perlbench	8	115
401.bzip2	8	51.4
403.gcc	8	68.6
429.mcf	8	52.9
445.gobmk	8	115
456.hmmer	8	76.8
458.sjeng	8	110
462.libquantum	8	39.3
464.h264ref	8	174
471.omnetpp	8	52.6
473.astar	8	59.8
483.xalancbmk	8	80.4

SPECint_rate_base2006 = 75.8

Hardware

CPU Name: Intel Xeon E7330
CPU Characteristics: Quad Core, 2.4GHz
CPU MHz: 2400
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1,2,4 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 6 MB I+D on chip per chip, 3 MB shared / 2 cores
L3 Cache: None
Other Cache: None
Memory: 64 GB (16 x 4 GB DDR@-5300 ECC)
Disk Subsystem: 1 x 73 GB SAS, 15000 RPM
Other Hardware: None

Software

Operating System: SuSE Linux Enterprise Sever 10 SP2 (x86_64), Kernel 2.6.16.60-0.21-smp
Compiler: Intel C++ Compiler 10.1 for Linux Build 20080312 Package ID: l_cc_p_10.1.015
Auto Parallel: No
File System: ReiserFS
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: Not Applicable
Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	Base Copies	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Peak Copies	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio
400.perlbench	8	697	112	**681**	**115**	679	115							
401.bzip2	8	1515	51.0	1490	51.8	**1503**	**51.4**							
403.gcc	8	935	68.9	952	67.7	**939**	**68.6**							
429.mcf	8	**1380**	**52.9**	1381	52.8	1380	52.9							
445.gobmk	8	728	115	730	115	**730**	**115**							
456.hmmer	8	971	76.9	980	76.2	**971**	**76.8**							
458.sjeng	8	881	110	884	110	**883**	**110**							
462.libquantum	8	4263	38.9	4215	39.3	**4215**	**39.3**							
464.h264ref	8	1012	175	**1015**	**174**	1016	174							
471.omnetpp	8	949	52.7	**950**	**52.6**	952	52.5							
473.astar	8	941	59.7	939	59.8	**939**	**59.8**							
483.xalancbmk	8	684	80.7	692	79.8	**687**	**80.4**							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
    The config file option 'submit' was used.
     taskset was used to bind processes to cores
```

General Notes

```
    Hardware Prefetch Disabled, Adjacent Sector Prefetch Disabled
     'ulimit -s unlimited' was used to set the stack size to unlimited prior to run
```

Base Compiler Invocation

C benchmarks:
```
     icc
```

C++ benchmarks:
```
     icpc
```

Base Portability Flags

```
    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX
```

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Base Optimization Flags

C benchmarks:
```
-fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:
```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.09.html
http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revD.20090713.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.09.xml
http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revD.20090713.xml

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For other inquiries, please contact webmaster@spec.org.

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