



SPEC® CINT2006 Result

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IBM Corporation

SPECint®_rate2006 = Not Run

IBM System x3500 (Intel Xeon X5460)

SPECint_rate_base2006 = 133

CPU2006 license: 11

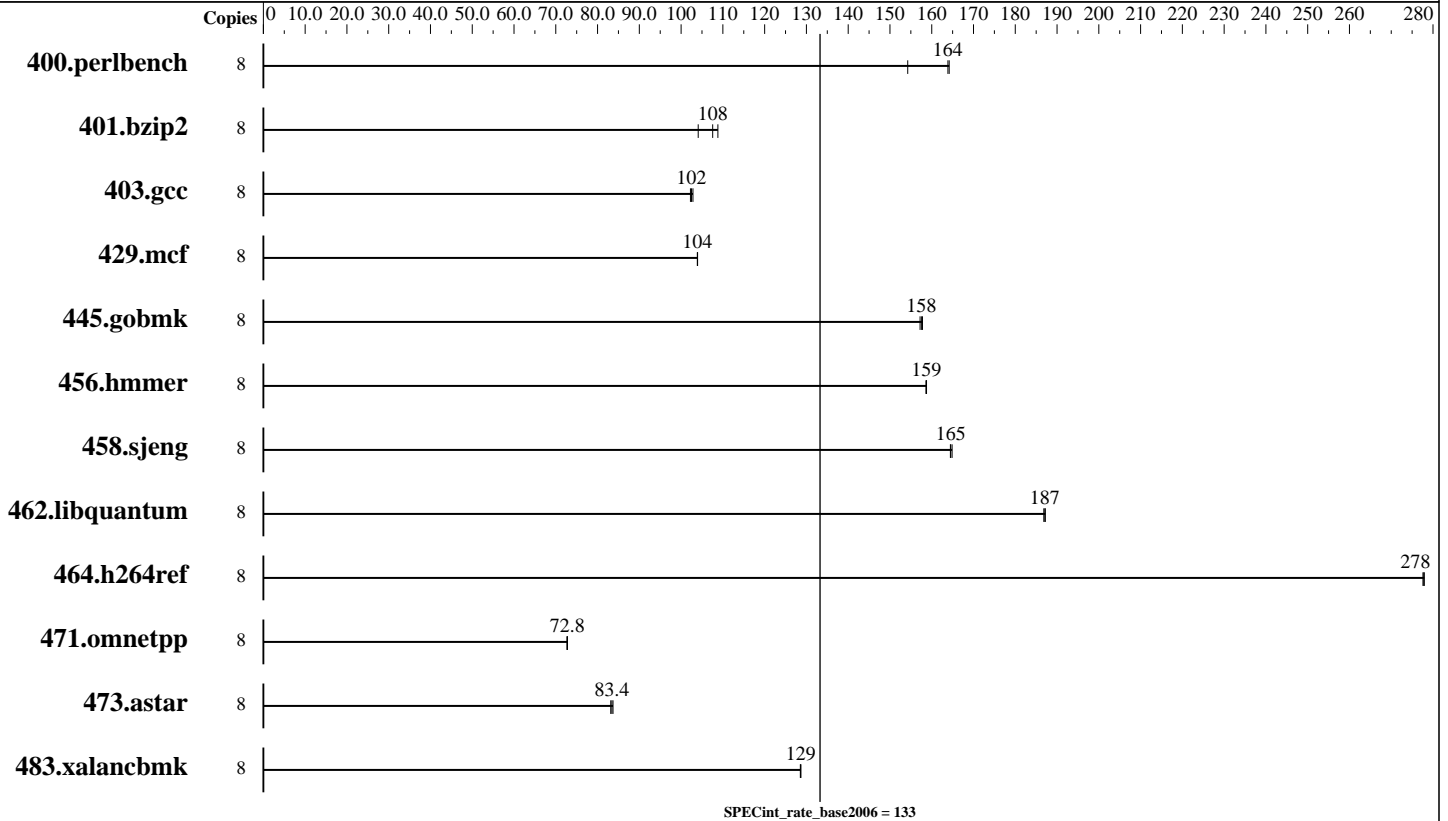
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Sep-2008

Hardware Availability: Nov-2008

Software Availability: Nov-2008



Hardware

CPU Name: Intel Xeon X5460
 CPU Characteristics: 1333MHz system bus
 CPU MHz: 3167
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 x 2 GB DDR2-5300F ECC)
 Disk Subsystem: 1 x 80 GB SATA, 72000 RPM
 Other Hardware: None

Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.042
 Auto Parallel: No
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	507	154	<u>477</u>	<u>164</u>	476	164							
401.bzip2	8	742	104	<u>718</u>	<u>108</u>	710	109							
403.gcc	8	<u>628</u>	<u>102</u>	630	102	626	103							
429.mcf	8	<u>702</u>	<u>104</u>	702	104	702	104							
445.gobmk	8	532	158	<u>533</u>	<u>158</u>	534	157							
456.hammer	8	471	159	<u>470</u>	<u>159</u>	470	159							
458.sjeng	8	589	164	587	165	<u>588</u>	<u>165</u>							
462.libquantum	8	887	187	886	187	<u>886</u>	<u>187</u>							
464.h264ref	8	<u>637</u>	<u>278</u>	638	278	637	278							
471.omnetpp	8	<u>687</u>	<u>72.8</u>	688	72.7	687	72.8							
473.astar	8	<u>673</u>	<u>83.4</u>	671	83.7	675	83.2							
483.xalancbmk	8	429	129	<u>429</u>	<u>129</u>	429	129							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
taskset was used to bind processes to cores

General Notes

Hardware Prefetch Disabled, Adjacent Sector Prefetch Disabled
'ulimit -s unlimited' was used to set the stack size to unlimited prior to the run

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

```
-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3 -opt-prefetch
```

C++ benchmarks:

```
-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.03.html>

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.11.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.03.xml>

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.11.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.

Report generated on Tue Jul 22 20:29:46 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 29 October 2008.