



SPEC® CINT2006 Result

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IBM Corporation

SPECint®_rate2006 = Not Run

IBM System x3650 (Intel Xeon X5470)

SPECint_rate_base2006 = 139

CPU2006 license: 11

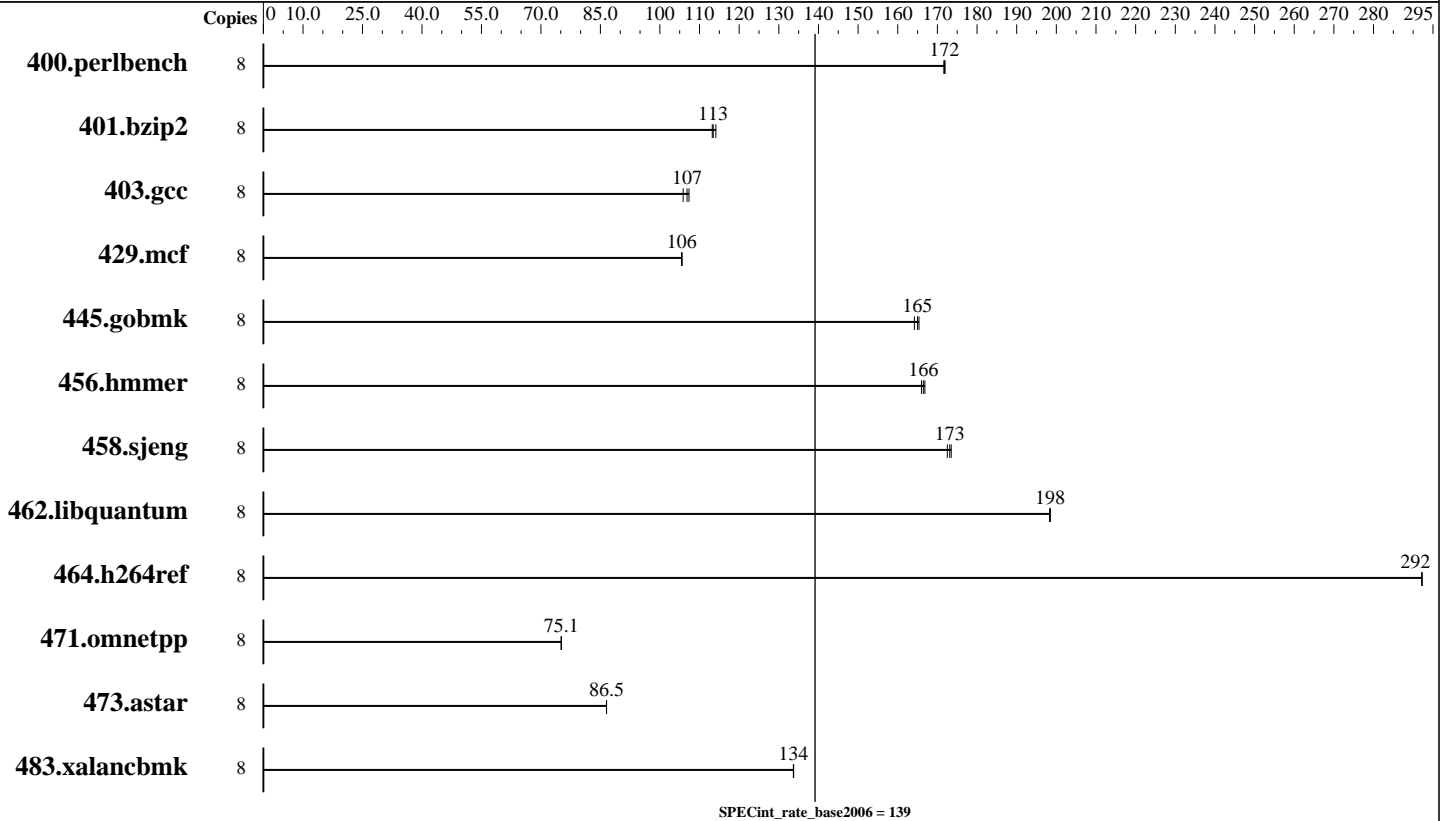
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Sep-2008

Hardware Availability: Nov-2008

Software Availability: Nov-2008



Hardware

CPU Name: Intel Xeon X5470
 CPU Characteristics: 1333MHz system bus
 CPU MHz: 3333
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 x 2 GB DDR2-5300F ECC)
 Disk Subsystem: 1 x 36 GB SAS, 15000 RPM
 Other Hardware: None

Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.042
 Auto Parallel: No
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	455	172	<u>455</u>	<u>172</u>	455	172							
401.bzip2	8	<u>680</u>	<u>113</u>	682	113	676	114							
403.gcc	8	600	107	<u>603</u>	<u>107</u>	608	106							
429.mcf	8	<u>691</u>	<u>106</u>	691	106	692	105							
445.gobmk	8	<u>509</u>	<u>165</u>	511	164	508	165							
456.hammer	8	450	166	<u>448</u>	<u>166</u>	447	167							
458.sjeng	8	561	173	<u>559</u>	<u>173</u>	558	173							
462.libquantum	8	836	198	<u>835</u>	<u>198</u>	835	198							
464.h264ref	8	<u>606</u>	<u>292</u>	606	292	606	292							
471.omnetpp	8	665	75.2	<u>666</u>	<u>75.1</u>	666	75.1							
473.astar	8	649	86.6	<u>649</u>	<u>86.5</u>	649	86.5							
483.xalanbmk	8	413	134	413	134	<u>413</u>	<u>134</u>							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
taskset was used to bind processes to cores except for 462.libquantum peak

General Notes

All benchmarks compiled in 32-bit mode
Hardware Prefetch Disabled, Adjacent Sector Prefetch Disabled
'ulimit -s unlimited' was used to set the stack size to unlimited prior to run

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalanbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

```
-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3 -opt-prefetch
```

C++ benchmarks:

```
-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.03.html>

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.html>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.04.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.03.xml>

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.xml>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.04.xml>

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For other inquiries, please contact webmaster@spec.org.

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