



SPEC® CINT2006 Result

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ASUSTeK Computer Inc.

SPECint®_rate2006 = 148

ASUS D5EB-DG Server Motherboard (Intel XEON X5482)

SPECint_rate_base2006 = 122

CPU2006 license: 001034

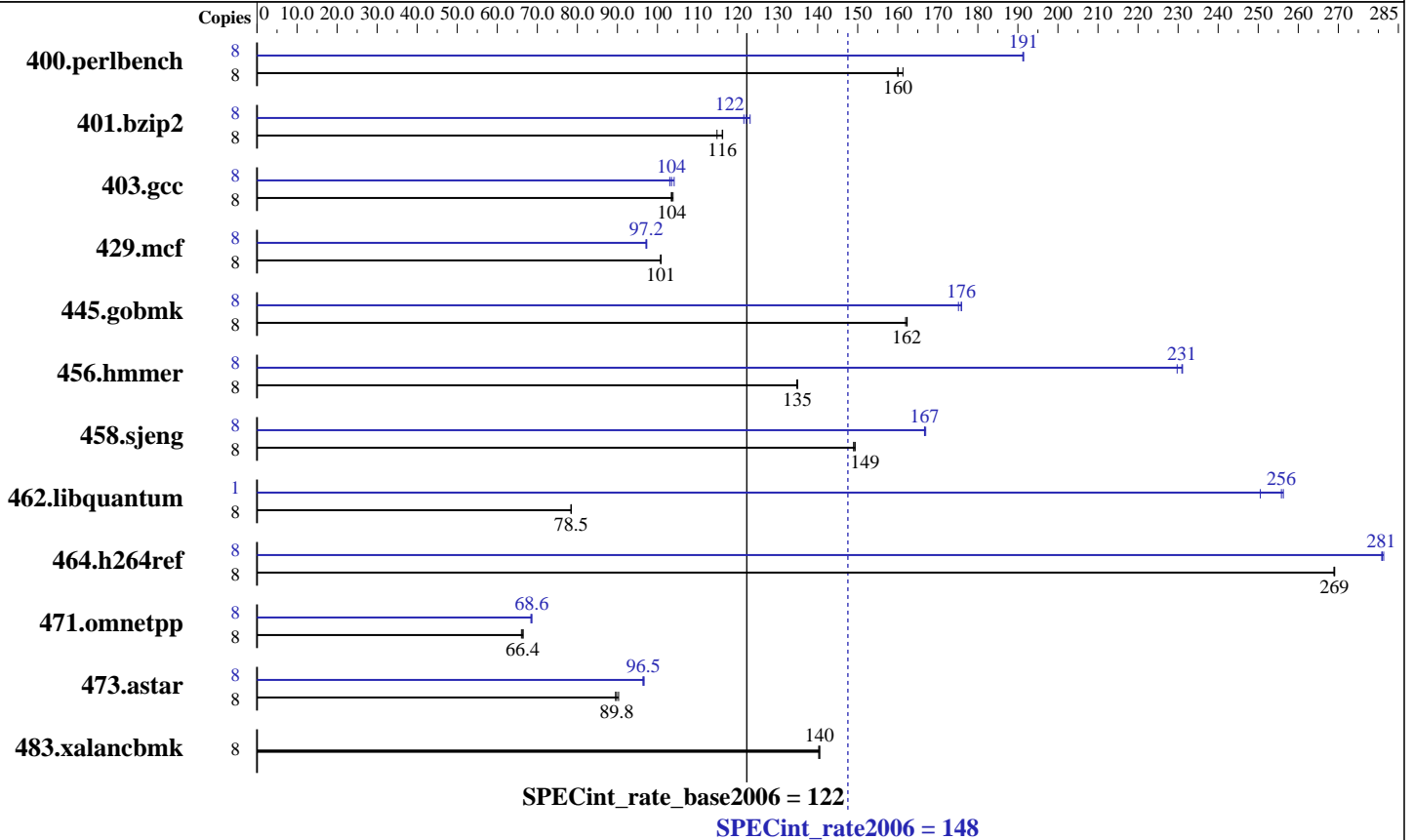
Test date: Aug-2008

Test sponsor: ASUSTeK Computer Inc.

Hardware Availability: Feb-2008

Tested by: ASUSTeK Computer Inc.

Software Availability: Feb-2008



Hardware

CPU Name: Intel Xeon X5482
 CPU Characteristics: 1600 MHz Bus Speed
 CPU MHz: 3200
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 32 GB (8 X 4GB PC2-6400F, CL5, FBDIMM)
 Disk Subsystem: Seagate ST3500830AS 500GB SATAII, 7200RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Multi-user, run level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: SmartHeap library V8.1 Binutils 2.17.50.0.15



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	488	160	485	161	488	160	8	409	191	408	191	408	191
401.bzip2	8	672	115	664	116	665	116	8	627	123	635	122	632	122
403.gcc	8	623	103	620	104	622	104	8	625	103	618	104	622	104
429.mcf	8	724	101	723	101	724	101	8	751	97.2	751	97.2	750	97.3
445.gobmk	8	517	162	518	162	517	162	8	477	176	477	176	479	175
456.hmmer	8	553	135	554	135	553	135	8	323	231	323	231	325	230
458.sjeng	8	649	149	650	149	648	149	8	581	167	580	167	581	167
462.libquantum	8	2113	78.5	2113	78.5	2114	78.4	1	80.8	256	81.0	256	82.7	251
464.h264ref	8	658	269	658	269	658	269	8	630	281	630	281	629	281
471.omnetpp	8	752	66.4	757	66.0	753	66.4	8	729	68.6	729	68.6	731	68.4
473.astar	8	627	89.5	622	90.3	625	89.8	8	582	96.5	581	96.7	583	96.4
483.xalanbmk	8	393	140	393	141	394	140	8	393	140	393	141	394	140

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0

General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer, for peak, are compiled in 64-bit mode

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalanbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

Continued on next page



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Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revC.20090713.html>



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You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revC.20090713.xml>

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