



# SPEC® CINT2006 Result

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**ACTION S.A.**

**SPECint®\_rate2006 = 33.8**

**ACTINA SIERRA (Intel Core 2 Duo E7200)**

**SPECint\_rate\_base2006 = 29.4**

CPU2006 license: 9008

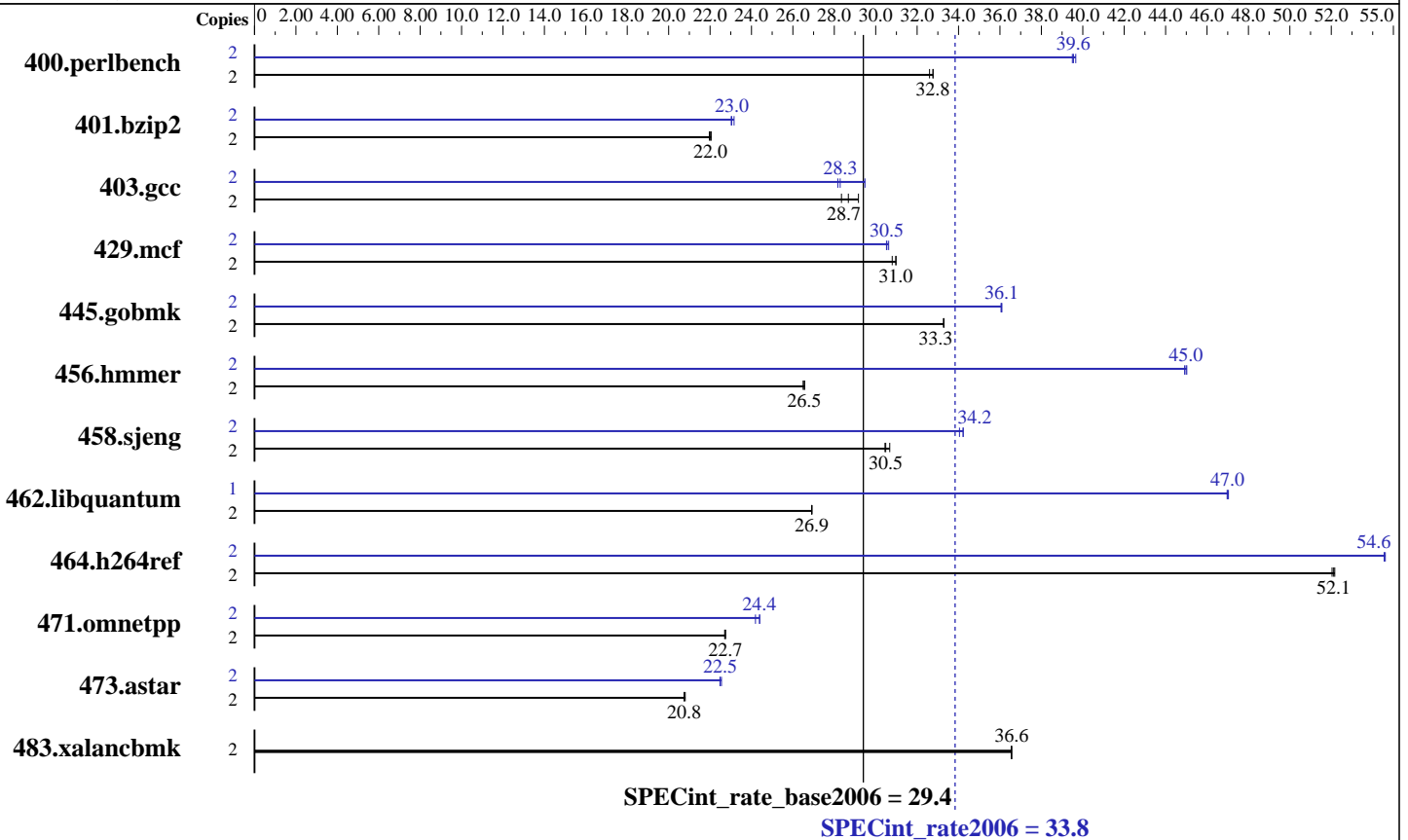
Test sponsor: ACTION S.A.

Tested by: Piotr Nowicki

Test date: Aug-2008

Hardware Availability: Dec-2007

Software Availability: Nov-2007



### Hardware

CPU Name: Intel Core 2 Duo E7200  
 CPU Characteristics: 1066 MHz bus  
 CPU MHz: 2533  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 3 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 2 GB (2x 1 GB PC2-6400 DDR-2)  
 Disk Subsystem: 1x 250 GB SATA II, 7200 RPM  
 Other Hardware: None

### Software

Operating System: SuSE Linux Enterprise Server 10 (x86\_64) SP1, Kernel 2.6.16.46-0.12-smp  
 Compiler: Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l\_cc\_p\_10.1.008  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Multi-user, run level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1 Binutils 2.17.50.0.15



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## Results Table

| Benchmark      | Base   |             |             |            |             |            |             | Peak   |            |             |            |             |            |             |
|----------------|--------|-------------|-------------|------------|-------------|------------|-------------|--------|------------|-------------|------------|-------------|------------|-------------|
|                | Copies | Seconds     | Ratio       | Seconds    | Ratio       | Seconds    | Ratio       | Copies | Seconds    | Ratio       | Seconds    | Ratio       | Seconds    | Ratio       |
| 400.perlbench  | 2      | 599         | 32.6        | 596        | 32.8        | <b>596</b> | <b>32.8</b> | 2      | 495        | 39.5        | 493        | 39.7        | <b>494</b> | <b>39.6</b> |
| 401.bzip2      | 2      | 879         | 22.0        | <b>876</b> | <b>22.0</b> | 875        | 22.1        | 2      | <b>837</b> | <b>23.0</b> | 833        | 23.2        | 838        | 23.0        |
| 403.gcc        | 2      | <b>561</b>  | <b>28.7</b> | 552        | 29.2        | 568        | 28.3        | 2      | 546        | 29.5        | <b>569</b> | <b>28.3</b> | 571        | 28.2        |
| 429.mcf        | 2      | <b>589</b>  | <b>31.0</b> | 592        | 30.8        | 589        | 31.0        | 2      | <b>597</b> | <b>30.5</b> | 596        | 30.6        | 598        | 30.5        |
| 445.gobmk      | 2      | <b>630</b>  | <b>33.3</b> | 630        | 33.3        | 630        | 33.3        | 2      | 582        | 36.1        | 581        | 36.1        | <b>581</b> | <b>36.1</b> |
| 456.hmmmer     | 2      | 702         | 26.6        | <b>703</b> | <b>26.5</b> | 704        | 26.5        | 2      | 414        | 45.0        | <b>415</b> | <b>45.0</b> | 415        | 44.9        |
| 458.sjeng      | 2      | <b>794</b>  | <b>30.5</b> | 789        | 30.7        | 795        | 30.4        | 2      | 707        | 34.2        | 711        | 34.0        | <b>707</b> | <b>34.2</b> |
| 462.libquantum | 2      | <b>1540</b> | <b>26.9</b> | 1540       | 26.9        | 1539       | 26.9        | 1      | 441        | 47.0        | 441        | 47.0        | <b>441</b> | <b>47.0</b> |
| 464.h264ref    | 2      | <b>849</b>  | <b>52.1</b> | 848        | 52.2        | 851        | 52.0        | 2      | <b>811</b> | <b>54.6</b> | 811        | 54.6        | 810        | 54.6        |
| 471.omnetpp    | 2      | <b>550</b>  | <b>22.7</b> | 549        | 22.7        | 551        | 22.7        | 2      | 512        | 24.4        | 517        | 24.2        | <b>513</b> | <b>24.4</b> |
| 473.astar      | 2      | 677         | 20.7        | 675        | 20.8        | <b>677</b> | <b>20.8</b> | 2      | 625        | 22.5        | <b>624</b> | <b>22.5</b> | 623        | 22.6        |
| 483.xalancbmk  | 2      | 378         | 36.5        | 377        | 36.6        | <b>377</b> | <b>36.6</b> | 2      | 378        | 36.5        | 377        | 36.6        | <b>377</b> | <b>36.6</b> |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmmer, for peak, are compiled in 64-bit mode.  
Taskset command was used to bind processes to CPUs.

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-fast -inline-calloc -opt-malloc-options=3

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## Base Optimization Flags (Continued)

C++ benchmarks:

```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

```
401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include
```

```
456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include
```

C++ benchmarks:

icpc

## Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

## Peak Optimization Flags

C benchmarks:

```
400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch
```

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## Peak Optimization Flags (Continued)

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec-div -ansi-alias

456.hmmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch  
-opt-streaming-stores always -vec-guard-write  
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs  
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs  
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revC.20090714.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revC.20090714.00.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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