



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECint®_rate2006 = 112

PowerEdge 2900 III (Intel Xeon E5410, 2.33 GHz)

SPECint_rate_base2006 = 91.8

CPU2006 license: 55

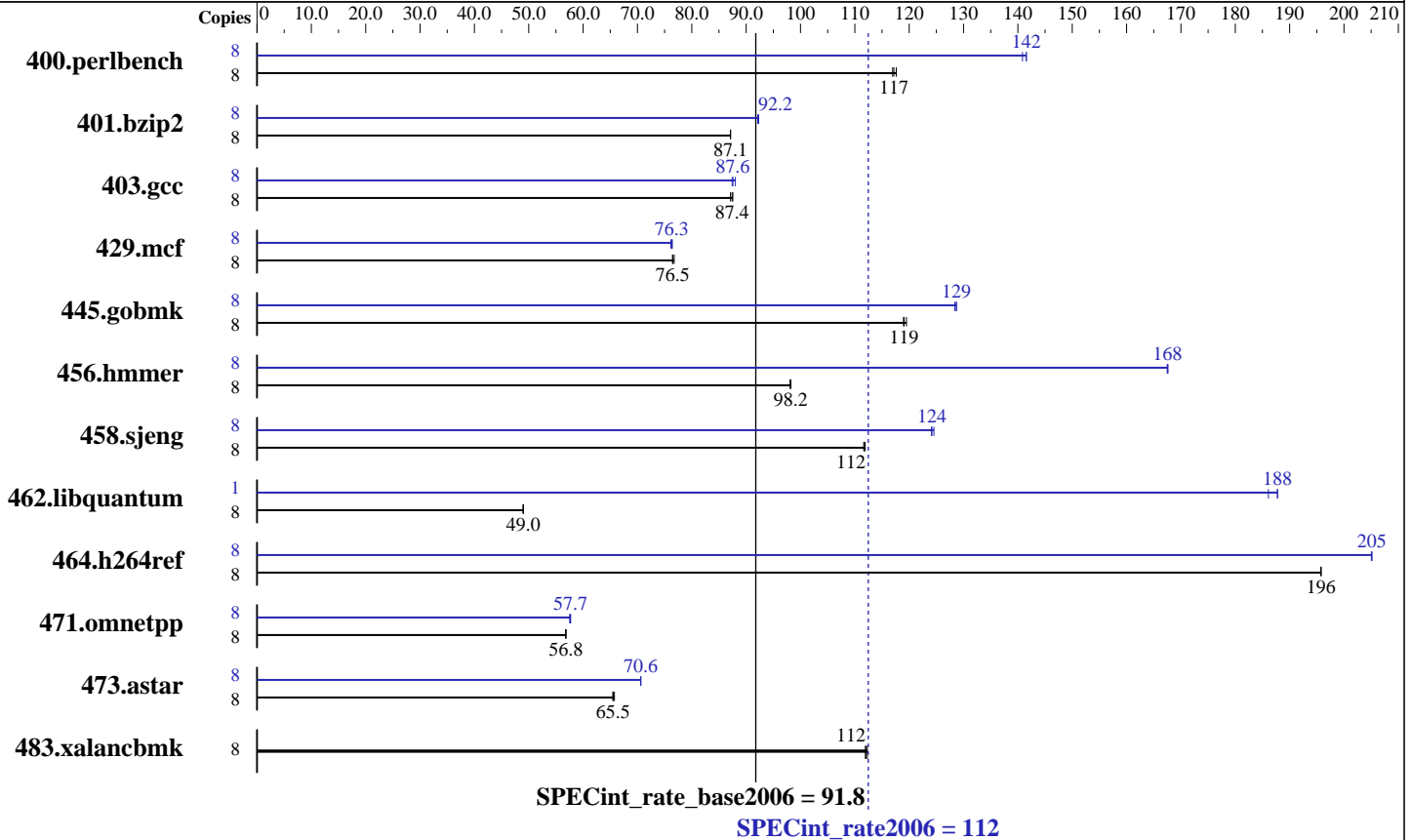
Test date: Jul-2008

Test sponsor: Dell Inc.

Hardware Availability: Nov-2007

Tested by: Dell Inc.

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E5410
 CPU Characteristics: 1333 MHz system bus
 CPU MHz: 2333
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (8 x 1GB 667 MHz DDR2 FB-DIMM)
 Disk Subsystem: 1 x 300GB 10000 RPM SAS
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16-60.0.21-smp
 Compiler: Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Multi-user, run level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1 Binutils 2.17.50.0.15



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECint_rate2006 = 112

PowerEdge 2900 III (Intel Xeon E5410, 2.33 GHz)

SPECint_rate_base2006 = 91.8

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Jul-2008
Hardware Availability: Nov-2007
Software Availability: Nov-2007

Results Table

| Benchmark | Base | | | | | | Peak | | | | | | | |
|----------------|--------|------------|-------------|-------------|-------------|------------|-------------|--------|------------|------------|------------|-------------|------------|-------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 8 | 668 | 117 | 667 | 117 | 664 | 118 | 8 | 555 | 141 | 552 | 142 | 552 | 142 |
| 401.bzip2 | 8 | 886 | 87.1 | 886 | 87.1 | 886 | 87.1 | 8 | 838 | 92.1 | 836 | 92.3 | 838 | 92.2 |
| 403.gcc | 8 | 739 | 87.1 | 736 | 87.5 | 737 | 87.4 | 8 | 732 | 88.0 | 735 | 87.6 | 737 | 87.4 |
| 429.mcf | 8 | 950 | 76.8 | 954 | 76.5 | 954 | 76.5 | 8 | 955 | 76.4 | 958 | 76.2 | 956 | 76.3 |
| 445.gobmk | 8 | 705 | 119 | 702 | 119 | 705 | 119 | 8 | 652 | 129 | 654 | 128 | 652 | 129 |
| 456.hammer | 8 | 761 | 98.1 | 760 | 98.2 | 760 | 98.2 | 8 | 445 | 168 | 446 | 167 | 446 | 168 |
| 458.sjeng | 8 | 866 | 112 | 867 | 112 | 865 | 112 | 8 | 780 | 124 | 777 | 125 | 779 | 124 |
| 462.libquantum | 8 | 3384 | 49.0 | 3384 | 49.0 | 3384 | 49.0 | 1 | 111 | 186 | 110 | 188 | 110 | 188 |
| 464.h264ref | 8 | 904 | 196 | 904 | 196 | 904 | 196 | 8 | 863 | 205 | 863 | 205 | 863 | 205 |
| 471.omnetpp | 8 | 880 | 56.8 | 880 | 56.8 | 880 | 56.8 | 8 | 869 | 57.5 | 867 | 57.7 | 866 | 57.7 |
| 473.astar | 8 | 858 | 65.5 | 857 | 65.5 | 854 | 65.8 | 8 | 795 | 70.6 | 795 | 70.6 | 796 | 70.6 |
| 483.xalancbmk | 8 | 493 | 112 | 492 | 112 | 493 | 112 | 8 | 493 | 112 | 492 | 112 | 493 | 112 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Invocation Notes

OMP_NUM_THREADS set to number of cores
KMP_STACK_SIZE set to 64M
KMP_AFFINITY set to physical,0

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs

Platform Notes

BIOS Settings:
Adjacent Cache Line Prefetch = Disabled (default Enabled)

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECint_rate2006 = 112

PowerEdge 2900 III (Intel Xeon E5410, 2.33 GHz)

SPECint_rate_base2006 = 91.8

CPU2006 license: 55

Test date: Jul-2008

Test sponsor: Dell Inc.

Hardware Availability: Nov-2007

Tested by: Dell Inc.

Software Availability: Nov-2007

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3
C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc
401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include
456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include
C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECint_rate2006 = 112

PowerEdge 2900 III (Intel Xeon E5410, 2.33 GHz)

SPECint_rate_base2006 = 91.8

CPU2006 license: 55

Test date: Jul-2008

Test sponsor: Dell Inc.

Hardware Availability: Nov-2007

Tested by: Dell Inc.

Software Availability: Nov-2007

Peak Portability Flags (Continued)

483.xalanbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

483.xalanbmk: basepeak = yes



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECint_rate2006 = 112

PowerEdge 2900 III (Intel Xeon E5410, 2.33 GHz)

SPECint_rate_base2006 = 91.8

CPU2006 license: 55

Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: Jul-2008

Hardware Availability: Nov-2007

Software Availability: Nov-2007

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Dell-Intel-ic10.1-int-linux64-revC.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Dell-Intel-ic10.1-int-linux64-revC.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 18:45:35 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 5 August 2008.