



# SPEC® CINT2006 Result

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## Bull SAS

NovaScale T860 E1  
(Intel Xeon E5405, 2.00 GHz)

SPECint®\_rate2006 = 54.4

SPECint\_rate\_base2006 = 46.2

CPU2006 license: 20

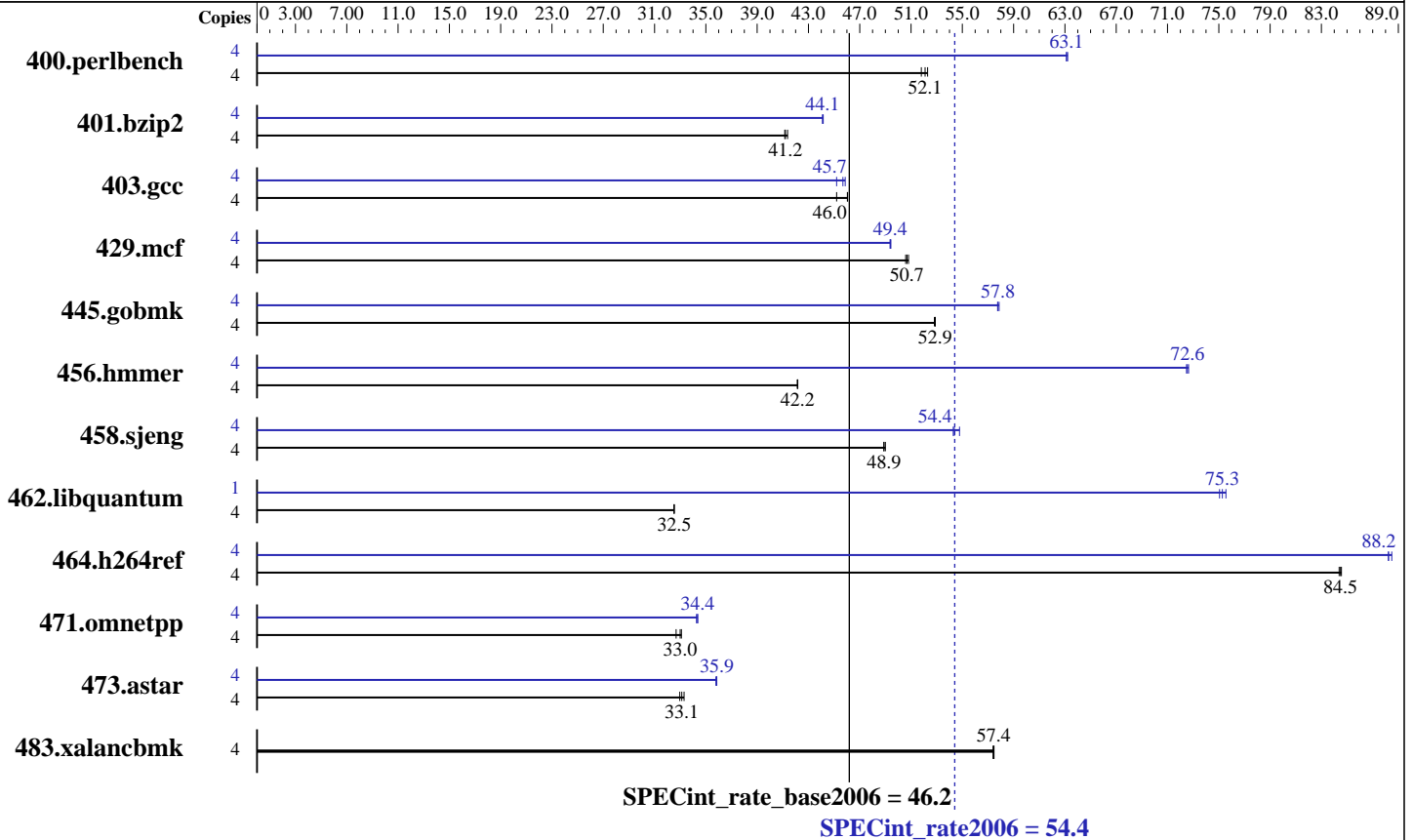
Test sponsor: Bull SAS

Tested by: NEC Corporation

Test date: May-2008

Hardware Availability: Apr-2008

Software Availability: Nov-2007



### Hardware

CPU Name: Intel Xeon E5405  
 CPU Characteristics: 2.00 GHz, 2x6 MB L2 shared, 1333 MHz bus  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8x2 GB PC2-5300F, 2 rank, CL5-5-5, ECC)  
 Disk Subsystem: 1x73.2 GB SAS, 15000RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP1, Kernel 2.6.16.46-0.12-smp  
 Compiler: Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070913 Package ID: L\_cc\_p\_10.1.008  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: MicroQuill SmartHeap library 8.1 binutils-2.17.tar.gz, Version 2.17



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	<b>750</b>	<b>52.1</b>	754	51.8	747	52.3	4	619	63.1	<b>619</b>	<b>63.1</b>	618	63.2
401.bzip2	4	938	41.2	933	41.4	<b>937</b>	<b>41.2</b>	4	<b>875</b>	<b>44.1</b>	875	44.1	874	44.2
403.gcc	4	<b>699</b>	<b>46.0</b>	712	45.2	699	46.1	4	<b>705</b>	<b>45.7</b>	702	45.9	712	45.2
429.mcf	4	<b>720</b>	<b>50.7</b>	721	50.6	718	50.8	4	<b>739</b>	<b>49.4</b>	739	49.4	738	49.4
445.gobmk	4	793	52.9	794	52.8	<b>793</b>	<b>52.9</b>	4	727	57.8	725	57.9	<b>726</b>	<b>57.8</b>
456.hmmer	4	886	42.1	<b>885</b>	<b>42.2</b>	885	42.2	4	515	72.5	<b>514</b>	<b>72.6</b>	514	72.6
458.sjeng	4	988	49.0	<b>990</b>	<b>48.9</b>	990	48.9	4	<b>890</b>	<b>54.4</b>	884	54.8	891	54.3
462.libquantum	4	2549	32.5	2548	32.5	<b>2548</b>	<b>32.5</b>	1	274	75.6	<b>275</b>	<b>75.3</b>	276	75.1
464.h264ref	4	1047	84.6	<b>1048</b>	<b>84.5</b>	1049	84.4	4	1000	88.5	1003	88.2	<b>1003</b>	<b>88.2</b>
471.omnetpp	4	765	32.7	<b>758</b>	<b>33.0</b>	755	33.1	4	728	34.4	<b>728</b>	<b>34.4</b>	730	34.3
473.astar	4	843	33.3	852	33.0	<b>848</b>	<b>33.1</b>	4	785	35.8	<b>783</b>	<b>35.9</b>	783	35.9
483.xalancbmk	4	<b>480</b>	<b>57.4</b>	480	57.5	481	57.4	4	<b>480</b>	<b>57.4</b>	480	57.5	481	57.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run  
OMP\_NUM\_THREADS set to number of cores

## Platform Notes

Bios settings:  
Hardware Prefetcher: Enabled  
Adjacent Cache Line Prefetch: Disabled  
Intel SpeedStep Technology: Disabled

## General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2, 456.hmmer, for peak, are compiled in 64-bit mode

The NEC Express5800/120Lj(Intel Xeon E5405) and the Bull NovaScale T860 E1(Intel Xeon E5405,2.00GHz) models are electronically equivalent. The results have been measured on a NEC Express5800/120Lj(Intel Xeon E5405) model.

## Base Compiler Invocation

C benchmarks:  
icc

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## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-fast -inline-calloc -opt-malloc-options=3  
C++ benchmarks:  
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/opt/SmartHeap\_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc  
401.bzip2: /opt/intel/cce/10.1.008/bin/icc  
-L/opt/intel/cce/10.1.008/lib  
-I/opt/intel/cce/10.1.008/include  
456.hmmer: /opt/intel/cce/10.1.008/bin/icc  
-L/opt/intel/cce/10.1.008/lib  
-I/opt/intel/cce/10.1.008/include  
C++ benchmarks:  
icpc



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## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

### C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch  
401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch  
403.gcc: -fast -inline-calloc -opt-malloc-options=3  
429.mcf: -fast -prefetch  
445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec-div -ansi-alias  
456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive  
458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4  
462.libquantum: -fast -unroll4 -Ob0 -prefetch  
-opt-streaming-stores always -vec-guard-write  
-opt-malloc-options=3 -parallel -par-runtime-control  
464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

### C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs -L/opt/SmartHeap\_8.1/lib -lsmarheap  
473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs -L/opt/SmartHeap\_8.1/lib -lsmarheap  
483.xalancbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-INT-ia32-linux-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-INT-ia32-linux-flags.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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