



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

Express5800/120Bb-6
(Intel Xeon E5205)

SPECint®_rate2006 = 27.5

SPECint_rate_base2006 = 23.5

CPU2006 license: 9006

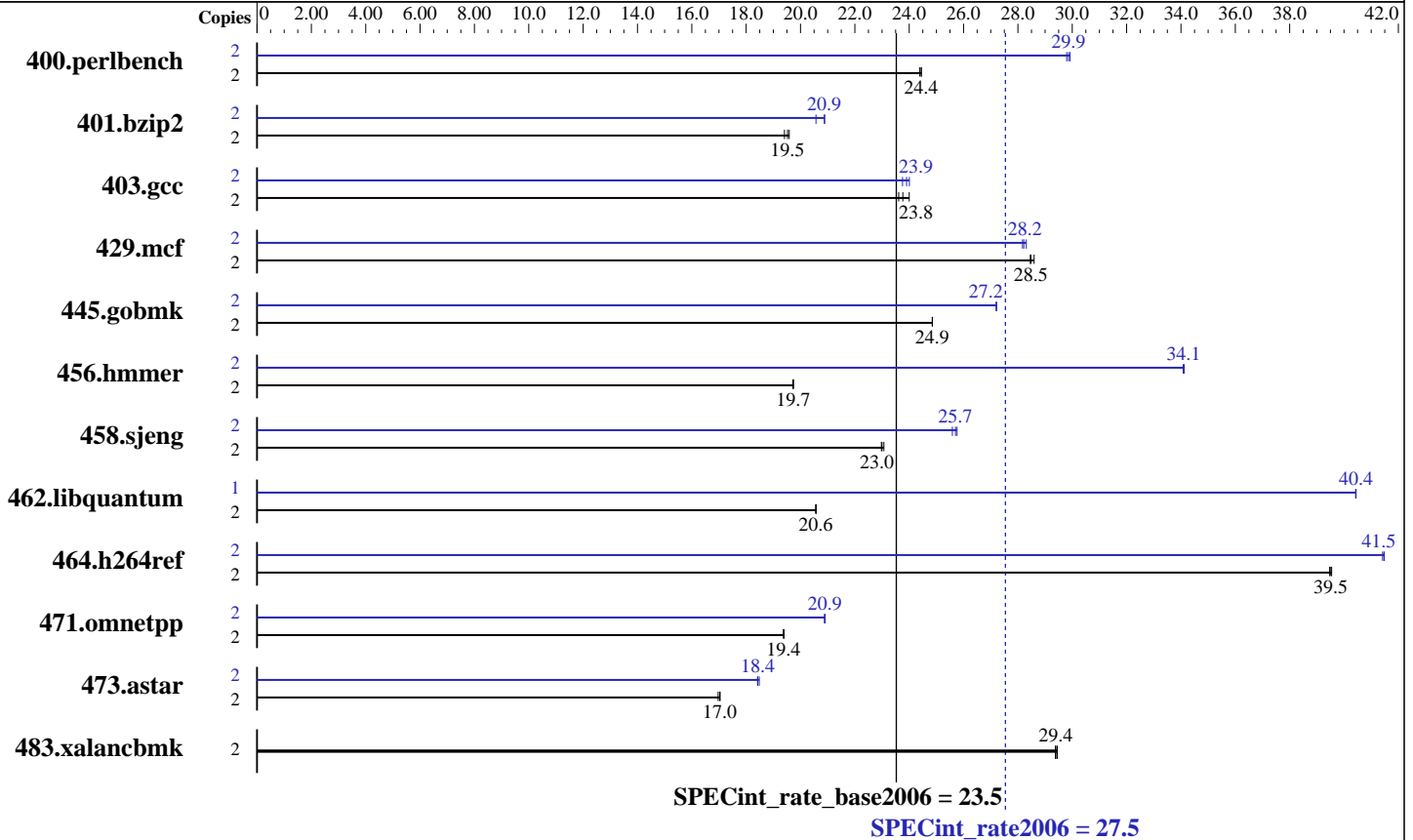
Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: May-2008

Hardware Availability: Feb-2008

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E5205
 CPU Characteristics: 1.86 GHz, 6 MB L2, 1066 MHz bus
 CPU MHz: 1867
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (4x2 GB PC2-5300F, 2 rank, CL5-5-5, ECC)
 Disk Subsystem: 1x73.2 GB SAS, 15000RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070913 Package ID: L_cc_p_10.1.008
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap library 8.1 binutils-2.17.tar.gz, Version 2.17



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

Express5800/120Bb-6
(Intel Xeon E5205)

SPECint_rate2006 = 27.5

SPECint_rate_base2006 = 23.5

CPU2006 license: 9006

Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: May-2008

Hardware Availability: Feb-2008

Software Availability: Nov-2007

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	799	24.4	801	24.4	<u>799</u>	<u>24.4</u>	2	653	29.9	656	29.8	<u>654</u>	<u>29.9</u>
401.bzip2	2	985	19.6	994	19.4	<u>989</u>	<u>19.5</u>	2	<u>925</u>	<u>20.9</u>	938	20.6	924	20.9
403.gcc	2	682	23.6	<u>677</u>	<u>23.8</u>	671	24.0	2	<u>674</u>	<u>23.9</u>	678	23.8	671	24.0
429.mcf	2	641	28.5	<u>640</u>	<u>28.5</u>	638	28.6	2	648	28.2	644	28.3	<u>646</u>	<u>28.2</u>
445.gobmk	2	844	24.8	<u>844</u>	<u>24.9</u>	844	24.9	2	<u>771</u>	<u>27.2</u>	771	27.2	771	27.2
456.hmmer	2	945	19.7	<u>946</u>	<u>19.7</u>	946	19.7	2	547	34.1	548	34.1	<u>547</u>	<u>34.1</u>
458.sjeng	2	1053	23.0	1049	23.1	<u>1052</u>	<u>23.0</u>	2	939	25.8	946	25.6	<u>942</u>	<u>25.7</u>
462.libquantum	2	2014	20.6	<u>2015</u>	<u>20.6</u>	2015	20.6	1	513	40.4	512	40.4	<u>512</u>	<u>40.4</u>
464.h264ref	2	<u>1120</u>	<u>39.5</u>	1119	39.5	1121	39.5	2	1069	41.4	<u>1067</u>	<u>41.5</u>	1067	41.5
471.omnetpp	2	645	19.4	645	19.4	<u>645</u>	<u>19.4</u>	2	<u>598</u>	<u>20.9</u>	599	20.9	598	20.9
473.astar	2	827	17.0	<u>824</u>	<u>17.0</u>	824	17.0	2	762	18.4	<u>762</u>	<u>18.4</u>	760	18.5
483.xalanbmk	2	469	29.4	<u>469</u>	<u>29.4</u>	470	29.4	2	469	29.4	<u>469</u>	<u>29.4</u>	470	29.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores

General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2,
456.hmmer, for peak, are compiled in 64-bit mode

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalanbmk: -DSPEC_CPU_LINUX



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

Express5800/120Bb-6
(Intel Xeon E5205)

SPECint_rate2006 = 27.5

SPECint_rate_base2006 = 23.5

CPU2006 license: 9006

Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: May-2008

Hardware Availability: Feb-2008

Software Availability: Nov-2007

Base Optimization Flags

C benchmarks:

```
-fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:

```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/opt/SmartHeap_8.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

```
401.bzip2: /opt/intel/cce/10.1.008/bin/icc  
-L/opt/intel/cce/10.1.008/lib  
-I/opt/intel/cce/10.1.008/include
```

```
456.hmmer: /opt/intel/cce/10.1.008/bin/icc  
-L/opt/intel/cce/10.1.008/lib  
-I/opt/intel/cce/10.1.008/include
```

C++ benchmarks:

icpc

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32  
401.bzip2: -DSPEC_CPU_LP64  
456.hmmer: -DSPEC_CPU_LP64  
462.libquantum: -DSPEC_CPU_LINUX  
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

Express5800/120Bb-6
(Intel Xeon E5205)

SPECint_rate2006 = 27.5

SPECint_rate_base2006 = 23.5

CPU2006 license: 9006

Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: May-2008

Hardware Availability: Feb-2008

Software Availability: Nov-2007

Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmr: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmarheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-INT-ia32-linux-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-INT-ia32-linux-flags.xml>



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

Express5800/120Bb-6
(Intel Xeon E5205)

SPECint_rate2006 = 27.5

SPECint_rate_base2006 = 23.5

CPU2006 license: 9006

Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: May-2008

Hardware Availability: Feb-2008

Software Availability: Nov-2007

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 17:36:43 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 11 June 2008.