



SPEC® CINT2006 Result

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Itautec

SPECint®_rate2006 = 125

Servidor Itautec MX221 (Intel Xeon E5430)

SPECint_rate_base2006 = 102

CPU2006 license: 9001

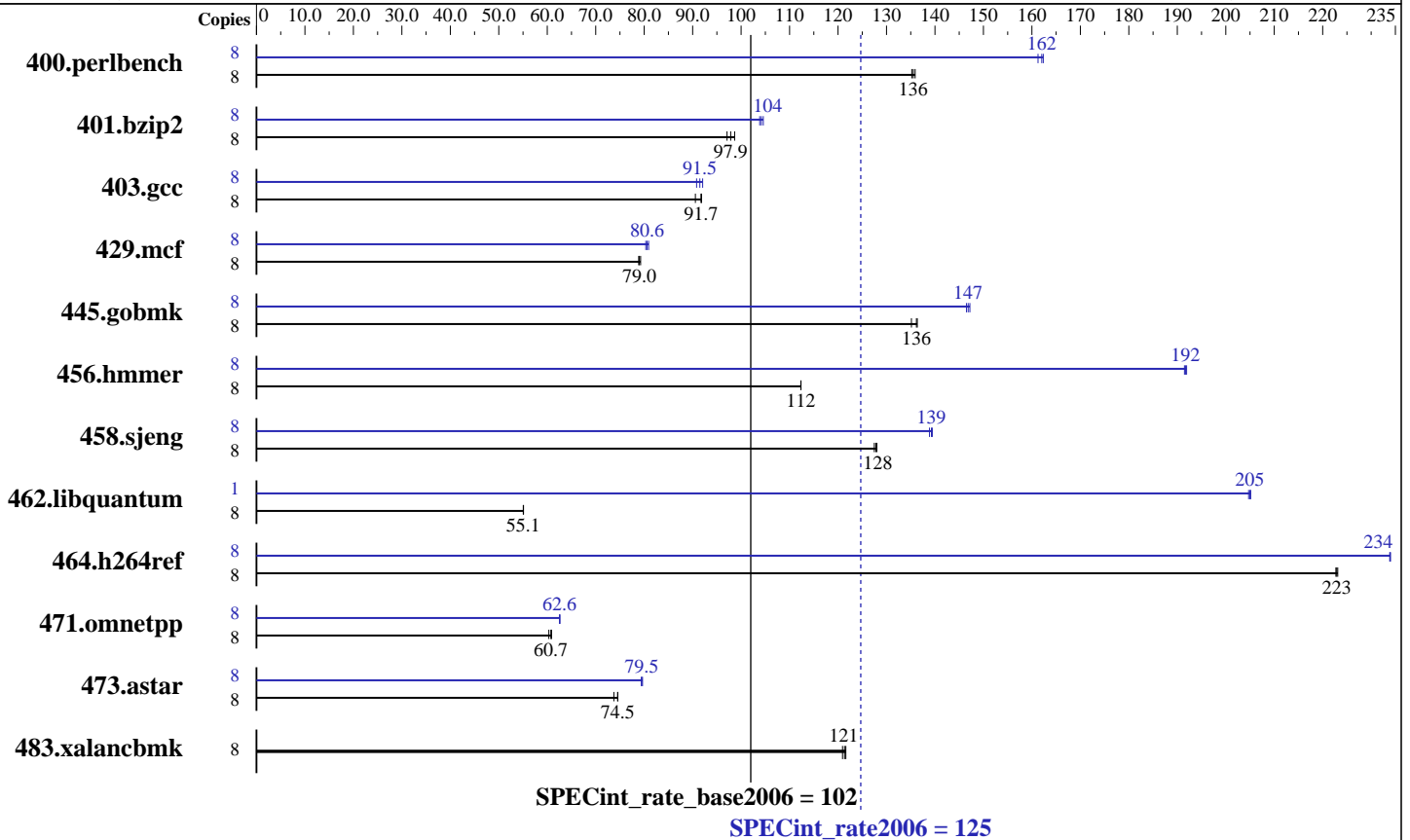
Test date: May-2008

Test sponsor: Itautec

Hardware Availability: Dec-2007

Tested by: Itautec

Software Availability: Jan-2008



Hardware

CPU Name: Intel Xeon E5430
 CPU Characteristics:
 CPU MHz: 2660
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 * 2 GB PC2-5300 FBDIMM, CL-5-5-5, ECC)
 Disk Subsystem: 1 x SCSI, 73GB, 15000 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler for Linux version 10.1 Build 20080112 Package ID: l_cc_p_10.1.012
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run Level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Binutils 2.17.10.50
 MicroQuill SmartHeap V8.1



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	578	135	<u>577</u>	<u>136</u>	575	136	8	485	161	<u>482</u>	<u>162</u>	481	162
401.bzip2	8	<u>789</u>	<u>97.9</u>	782	98.7	795	97.1	8	738	105	743	104	<u>741</u>	<u>104</u>
403.gcc	8	<u>702</u>	<u>91.7</u>	711	90.6	701	91.8	8	709	90.8	700	92.1	<u>704</u>	<u>91.5</u>
429.mcf	8	920	79.3	<u>923</u>	<u>79.0</u>	925	78.9	8	<u>905</u>	<u>80.6</u>	902	80.9	908	80.3
445.gobmk	8	621	135	615	136	<u>616</u>	<u>136</u>	8	<u>571</u>	<u>147</u>	573	146	570	147
456.hammer	8	664	112	<u>664</u>	<u>112</u>	665	112	8	<u>389</u>	<u>192</u>	390	191	389	192
458.sjeng	8	<u>757</u>	<u>128</u>	756	128	759	127	8	<u>695</u>	<u>139</u>	697	139	694	139
462.libquantum	8	3011	55.1	<u>3010</u>	<u>55.1</u>	3008	55.1	1	101	205	<u>101</u>	<u>205</u>	101	205
464.h264ref	8	794	223	795	223	<u>794</u>	<u>223</u>	8	757	234	757	234	<u>757</u>	<u>234</u>
471.omnetpp	8	<u>823</u>	<u>60.7</u>	829	60.3	821	60.9	8	<u>799</u>	<u>62.6</u>	799	62.6	799	62.6
473.astar	8	762	73.7	753	74.5	<u>753</u>	<u>74.5</u>	8	707	79.4	<u>706</u>	<u>79.5</u>	705	79.7
483.xalancbmk	8	<u>455</u>	<u>121</u>	454	122	456	121	8	<u>455</u>	<u>121</u>	454	122	456	121

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Invocation Notes

OMP_NUM_THREADS set to number of cores
KMP_STACK_SIZE set to 64M
KMP_AFFINITY set to physical,0

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
'/usr/bin/taskset' used to bind benchmark copies to processor, except for 462.libquantum at peak.

Platform Notes

BIOS configuration:
Hardware Prefetch Enabled

General Notes

This result was measured on the Servidor Itautec MX201.
The Servidor Itautec MX221 and the Servidor Itautec MX201 are electronically equivalent.

Base Compiler Invocation

C benchmarks:
icc

Continued on next page



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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/opt/sh/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.1.012/bin/icc
-L/opt/intel/cce/10.1.012/lib
-I/opt/intel/cce/10.1.012/include

456.hmmer: /opt/intel/cce/10.1.012/bin/icc
-L/opt/intel/cce/10.1.012/lib
-I/opt/intel/cce/10.1.012/include

C++ benchmarks:
icpc



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Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias -prefetch
401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
403.gcc: -fast -inline-calloc -opt-malloc-options=3
429.mcf: -fast -prefetch
445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo -no-prec-div -ansi-alias
456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive
458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4
462.libquantum: -fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control
464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/opt/sh/SmartHeap_8.1/lib -lsmarheap
473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/opt/sh/SmartHeap_8.1/lib -lsmarheap
483.xalancbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itautec-ic10.1-INT-intel64-linux-flags.20090714.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itautec-ic10.1-INT-intel64-linux-flags.20090714.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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