



SPEC® CINT2006 Result

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Supermicro Motherboard X7DBT

SPECint®_rate2006 = 130

SPECint_rate_base2006 = 106

CPU2006 license: 001176

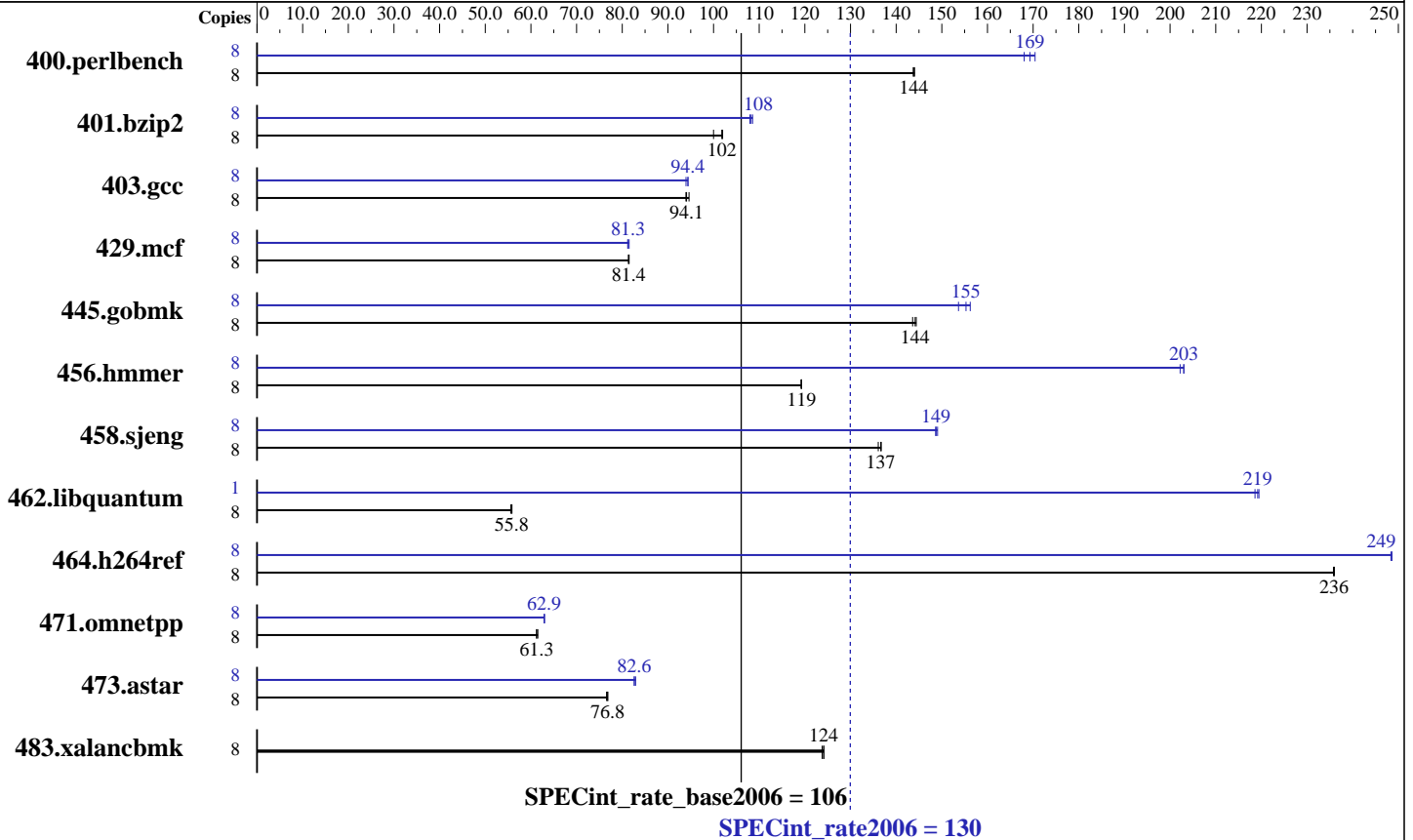
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Mar-2008

Hardware Availability: Nov-2007

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E5440
 CPU Characteristics: Quad Core, 2.83 GHz, 1333 MHz System Bus
 CPU MHz: 2833
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 32 GB (8 X 4 GB ECC PC2-5300, CL5, FBDIMM)
 Disk Subsystem: SAMSUNG HD501LJ 500 GB SATA II, 7200 RPM
 Other Hardware: None

Software

Operating System: 64-Bit SUSE Linux Enterprise Server 10 SP1
 Compiler: Intel C++ and Fortran Compiler for Linux32 and Linux64 version 10.1 Build 20070725
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Multi-user, run level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap Library 8.1 Binutils 2.17.50.0.15



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	543	144	544	144	543	144	8	465	168	459	170	462	169
401.bzip2	8	758	102	772	100	757	102	8	711	109	713	108	715	108
403.gcc	8	684	94.1	685	93.9	681	94.6	8	682	94.4	685	94.0	682	94.4
429.mcf	8	896	81.4	895	81.5	897	81.4	8	899	81.2	897	81.3	895	81.5
445.gobmk	8	584	144	581	144	582	144	8	537	156	546	154	540	155
456.hmmer	8	626	119	626	119	626	119	8	368	203	369	202	368	203
458.sjeng	8	711	136	708	137	708	137	8	651	149	649	149	651	149
462.libquantum	8	2982	55.6	2973	55.8	2968	55.8	1	94.8	219	94.4	219	94.5	219
464.h264ref	8	751	236	750	236	750	236	8	712	249	713	248	712	249
471.omnetpp	8	817	61.2	816	61.3	813	61.5	8	794	62.9	795	62.9	794	63.0
473.astar	8	731	76.8	734	76.6	731	76.8	8	680	82.6	677	82.9	680	82.6
483.xalancbmk	8	446	124	445	124	445	124	8	446	124	445	124	445	124

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer, for peak, which are compiled in 64-bit mode
OMP_NUM_THREADS set to number of processors
KMP_AFFINITY set to "physical,0"

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3
C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc
401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include
456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include
C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

Continued on next page



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Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-INT-ia32-linux-flags.20090713.00.html>



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You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-INT-ia32-linux-flags.20090713.00.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.1.
Report generated on Tue Jul 22 17:06:54 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 27 May 2008.