



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

SPECint®\_rate2006 = 68.9

Servidor Itaotec MX201 (Intel Xeon E5430)

SPECint\_rate\_base2006 = 57.8

CPU2006 license: 9001

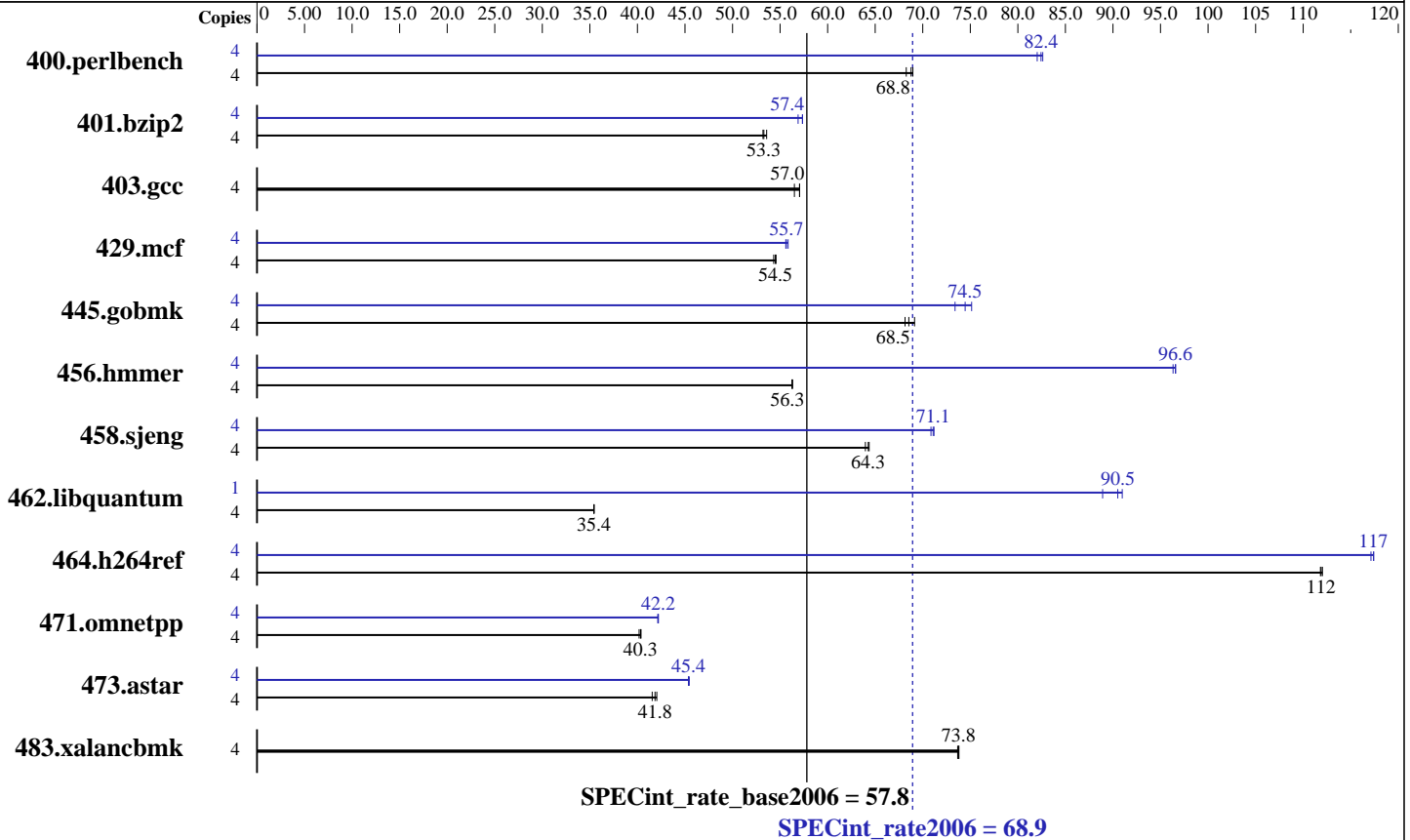
Test date: Apr-2008

Test sponsor: Itaotec

Hardware Availability: Dec-2007

Tested by: Itaotec

Software Availability: Jan-2008



## Hardware

CPU Name: Intel Xeon E5430  
 CPU Characteristics:  
 CPU MHz: 2660  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8 \* 2 GB PC2-5300 FBDIMM, CL-5-5-5, ECC)  
 Disk Subsystem: 1 x SCSI, 73GB, 15000 RPM  
 Other Hardware: None

## Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP1, Kernel 2.6.16.46-0.12-smp  
 Compiler: Intel C++ Compiler for Linux version 10.1 Build 20080112 Package ID: l\_cc\_p\_10.1.012  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Run Level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Binutils 2.17.10.50  
 MicroQuill SmartHeap V8.1



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

SPECint\_rate2006 = 68.9

Servidor Itautec MX201 (Intel Xeon E5430)

SPECint\_rate\_base2006 = 57.8

CPU2006 license: 9001  
Test sponsor: Itautec  
Tested by: Itautec

Test date: Apr-2008  
Hardware Availability: Dec-2007  
Software Availability: Jan-2008

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	<b>568</b>	<b>68.8</b>	567	69.0	572	68.3	4	<b>474</b>	<b>82.4</b>	473	82.6	476	82.0
401.bzip2	4	720	53.6	726	53.2	<b>724</b>	<b>53.3</b>	4	673	57.4	678	56.9	<b>673</b>	<b>57.4</b>
403.gcc	4	<b>564</b>	<b>57.0</b>	564	57.1	570	56.5	4	<b>564</b>	<b>57.0</b>	564	57.1	570	56.5
429.mcf	4	668	54.6	671	54.3	<b>669</b>	<b>54.5</b>	4	653	55.8	<b>655</b>	<b>55.7</b>	656	55.6
445.gobmk	4	607	69.1	616	68.1	<b>612</b>	<b>68.5</b>	4	558	75.1	<b>564</b>	<b>74.5</b>	572	73.4
456.hammer	4	663	56.3	663	56.3	<b>663</b>	<b>56.3</b>	4	<b>386</b>	<b>96.6</b>	386	96.6	387	96.3
458.sjeng	4	752	64.4	<b>753</b>	<b>64.3</b>	757	64.0	4	683	70.9	<b>681</b>	<b>71.1</b>	680	71.2
462.libquantum	4	<b>2340</b>	<b>35.4</b>	2340	35.4	2338	35.5	1	228	91.0	233	88.9	<b>229</b>	<b>90.5</b>
464.h264ref	4	792	112	790	112	<b>791</b>	<b>112</b>	4	756	117	<b>754</b>	<b>117</b>	754	117
471.omnetpp	4	622	40.2	<b>620</b>	<b>40.3</b>	619	40.4	4	<b>593</b>	<b>42.2</b>	593	42.1	592	42.2
473.astar	4	668	42.0	676	41.5	<b>671</b>	<b>41.8</b>	4	618	45.4	619	45.4	<b>619</b>	<b>45.4</b>
483.xalancbmk	4	374	73.8	<b>374</b>	<b>73.8</b>	375	73.7	4	374	73.8	<b>374</b>	<b>73.8</b>	375	73.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Invocation Notes

OMP\_NUM\_THREADS set to number of cores  
KMP\_STACK\_SIZE set to 64M  
KMP\_AFFINITY set to physical,0

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.  
'/usr/bin/taskset' used to bind benchmark copies to processors, except for 462.libquantum at peak.

## Platform Notes

BIOS configuration:  
Hardware Prefetch Enabled

## General Notes

This result was measured on the Servidor Itautec MX201.  
The Servidor Itautec MX201 and the Servidor Itautec MX221 are electronically equivalent.

## Base Compiler Invocation

C benchmarks:  
icc

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

SPECint\_rate2006 = 68.9

Servidor Itaotec MX201 (Intel Xeon E5430)

SPECint\_rate\_base2006 = 57.8

CPU2006 license: 9001  
Test sponsor: Itaotec  
Tested by: Itaotec

Test date: Apr-2008  
Hardware Availability: Dec-2007  
Software Availability: Jan-2008

## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:  
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/opt/sh/SmartHeap\_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc

401.bzip2: /opt/intel/cce/10.1.012/bin/icc  
-L/opt/intel/cce/10.1.012/lib  
-I/opt/intel/cce/10.1.012/include

456.hmmer: /opt/intel/cce/10.1.012/bin/icc  
-L/opt/intel/cce/10.1.012/lib  
-I/opt/intel/cce/10.1.012/include

C++ benchmarks:  
icpc



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

SPECint\_rate2006 = 68.9

Servidor Itautec MX201 (Intel Xeon E5430)

SPECint\_rate\_base2006 = 57.8

CPU2006 license: 9001  
Test sponsor: Itautec  
Tested by: Itautec

Test date: Apr-2008  
Hardware Availability: Dec-2007  
Software Availability: Jan-2008

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch  
401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch  
403.gcc: basepeak = yes  
429.mcf: -fast -prefetch  
445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec-div -ansi-alias  
456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive  
458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4  
462.libquantum: -fast -unroll4 -Ob0 -prefetch  
-opt-streaming-stores always -vec-guard-write  
-opt-malloc-options=3 -parallel -par-runtime-control  
464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs -L/opt/sh/SmartHeap\_8.1/lib -lsmarheap  
473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs -L/opt/sh/SmartHeap\_8.1/lib -lsmarheap  
483.xalancbmk: basepeak = yes



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

SPECint\_rate2006 = 68.9

Servidor Itautec MX201 (Intel Xeon E5430)

SPECint\_rate\_base2006 = 57.8

CPU2006 license: 9001  
Test sponsor: Itautec  
Tested by: Itautec

Test date: Apr-2008  
Hardware Availability: Dec-2007  
Software Availability: Jan-2008

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itautec-ic10.1-INT-intel64-linux-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itautec-ic10.1-INT-intel64-linux-flags.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.0.1.  
Report generated on Tue Jul 22 17:20:35 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 13 May 2008.