



SPEC® CINT2006 Result

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Dell Inc.

SPECint®_rate2006 = 73.7

PowerEdge R200 (Intel Xeon X3360, 2.83 GHz)

SPECint_rate_base2006 = 64.2

CPU2006 license: 55

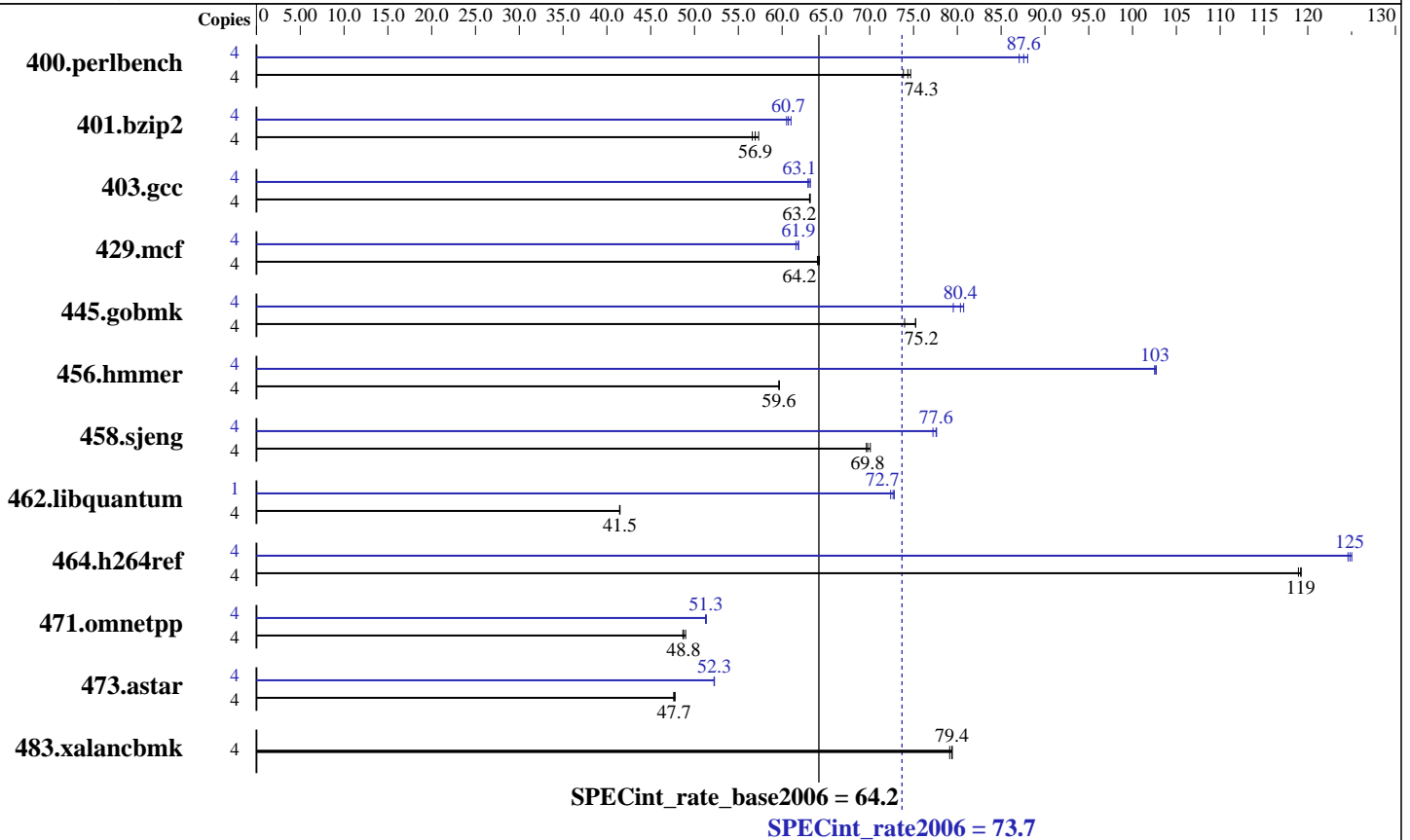
Test date: Mar-2008

Test sponsor: Dell Inc.

Hardware Availability: Mar-2008

Tested by: Dell Inc.

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon X3360
 CPU Characteristics: 1333 MHz system bus
 CPU MHz: 2833
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (4 x 2GB 800MHz DDR2)
 Disk Subsystem: 1 x 750 GB 7200 RPM SATA
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070725
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap library V8.1



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	529	73.8	<u>526</u>	<u>74.3</u>	523	74.7	4	449	87.1	<u>446</u>	<u>87.6</u>	444	88.0
401.bzip2	4	682	56.6	<u>678</u>	<u>56.9</u>	673	57.3	4	<u>636</u>	<u>60.7</u>	638	60.5	633	61.0
403.gcc	4	509	63.2	<u>510</u>	<u>63.2</u>	510	63.1	4	509	63.2	<u>511</u>	<u>63.1</u>	512	62.9
429.mcf	4	570	64.0	568	64.3	<u>569</u>	<u>64.2</u>	4	<u>590</u>	<u>61.9</u>	590	61.9	592	61.6
445.gobmk	4	567	74.0	558	75.2	<u>558</u>	<u>75.2</u>	4	<u>522</u>	<u>80.4</u>	528	79.5	520	80.7
456.hammer	4	626	59.6	<u>626</u>	<u>59.6</u>	625	59.7	4	364	103	363	103	<u>364</u>	<u>103</u>
458.sjeng	4	691	70.1	695	69.6	<u>694</u>	<u>69.8</u>	4	623	77.6	627	77.2	<u>624</u>	<u>77.6</u>
462.libquantum	4	<u>1998</u>	<u>41.5</u>	2000	41.4	1997	41.5	1	285	72.8	286	72.4	<u>285</u>	<u>72.7</u>
464.h264ref	4	744	119	<u>743</u>	<u>119</u>	742	119	4	710	125	708	125	<u>709</u>	<u>125</u>
471.omnetpp	4	514	48.7	510	49.0	<u>513</u>	<u>48.8</u>	4	488	51.3	<u>487</u>	<u>51.3</u>	487	51.3
473.astar	4	590	47.6	588	47.8	<u>589</u>	<u>47.7</u>	4	538	52.2	537	52.3	<u>537</u>	<u>52.3</u>
483.xalancbmk	4	349	79.1	<u>348</u>	<u>79.4</u>	348	79.4	4	349	79.1	<u>348</u>	<u>79.4</u>	348	79.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Invocation Notes

OMP_NUM_THREADS set to number of cores
KMP_STACK_SIZE set to 64M
KMP_AFFINITY set to physical,0

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs

Platform Notes

BIOS Settings:
Adjacent Cache Line Prefetch = Disabled (default Enabled)
Hardware Prefetcher = Disabled (default Enabled)

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

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Peak Portability Flags (Continued)

483.xalanbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalanbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090713.03.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090713.03.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 18:21:53 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 15 April 2008.