



SPEC® CINT2006 Result

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Itautec

SPECint®_rate2006 = 107

Servidor Itautec MX201 (Intel Xeon E5410)

SPECint_rate_base2006 = 95.2

CPU2006 license: 9001

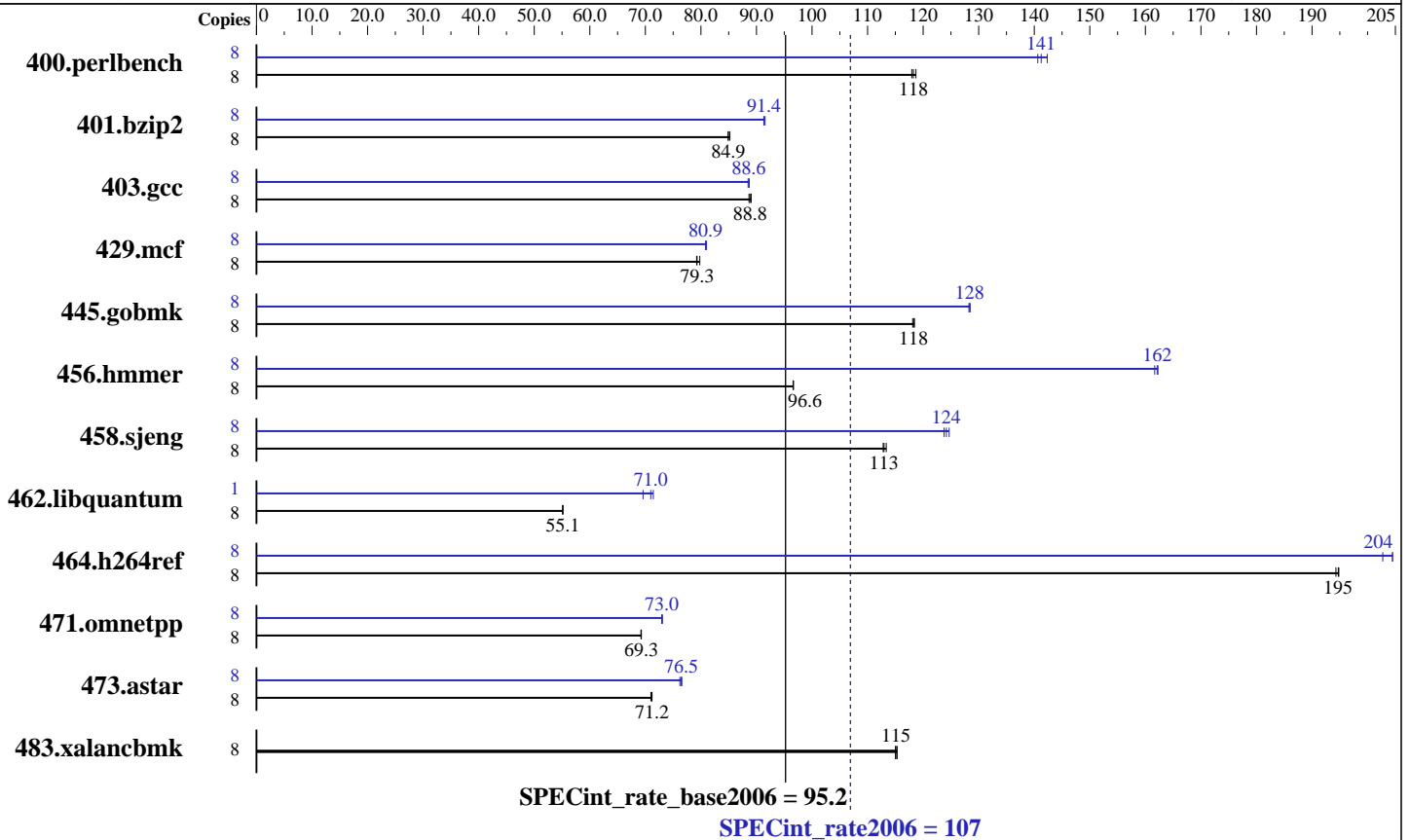
Test sponsor: Itautec

Tested by: Itautec

Test date: Mar-2008

Hardware Availability: Dec-2007

Software Availability: Jan-2008



Hardware

CPU Name: Intel Xeon E5410
 CPU Characteristics:
 CPU MHz: 2330
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 * 2 GB PC2-5300 FBDIMM, CL-5-5-5, ECC)
 Disk Subsystem: 1 x SCSI, 73GB, 15000 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler for Linux version 10.1 Build 20080112 Package ID: l_cc_p_10.1.012
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run Level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Binutils 2.17.10.50
 MicroQuill SmartHeap V8.1



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Results Table

| Benchmark | Base | | | | | | Peak | | | | | | | |
|----------------|--------|------------|-------------|-------------|-------------|------------|-------------|--------|------------|------------|------------|-------------|------------|-------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 8 | 663 | 118 | 661 | 118 | 658 | 119 | 8 | 549 | 142 | 553 | 141 | 556 | 141 |
| 401.bzip2 | 8 | 909 | 84.9 | 909 | 84.9 | 906 | 85.2 | 8 | 843 | 91.5 | 845 | 91.4 | 845 | 91.3 |
| 403.gcc | 8 | 725 | 88.8 | 723 | 89.0 | 726 | 88.7 | 8 | 726 | 88.7 | 728 | 88.5 | 727 | 88.6 |
| 429.mcf | 8 | 914 | 79.8 | 920 | 79.3 | 921 | 79.2 | 8 | 902 | 80.8 | 902 | 80.9 | 901 | 81.0 |
| 445.gobmk | 8 | 709 | 118 | 711 | 118 | 709 | 118 | 8 | 654 | 128 | 654 | 128 | 653 | 128 |
| 456.hammer | 8 | 773 | 96.6 | 772 | 96.6 | 772 | 96.6 | 8 | 460 | 162 | 462 | 162 | 460 | 162 |
| 458.sjeng | 8 | 857 | 113 | 854 | 113 | 858 | 113 | 8 | 782 | 124 | 776 | 125 | 780 | 124 |
| 462.libquantum | 8 | 3006 | 55.1 | 3007 | 55.1 | 3007 | 55.1 | 1 | 298 | 69.6 | 292 | 71.0 | 290 | 71.4 |
| 464.h264ref | 8 | 909 | 195 | 911 | 194 | 909 | 195 | 8 | 866 | 204 | 873 | 203 | 866 | 204 |
| 471.omnetpp | 8 | 722 | 69.3 | 722 | 69.2 | 722 | 69.3 | 8 | 685 | 73.0 | 684 | 73.1 | 685 | 73.0 |
| 473.astar | 8 | 789 | 71.2 | 791 | 71.0 | 789 | 71.2 | 8 | 737 | 76.2 | 734 | 76.5 | 734 | 76.6 |
| 483.xalancbmk | 8 | 480 | 115 | 479 | 115 | 480 | 115 | 8 | 480 | 115 | 479 | 115 | 480 | 115 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Invocation Notes

OMP_NUM_THREADS set to number of cores
KMP_STACK_SIZE set to 64M
KMP_AFFINITY set to physical,0

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
'/usr/bin/taskset' used to bind benchmark copies to processors.

Platform Notes

BIOS configuration:
Hardware Prefetch Disabled

General Notes

This result was measured on the Servidor Itautec MX201.
The Servidor Itautec MX201 and the Servidor Itautec MX221 are electronically equivalent.

Base Compiler Invocation

C benchmarks:
icc

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3
C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/opt/sh/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc
401.bzip2: /opt/intel/cce/10.1.012/bin/icc
-L/opt/intel/cce/10.1.012/lib
-I/opt/intel/cce/10.1.012/include
456.hmmer: /opt/intel/cce/10.1.012/bin/icc
-L/opt/intel/cce/10.1.012/lib
-I/opt/intel/cce/10.1.012/include
C++ benchmarks:
icpc



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Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch
401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
403.gcc: -fast -inline-calloc -opt-malloc-options=3
429.mcf: -fast -prefetch
445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias
456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive
458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4
462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control
464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/opt/sh/SmartHeap_8.1/lib -lsmarheap
473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/opt/sh/SmartHeap_8.1/lib -lsmarheap
483.xalancbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itautec-ic10.1-INT-intel64-linux-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itautec-ic10.1-INT-intel64-linux-flags.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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