SPEC® CINT2006 Result
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Fujitsu Siemens Computers

CELSIUS M460, Intel Core 2 Duo E8500

$SPECint®\_rate2006 =$ 41.9

$SPECint\_rate\_base2006 =$ 38.1
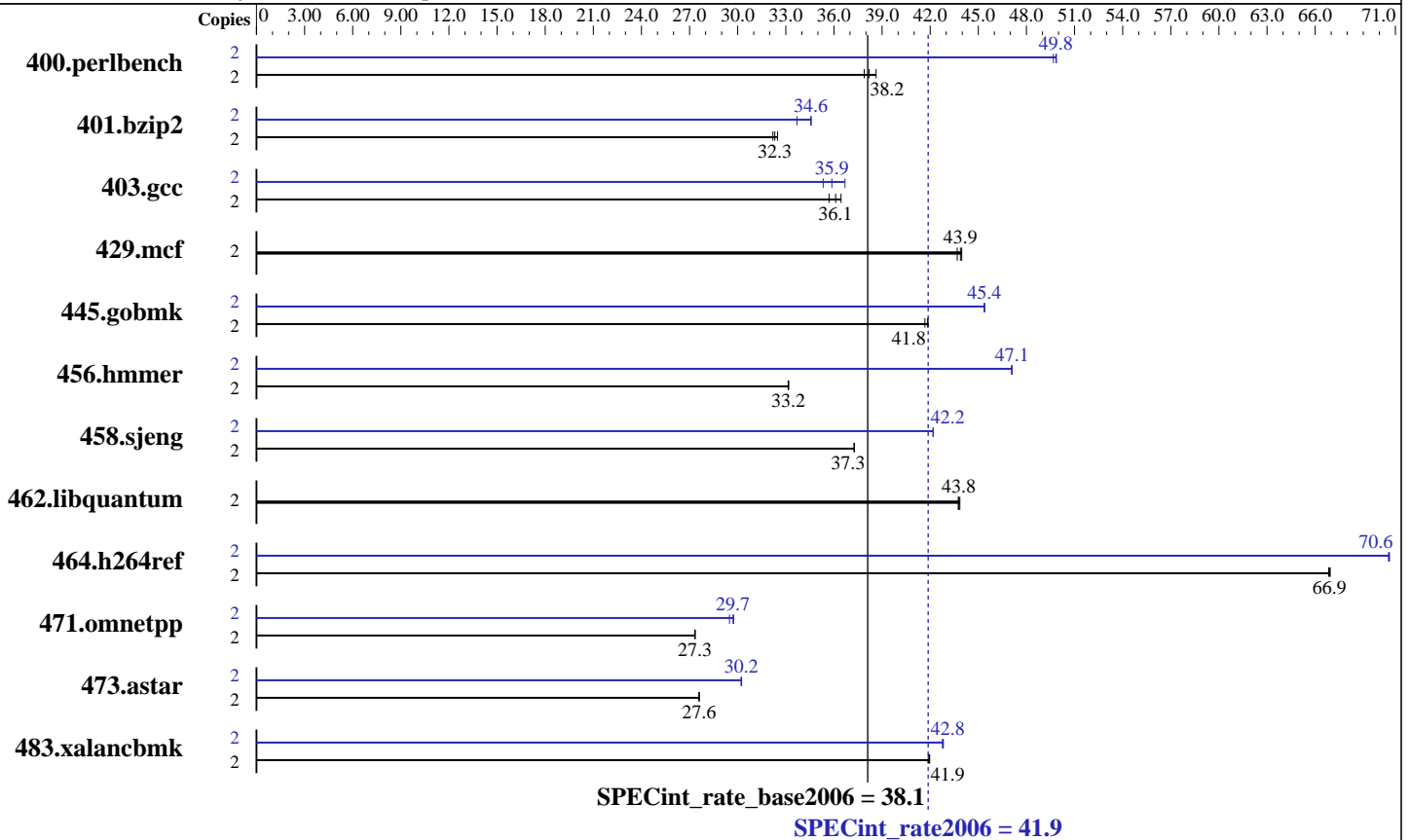
CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: Feb-2008
Hardware Availability: Feb-2008
Software Availability: Nov-2007

Copies 0 3.00 6.00 9.00 12.0 15.0 18.0 21.0 24.0 27.0 30.0 33.0 36.0 39.0 42.0 45.0 48.0 51.0 54.0 57.0 60.0 63.0 66.0 71.0

Benchmark	Copies	Value
400.perlbench	2	49.8
400.perlbench	2	38.2
401.bzip2	2	34.6
401.bzip2	2	32.3
403.gcc	2	35.9
403.gcc	2	36.1
429.mcf	2	43.9
445.gobmk	2	45.4
445.gobmk	2	41.8
456.hmmer	2	47.1
456.hmmer	2	33.2
458.sjeng	2	42.2
458.sjeng	2	37.3
462.libquantum	2	43.8
464.h264ref	2	70.6
464.h264ref	2	66.9
471.omnetpp	2	29.7
471.omnetpp	2	27.3
473.astar	2	30.2
473.astar	2	27.6
483.xalancbmk	2	42.8
483.xalancbmk	2	41.9

SPECint_rate_base2006 = 38.1

SPECint_rate2006 = 41.9

Hardware

CPU Name:	Intel Core 2 Duo E8500
CPU Characteristics:	
CPU MHz:	3167
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	6 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	4 GB (4x1 GB PC2-6400 CL6 SDRAM)
Disk Subsystem:	1 x 400 GB SATA 7200 RPM
Other Hardware:	None

Software

Operating System:	Windows Vista64 Ultimate
Compiler:	Intel C++ Compiler
	for applications running on IA-32,
	Version 10.1, Build 20070913
	Intel C++ Compiler
	for applications running on Intel 64,
	Version 10.1, Build 20070913
	Microsoft Visual Studio 2005 with SP1
	(for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	SmartHeap Library Version 8.1 from
	http://www.microquill.com/

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Results Table

| Benchmark | Base | | | | | | Peak | | | | | | |
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	506	38.6	516	37.9	**512**	**38.2**	2	393	49.7	392	49.9	**392**	**49.8**
401.bzip2	2	**597**	**32.3**	599	32.2	594	32.5	2	573	33.7	558	34.6	**559**	**34.6**
403.gcc	2	442	36.4	**446**	**36.1**	451	35.7	2	439	36.7	456	35.3	**449**	**35.9**
429.mcf	2	415	44.0	418	43.7	**416**	**43.9**	2	415	44.0	418	43.7	**416**	**43.9**
445.gobmk	2	501	41.9	**501**	**41.8**	503	41.7	2	462	45.4	**462**	**45.4**	462	45.4
456.hmmer	2	**563**	**33.2**	563	33.1	562	33.2	2	396	47.1	396	47.1	**396**	**47.1**
458.sjeng	2	650	37.3	649	37.3	**649**	**37.3**	2	574	42.2	574	42.2	**574**	**42.2**
462.libquantum	2	947	43.7	945	43.8	**946**	**43.8**	2	947	43.7	945	43.8	**946**	**43.8**
464.h264ref	2	661	66.9	662	66.8	**662**	**66.9**	2	627	70.6	**627**	**70.6**	627	70.6
471.omnetpp	2	457	27.3	457	27.3	**457**	**27.3**	2	420	29.7	**421**	**29.7**	424	29.5
473.astar	2	509	27.6	**509**	**27.6**	509	27.6	2	465	30.2	464	30.2	**464**	**30.2**
483.xalancbmk	2	329	42.0	**329**	**41.9**	330	41.9	2	**322**	**42.8**	322	42.8	323	42.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

```
BIOS default settings have been used.
```

General Notes

```
All binaries were built with 32-bit Intel compiler except:
401.bzip2 and 456.hmmer in peak were built with 64-bit Intel
compiler by changing the path for include and library files.

For information about Fujitsu Siemens Computers please see:
http://www.fujitsu-siemens.com/
```

Base Compiler Invocation

C benchmarks:
```
icl -Qvc8 -Qc99
```

C++ benchmarks:
```
icl -Qvc8
```

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Base Portability Flags

```
        403.gcc: -DSPEC_CPU_WIN32
     464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
   483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword
```

Base Optimization Flags

C benchmarks:
```
    -fast -Qvec-guard-write -F512000000
```

C++ benchmarks:
```
    -fast -Qcxx_features -F512000000 shlW32M.lib -link -FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
    403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
    icl -Qvc8 -Qc99
```

```
   401.bzip2: C:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Bin\\icl.exe
           -IC:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Include
           -link -LIBPATH:C:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Lib
           -link -LIBPATH:"C:\\Program Files\\Microsoft Visual Studio 8\\vc\\lib"
           -link -LIBPATH:"C:\\Program Files\\Microsoft Visual Studio 8\\vc\\lib\\amd64"

   456.hmmer: C:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Bin\\icl.exe
           -IC:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Include
           -link -LIBPATH:C:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Lib
           -link -LIBPATH:"C:\\Program Files\\Microsoft Visual Studio 8\\vc\\lib"
           -link -LIBPATH:"C:\\Program Files\\Microsoft Visual Studio 8\\vc\\lib\\amd64"
```

C++ benchmarks:
```
    icl -Qvc8
```

Peak Portability Flags

```
   401.bzip2: -DSPEC_CPU_P64
```

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Peak Portability Flags (Continued)

```
       403.gcc: -DSPEC_CPU_WIN32
    456.hmmer: -DSPEC_CPU_P64
    464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
  483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword
```

Peak Optimization Flags

C benchmarks:

```
   400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
                 -Qprefetch -F512000000 shlW32M.lib -link -FORCE:MULTIPLE

      401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch
                 -F512000000

       403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F512000000

       429.mcf: basepeak = yes

     445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo
                 -Qprec-div- -Qansi-alias -F512000000

    456.hmmer: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
                 -Qansi-alias -Qopt-multi-version-aggressive -F512000000

     458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4
                 -F512000000

  462.libquantum: basepeak = yes

    464.h264ref: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
                 -Qansi-alias -F512000000
```

C++ benchmarks:

```
    471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
                 -Qopt-ra-region-strategy=block -Qcxx_features -F512000000
                 shlW32M.lib -link -FORCE:MULTIPLE

      473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
                 -Qopt-ra-region-strategy=routine -Qcxx_features -F512000000
                 shlW32M.lib -link -FORCE:MULTIPLE

  483.xalancbmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
                 -Qcxx_features -F512000000 shlW32M.lib
                 -link -FORCE:MULTIPLE
```

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Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090713.02.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090713.02.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
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