



SPEC® CINT2006 Result

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Itautec

SPECint®_rate2006 = Not Run

Servidor Itautec LX201 (Intel Xeon X5460)

SPECint_rate_base2006 = 57.3

CPU2006 license: 9001

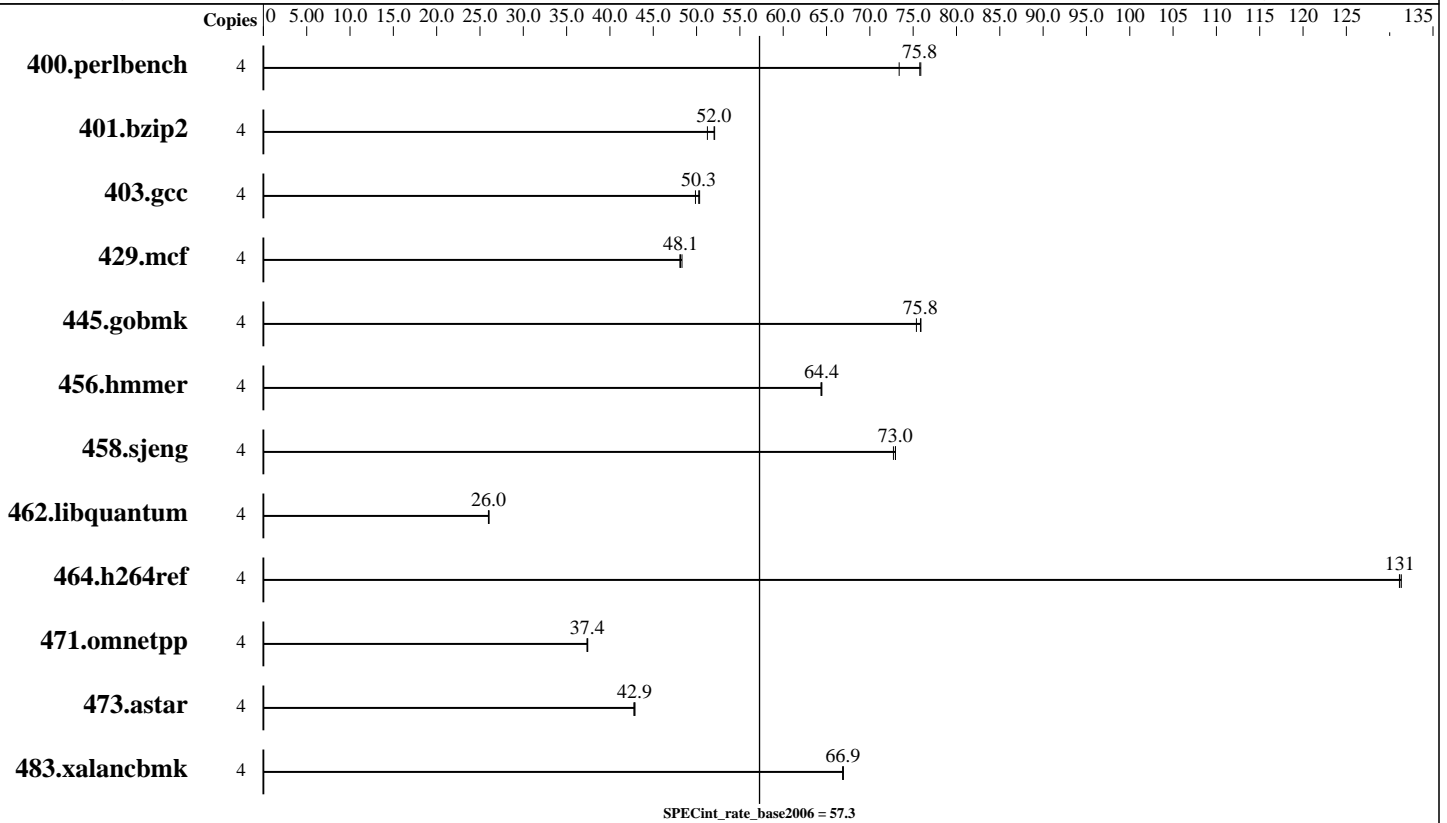
Test sponsor: Itautec

Tested by: Itautec

Test date: Mar-2008

Hardware Availability: Dec-2007

Software Availability: Jan-2008



Hardware

CPU Name: Intel Xeon X5460
 CPU Characteristics:
 CPU MHz: 3160
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 * 2 GB PC2-5300 FBDIMM, CL-5-5-5, ECC)
 Disk Subsystem: 1 x SATA-2, 160GB, 7200 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler for Linux version 10.1 Build 20080112 Package ID: l_cc_p_10.1.012
 Auto Parallel: No
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: Binutils 2.17.10.50
 MicroQuill SmartHeap V8.1



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	533	73.4	<u>516</u>	<u>75.8</u>	515	75.9							
401.bzip2	4	<u>742</u>	<u>52.0</u>	753	51.2	741	52.1							
403.gcc	4	640	50.4	<u>641</u>	<u>50.3</u>	646	49.9							
429.mcf	4	755	48.3	<u>758</u>	<u>48.1</u>	759	48.1							
445.gobmk	4	553	75.9	557	75.4	<u>553</u>	<u>75.8</u>							
456.hammer	4	579	64.5	<u>579</u>	<u>64.4</u>	580	64.4							
458.sjeng	4	<u>663</u>	<u>73.0</u>	663	73.0	666	72.7							
462.libquantum	4	3191	26.0	<u>3186</u>	<u>26.0</u>	3183	26.0							
464.h264ref	4	674	131	675	131	<u>675</u>	<u>131</u>							
471.omnetpp	4	668	37.4	669	37.4	<u>668</u>	<u>37.4</u>							
473.astar	4	655	42.9	657	42.8	<u>655</u>	<u>42.9</u>							
483.xalancbmk	4	412	66.9	<u>413</u>	<u>66.9</u>	413	66.9							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
'/usr/bin/taskset' used to bind benchmark copies to processors.

Platform Notes

BIOS configuration:
Hardware Prefetch Disabled

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/opt/sh/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itautec-ic10.1-FP-intel64-linux-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itautec-ic10.1-FP-intel64-linux-flags.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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