SPEC® CINT2006 Result
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Intel Corporation

Intel Desktop Board DQ35JO (Intel Core 2 Duo E8200)

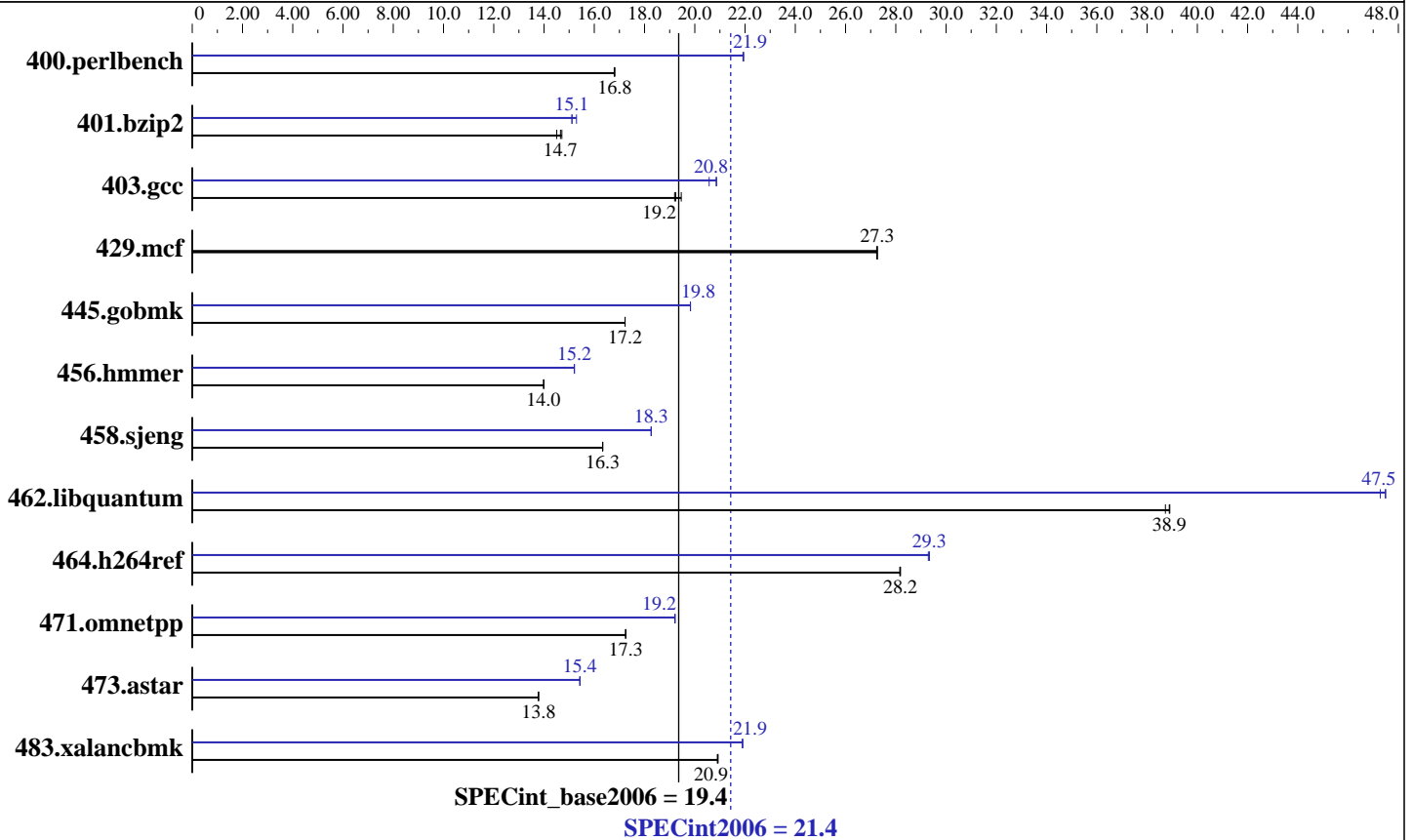SPECint®2006 = 21.4

SPECint_base2006 = 19.4

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Feb-2008
Hardware Availability: Feb-2008
Software Availability: Nov-2007

Benchmark	Peak	Base
400.perlbench	21.9	16.8
401.bzip2	15.1	14.7
403.gcc	20.8	19.2
429.mcf		27.3
445.gobmk	19.8	17.2
456.hmmer	15.2	14.0
458.sjeng	18.3	16.3
462.libquantum	47.5	38.9
464.h264ref	29.3	28.2
471.omnetpp	19.2	17.3
473.astar	15.4	13.8
483.xalancbmk	21.9	20.9

SPECint_base2006 = 19.4
SPECint2006 = 21.4

Hardware

CPU Name:	Intel Core 2 Duo E8200
CPU Characteristics:	2.66 GHz, 1333 FSB
CPU MHz:	2667
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	6 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	2 GB (2x1GB Micron DDR2-800 CL5)
Disk Subsystem:	Seagate 320GB NCQ SATA, 16MB cache, 7200 RPM
Other Hardware:	None

Software

Operating System:	Windows Vista Ultimate (64-bit)
Compiler:	Intel C++ Compiler for IA32 version 10.1 Build 20070913 Package ID: w_cc_p_10.1.011 Microsoft Visual Studio 2005 SP1 (for libraries)
Auto Parallel:	Yes
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	SmartHeap Library Version 8.1 from http://www.microquill.com/

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	582	16.8	**581**	**16.8**	581	16.8	**446**	**21.9**	445	21.9	446	21.9
401.bzip2	**659**	**14.7**	665	14.5	656	14.7	**639**	**15.1**	639	15.1	631	15.3
403.gcc	419	19.2	**418**	**19.2**	414	19.5	**386**	**20.8**	386	20.9	391	20.6
429.mcf	334	27.3	**335**	**27.3**	335	27.2	334	27.3	**335**	**27.3**	335	27.2
445.gobmk	609	17.2	**609**	**17.2**	609	17.2	**529**	**19.8**	529	19.8	529	19.8
456.hmmer	**667**	**14.0**	667	14.0	667	14.0	614	15.2	613	15.2	**613**	**15.2**
458.sjeng	741	16.3	**741**	**16.3**	741	16.3	663	18.3	662	18.3	**662**	**18.3**
462.libquantum	**533**	**38.9**	535	38.7	533	38.9	438	47.3	436	47.5	**436**	**47.5**
464.h264ref	786	28.2	**785**	**28.2**	785	28.2	755	29.3	754	29.3	**755**	**29.3**
471.omnetpp	362	17.3	**362**	**17.3**	362	17.2	325	19.2	325	19.2	**325**	**19.2**
473.astar	**509**	**13.8**	509	13.8	510	13.8	**455**	**15.4**	455	15.4	455	15.4
483.xalancbmk	**330**	**20.9**	330	20.9	330	20.9	315	21.9	315	21.9	**315**	**21.9**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Tested systems can be used with Shin-G ATX case, Antec NeoPower 480W power supply
Product description located as of 03/2008:
http://www.intel.com/products/motherboard/DQ35JO/index.htm
The system bus runs at 1333 MHz
System was configured with Asus EN8800GTX discrete graphics card
Binaries were built on Windows Vista Ultimate (32-bit)
The following VS 2005 SP1 updates were applied: KB926601 and KB932232
```

Base Compiler Invocation

C benchmarks:
```
icl -Qvc8 -Qc99
```

C++ benchmarks:
```
icl -Qvc8
```

Base Portability Flags

```
    403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword
```

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Base Optimization Flags

C benchmarks:

```
-fast -Qparallel -Qpar-runtime-control -Qvec-guard-write /F512000000
libguide40.lib
```

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib libguide40.lib
          -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks:

```
icl -Qvc8 -Qc99
```

C++ benchmarks:

```
icl -Qvc8
```

Peak Portability Flags

```
     403.gcc: -DSPEC_CPU_WIN32
  464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
               -Qprefetch -Qparallel -Qpar-runtime-control /F512000000
               shlw32m.lib libguide40.lib          -link /FORCE:MULTIPLE

    401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch
               /F512000000 libguide40.lib

      403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
               libguide40.lib
```

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Peak Optimization Flags (Continued)

```
   429.mcf: basepeak = yes

 445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo
            -Qprec-div- -Qansi-alias /F512000000

 456.hmmer: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
            -Qansi-alias -Qopt-multi-version-aggressive /F512000000
            libguide40.lib

 458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4
            /F512000000 libguide40.lib

462.libquantum: -fast -Qunroll4 -Ob0 -Qprefetch
            -Qopt-streaming-stores:always -Qparallel
            -Qpar-runtime-control /F512000000 libguide40.lib

464.h264ref: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
            -Qansi-alias /F512000000 libguide40.lib
```

C++ benchmarks:

```
471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
            -Qopt-ra-region-strategy=block -Qcxx_features /F512000000
            shlw32m.lib libguide40.lib          -link /FORCE:MULTIPLE

  473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
            -Qopt-ra-region-strategy=routine -Qcxx_features /F512000000
            shlw32m.lib libguide40.lib          -link /FORCE:MULTIPLE

483.xalancbmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
            -Qcxx_features /F512000000 shlw32m.lib libguide40.lib
                      -link /FORCE:MULTIPLE
```

Peak Other Flags

C benchmarks:

```
   403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10.1-win32-flags.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10.1-win32-flags.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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