



SPEC® CINT2006 Result

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Intel Corporation

Intel Desktop Board DQ35JO (Intel Core 2 Duo E8400)

SPECint®_rate2006 = 40.0

SPECint_rate_base2006 = 36.9

CPU2006 license: 13

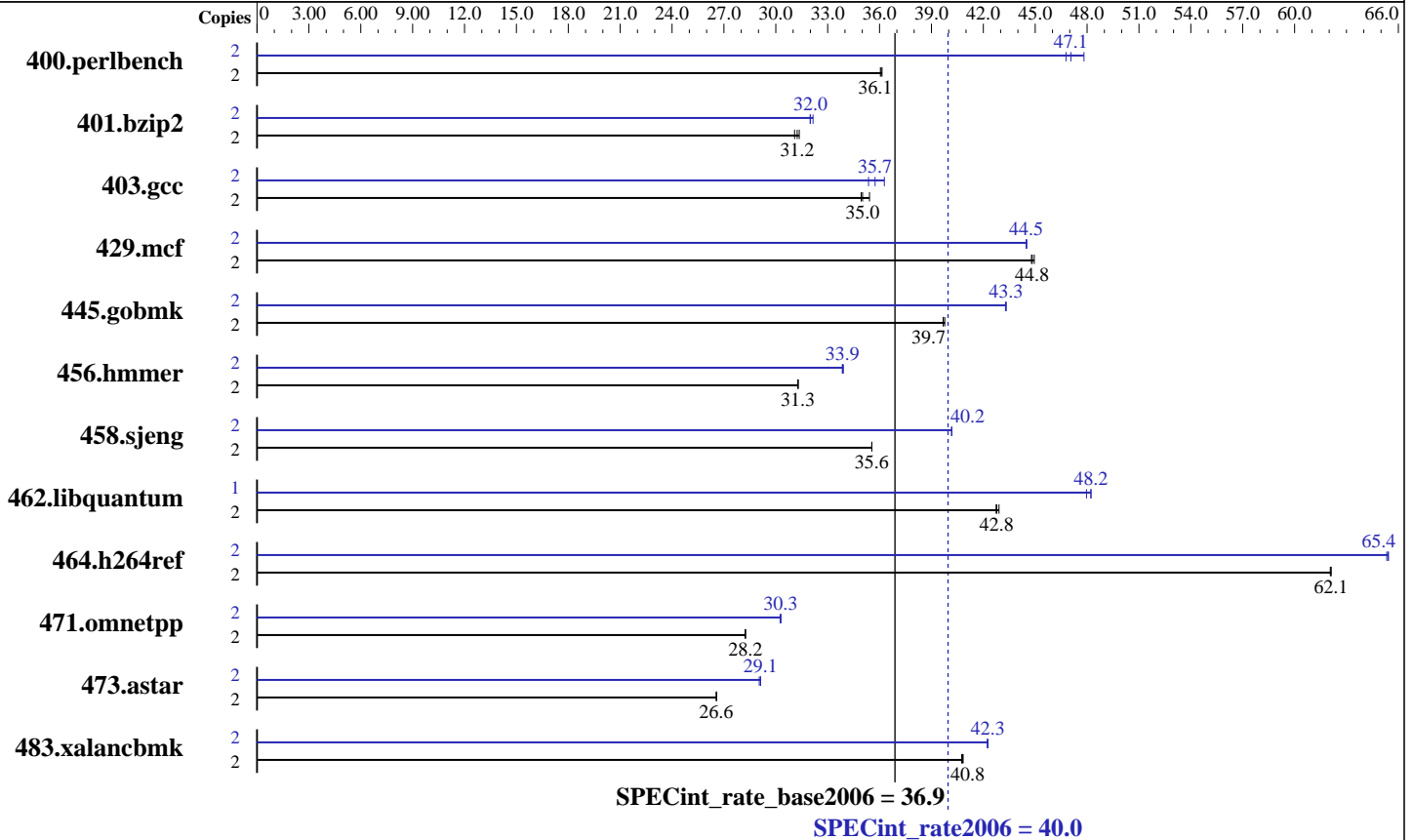
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Feb-2008

Hardware Availability: Feb-2008

Software Availability: Nov-2007



Hardware

CPU Name: Intel Core 2 Duo E8400
 CPU Characteristics: 3.00 GHz, 1333 FSB
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 2 GB (2x1GB Micron DDR2-800 CL5)
 Disk Subsystem: Seagate 320GB NCQ SATA, 16MB cache, 7200 RPM
 Other Hardware: None

Software

Operating System: Windows Vista Ultimate (64-bit)
 Compiler: Intel C++ Compiler for IA32 version 10.1
 Build 20070913 Package ID: w_cc_p_10.1.011
 Microsoft Visual Studio 2005 SP1 (for libraries)
 Auto Parallel: Yes
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: SmartHeap Library Version 8.1 from
<http://www.microquill.com/>



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	541	36.1	542	36.0	<u>541</u>	<u>36.1</u>	2	418	46.8	<u>415</u>	<u>47.1</u>	409	47.8
401.bzip2	2	621	31.1	616	31.4	<u>618</u>	<u>31.2</u>	2	600	32.2	603	32.0	<u>603</u>	<u>32.0</u>
403.gcc	2	461	34.9	<u>460</u>	<u>35.0</u>	455	35.4	2	<u>450</u>	<u>35.7</u>	455	35.4	444	36.3
429.mcf	2	406	44.9	407	44.8	<u>407</u>	<u>44.8</u>	2	410	44.5	<u>410</u>	<u>44.5</u>	410	44.5
445.gobmk	2	528	39.8	529	39.7	<u>529</u>	<u>39.7</u>	2	485	43.3	484	43.3	<u>484</u>	<u>43.3</u>
456.hammer	2	<u>597</u>	<u>31.3</u>	597	31.3	596	31.3	2	550	33.9	<u>551</u>	<u>33.9</u>	551	33.8
458.sjeng	2	681	35.6	<u>681</u>	<u>35.6</u>	681	35.6	2	602	40.2	602	40.2	<u>602</u>	<u>40.2</u>
462.libquantum	2	966	42.9	<u>969</u>	<u>42.8</u>	970	42.7	1	<u>430</u>	<u>48.2</u>	432	48.0	430	48.2
464.h264ref	2	713	62.1	<u>713</u>	<u>62.1</u>	713	62.1	2	678	65.3	<u>677</u>	<u>65.4</u>	676	65.4
471.omnetpp	2	<u>443</u>	<u>28.2</u>	443	28.2	443	28.2	2	<u>413</u>	<u>30.3</u>	413	30.2	413	30.3
473.astar	2	<u>529</u>	<u>26.6</u>	529	26.6	528	26.6	2	<u>482</u>	<u>29.1</u>	483	29.0	482	29.1
483.xalancbmk	2	338	40.8	339	40.7	<u>338</u>	<u>40.8</u>	2	327	42.2	<u>327</u>	<u>42.3</u>	327	42.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Tested systems can be used with Shin-G ATX case, Antec NeoPower 480W power supply
Product description located as of 03/2008:

<http://www.intel.com/products/motherboard/DQ35JO/index.htm>

The system bus runs at 1333 MHz

System was configured with Asus EN8800GTX discrete graphics card

Binaries were built on Windows Vista Ultimate (32-bit)

The following VS 2005 SP1 updates were applied: KB926601 and KB932232

OMP_NUM_THREADS was set to number of logical processors as seen by the OS

KMP_AFFINITY was set to physical,0

submit was disabled for 462.libquantum at peak.

Base Compiler Invocation

C benchmarks:

```
icl -Qvc8 -Qc99
```

C++ benchmarks:

```
icl -Qvc8
```

Base Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
```

```
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

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Base Portability Flags (Continued)

483.xalanbmk: -Qoption,cpp,--no_wchar_t_keyword

Base Optimization Flags

C benchmarks:

-fast -Qvec-guard-write /F512000000

C++ benchmarks:

-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:

icl -Qvc8 -Qc99

C++ benchmarks:

icl -Qvc8

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalanbmk: -Qoption,cpp,--no_wchar_t_keyword

Peak Optimization Flags

C benchmarks:

400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qprefetch /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

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Peak Optimization Flags (Continued)

401.bzp2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch /F512000000

403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000

429.mcf: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo -Qprec-div- -Qansi-alias /F512000000

456.hmmer: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2 -Qansi-alias -Qopt-multi-version-aggressive /F512000000

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4 /F512000000

462.libquantum: -fast -Qunroll14 -Ob0 -Qprefetch -Qopt-streaming-stores:always -Qparallel -Qpar-runtime-control /F512000000

464.h264ref: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll12 -Qansi-alias /F512000000

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias -Qopt-ra-region-strategy=block -Qcxx_features /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias -Qopt-ra-region-strategy=routine -Qcxx_features /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

483.xalancbmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias -Qcxx_features /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-win32-flags.html>



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You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-win32-flags.xml>

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For other inquiries, please contact webmaster@spec.org.

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