



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

Express5800/120Lj
(Intel Xeon E5205)

SPECint®2006 = 16.6

SPECint_base2006 = 14.0

CPU2006 license: 9006

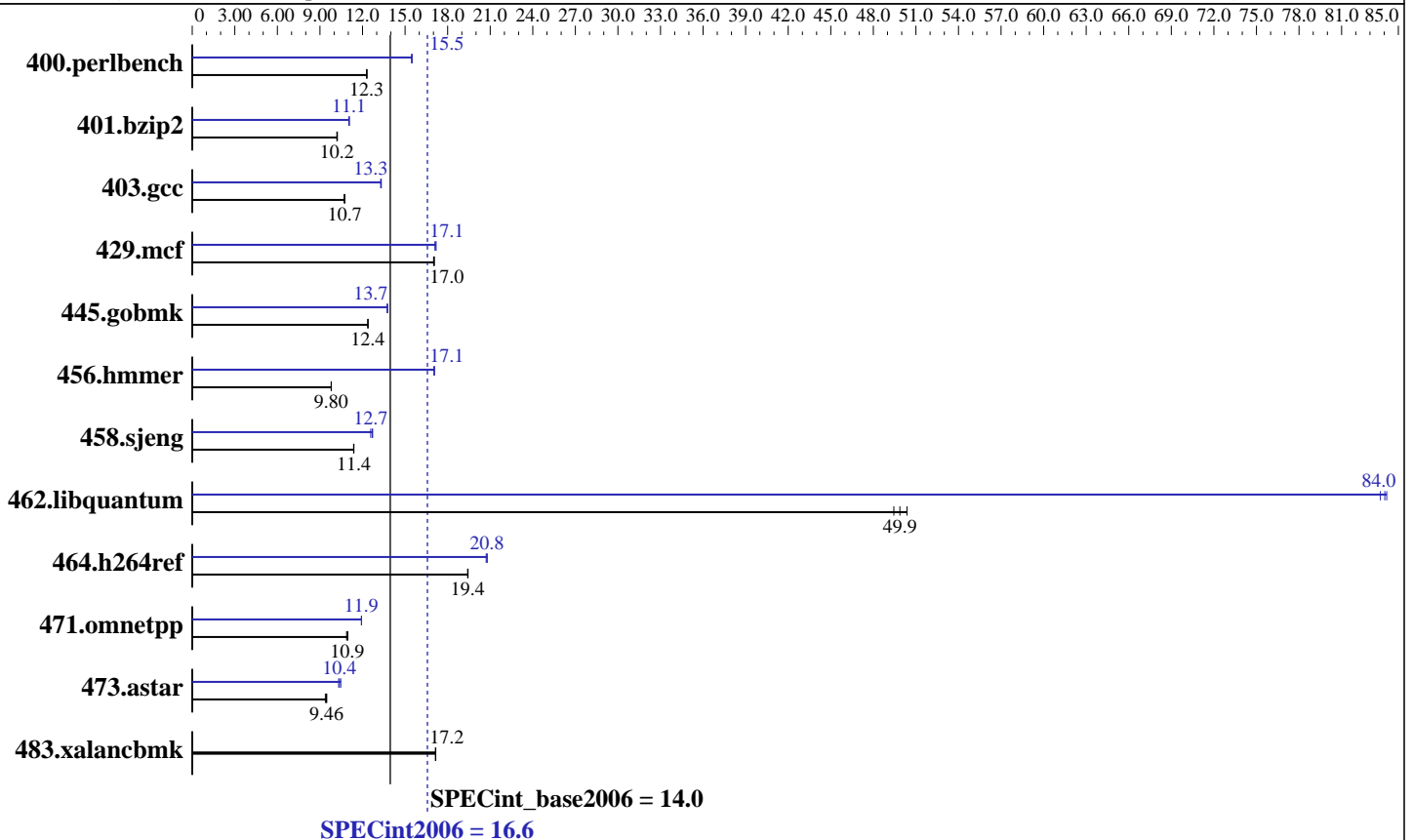
Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Feb-2008

Hardware Availability: Feb-2008

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E5205
 CPU Characteristics: 1.86 GHz, 6 MB L2, 1066 MHz bus
 CPU MHz: 1867
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 12 GB (12x1 GB PC2-5300F, 2 rank, CL5-5-5, ECC)
 Disk Subsystem: 1x73.2 GB SAS, 15000RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070913 Package ID: L_cc_p_10.1.008
 Auto Parallel: Yes
 File System: ext2
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap library 8.1 binutils-2.17.tar.gz, Version 2.17



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	795	12.3	793	12.3	<u>795</u>	<u>12.3</u>	632	15.5	<u>631</u>	<u>15.5</u>	630	15.5
401.bzip2	946	10.2	943	10.2	<u>946</u>	<u>10.2</u>	<u>873</u>	<u>11.1</u>	872	11.1	875	11.0
403.gcc	<u>749</u>	<u>10.7</u>	749	10.7	752	10.7	606	13.3	<u>605</u>	<u>13.3</u>	604	13.3
429.mcf	535	17.1	<u>535</u>	<u>17.0</u>	536	17.0	533	17.1	<u>533</u>	<u>17.1</u>	531	17.2
445.gobmk	847	12.4	<u>847</u>	<u>12.4</u>	847	12.4	762	13.8	<u>763</u>	<u>13.7</u>	764	13.7
456.hmmer	952	9.80	<u>952</u>	<u>9.80</u>	953	9.79	548	17.0	547	17.1	<u>547</u>	<u>17.1</u>
458.sjeng	1062	11.4	1064	11.4	<u>1063</u>	<u>11.4</u>	962	12.6	952	12.7	<u>954</u>	<u>12.7</u>
462.libquantum	419	49.4	411	50.4	<u>415</u>	<u>49.9</u>	247	83.7	246	84.2	<u>247</u>	<u>84.0</u>
464.h264ref	<u>1140</u>	<u>19.4</u>	1138	19.4	1140	19.4	1065	20.8	<u>1065</u>	<u>20.8</u>	1068	20.7
471.omnetpp	570	11.0	<u>571</u>	<u>10.9</u>	575	10.9	524	11.9	<u>524</u>	<u>11.9</u>	524	11.9
473.astar	748	9.38	741	9.48	<u>742</u>	<u>9.46</u>	670	10.5	680	10.3	<u>676</u>	<u>10.4</u>
483.xalancbmk	<u>402</u>	<u>17.2</u>	403	17.1	402	17.2	<u>402</u>	<u>17.2</u>	403	17.1	402	17.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores

Platform Notes

Bios settings:
Intel SpeedStep Technology: Disabled

General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer, for peak, are compiled in 64-bit mode

The NEC Express5800/120Lj(Intel Xeon E5205) and the Bull NovaScale T860 E1(Intel Xeon E5205,1.86GHz) models are electronically equivalent. The results have been measured on a NEC Express5800/120Lj(Intel Xeon E5205) model.

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -vec-guard-write -parallel -par-runtime-control

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/opt/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

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Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
-auto-ilp32

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmr: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive
-auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap

483.xalancbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-INT-ia32-linux-flags.20090713.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-INT-ia32-linux-flags.20090713.xml>

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For other inquiries, please contact webmaster@spec.org.

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