



SPEC® CINT2006 Result

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NEC Corporation

SPECint®2006 = 27.0

T120Rc-1
(Intel Xeon X5260)

SPECint_base2006 = 22.6

CPU2006 license: 9006

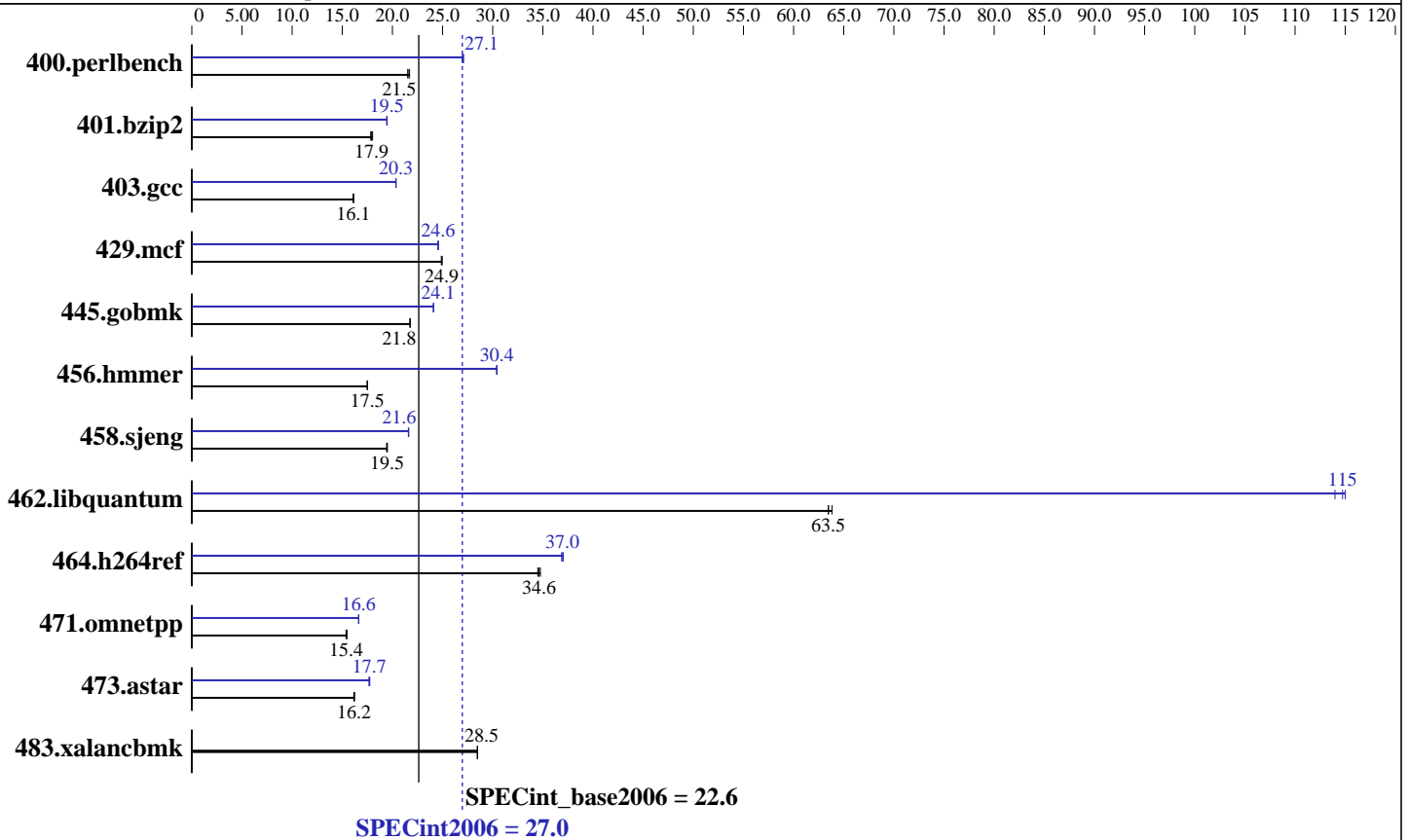
Test date: Feb-2008

Test sponsor: NEC Corporation

Hardware Availability: Jan-2008

Tested by: NEC Corporation

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon X5260
 CPU Characteristics: 3.33 GHz, 6 MB L2 , 1333 MHz bus
 CPU MHz: 3333
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 12 GB (12x1 GB PC2-5300F, 2 rank, CL5-5-5, ECC)
 Disk Subsystem: 1x250 GB SATA, 7200RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070913 Package ID: I_cc_p_10.1.008
 Auto Parallel: Yes
 File System: ext2
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap library 8.1 binutils-2.17.tar.gz, Version 2.17



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	450	21.7	<u>453</u>	<u>21.5</u>	454	21.5	361	27.1	362	27.0	<u>361</u>	<u>27.1</u>
401.bzip2	541	17.8	535	18.0	<u>538</u>	<u>17.9</u>	495	19.5	<u>496</u>	<u>19.5</u>	497	19.4
403.gcc	<u>499</u>	<u>16.1</u>	501	16.1	499	16.1	<u>396</u>	<u>20.3</u>	395	20.4	396	20.3
429.mcf	365	25.0	367	24.9	<u>366</u>	<u>24.9</u>	<u>371</u>	<u>24.6</u>	371	24.6	372	24.5
445.gobmk	<u>482</u>	<u>21.8</u>	482	21.8	482	21.7	436	24.1	<u>435</u>	<u>24.1</u>	435	24.1
456.hmmmer	<u>533</u>	<u>17.5</u>	533	17.5	534	17.5	<u>307</u>	<u>30.4</u>	307	30.4	307	30.4
458.sjeng	621	19.5	<u>621</u>	<u>19.5</u>	623	19.4	<u>560</u>	<u>21.6</u>	560	21.6	560	21.6
462.libquantum	326	63.5	325	63.8	<u>326</u>	<u>63.5</u>	<u>181</u>	<u>115</u>	180	115	182	114
464.h264ref	642	34.5	<u>639</u>	<u>34.6</u>	637	34.8	597	37.0	<u>599</u>	<u>37.0</u>	600	36.9
471.omnetpp	404	15.5	407	15.4	<u>406</u>	<u>15.4</u>	375	16.6	<u>376</u>	<u>16.6</u>	376	16.6
473.astar	<u>434</u>	<u>16.2</u>	433	16.2	435	16.2	397	17.7	<u>397</u>	<u>17.7</u>	396	17.7
483.xalancbmk	242	28.5	<u>242</u>	<u>28.5</u>	242	28.5	242	28.5	<u>242</u>	<u>28.5</u>	242	28.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores

Platform Notes

Bios settings:
Intel SpeedStep Technology: Disabled

General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmmer,
for peak, are compiled in 64-bit mode

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -vec-guard-write -parallel -par-runtime-control

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/opt/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

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Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
-auto-ilp32

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive
-auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap

483.xalancbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-INT-ia32-linux-flags.20090713.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-INT-ia32-linux-flags.20090713.xml>

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For other inquiries, please contact webmaster@spec.org.

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