



SPEC® CINT2006 Result

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Fujitsu Siemens Computers

SPECint®_rate2006 = 81.1

CELSIUS R650, Intel Xeon X5260, 3.33 GHz

SPECint_rate_base2006 = 69.6

CPU2006 license: 22

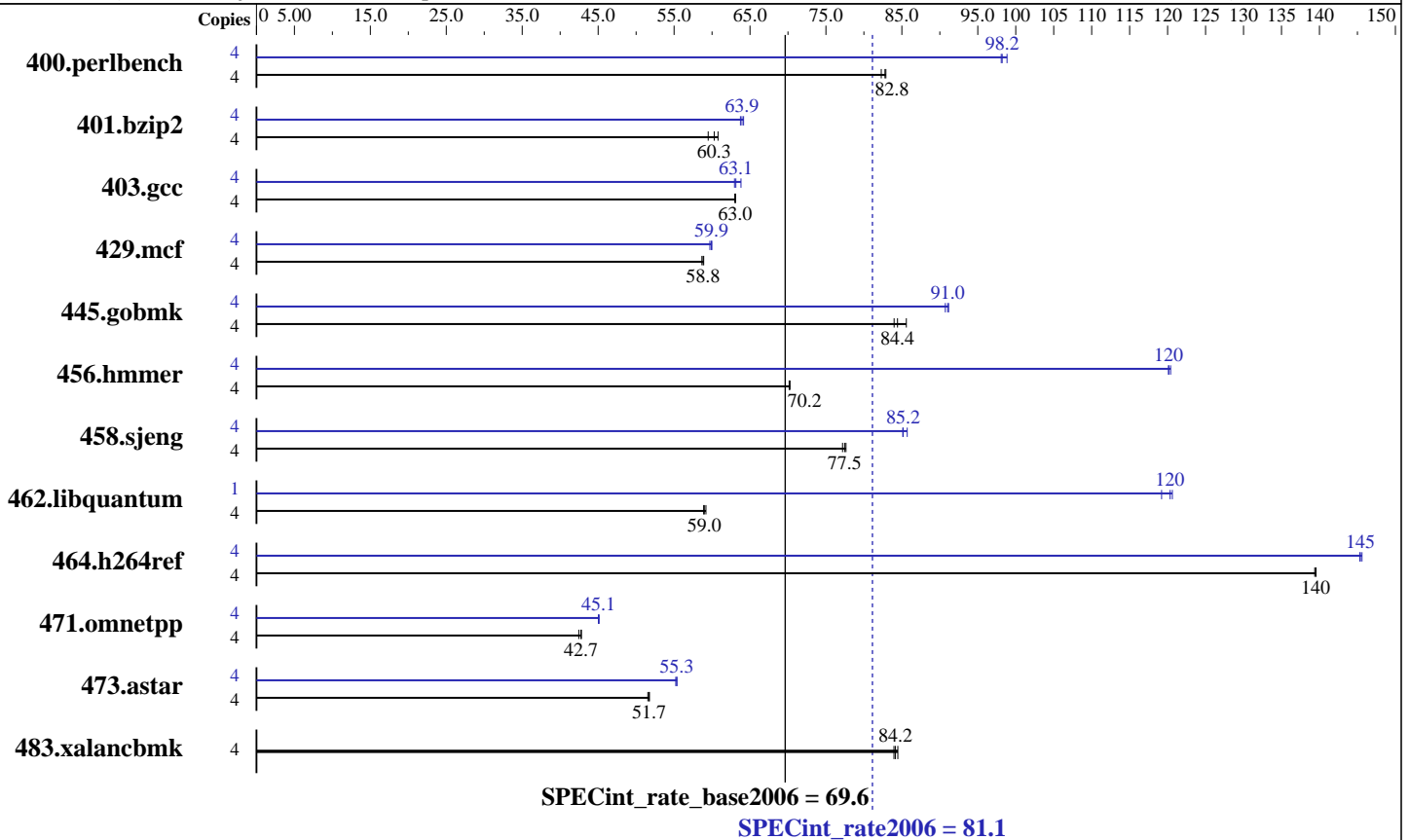
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Feb-2008

Hardware Availability: Jan-2008

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon X5260
 CPU Characteristics: 3333
 CPU MHz: 3333
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (8x1 GB PC2-5300F, 2 rank, CL5-5-5, ECC)
 Disk Subsystem: 1 x SATA II, 400 GB, 7200 rpm
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler for Linux32 and Linux64, Version 10.1, Build 20070913
 Auto Parallel: Yes
 File System: ext3
 System State: Multi-User, Run Level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap Library, Version 8.1
 binutils-2.17.50.0.5-0.1.x86_64



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	471	82.9	475	82.3	<u>472</u>	<u>82.8</u>	4	<u>398</u>	<u>98.2</u>	398	98.1	395	98.9
401.bzip2	4	649	59.5	<u>640</u>	<u>60.3</u>	635	60.8	4	606	63.7	602	64.2	<u>604</u>	<u>63.9</u>
403.gcc	4	<u>511</u>	<u>63.0</u>	511	63.0	510	63.1	4	505	63.8	511	63.0	<u>510</u>	<u>63.1</u>
429.mcf	4	<u>620</u>	<u>58.8</u>	619	58.9	622	58.7	4	611	59.7	<u>609</u>	<u>59.9</u>	608	60.0
445.gobmk	4	<u>497</u>	<u>84.4</u>	490	85.6	500	84.0	4	460	91.2	<u>461</u>	<u>91.0</u>	463	90.7
456.hmmer	4	532	70.2	<u>532</u>	<u>70.2</u>	531	70.3	4	311	120	310	120	<u>311</u>	<u>120</u>
458.sjeng	4	<u>625</u>	<u>77.5</u>	627	77.2	623	77.6	4	565	85.7	569	85.1	<u>568</u>	<u>85.2</u>
462.libquantum	4	1400	59.2	<u>1405</u>	<u>59.0</u>	1407	58.9	1	172	121	<u>172</u>	<u>120</u>	174	119
464.h264ref	4	<u>635</u>	<u>140</u>	635	139	634	140	4	<u>609</u>	<u>145</u>	609	145	608	146
471.omnetpp	4	589	42.5	584	42.8	<u>585</u>	<u>42.7</u>	4	554	45.1	<u>555</u>	<u>45.1</u>	555	45.0
473.astar	4	<u>543</u>	<u>51.7</u>	545	51.5	543	51.7	4	<u>507</u>	<u>55.3</u>	507	55.4	508	55.2
483.xalancbmk	4	329	84.0	<u>328</u>	<u>84.2</u>	327	84.5	4	329	84.0	<u>328</u>	<u>84.2</u>	327	84.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores (default)

Platform Notes

BIOS configuration:
Enhanced Speedstep Technology = Disable
Hardware Prefetch = Enable, Adjacent Sector Prefetch = Enable
SnoopFilter = Enable

General Notes

All binaries were built with 32-bit Intel compiler except:
401.bzip2 and 456.hmmer in peak were built with 64-bit Intel compiler by changing the path for include and library files.

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Base Compiler Invocation

C benchmarks:
icc

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3
C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/opt/SmartHeap_8.1/lib -lsmarheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc
401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include
456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include
C++ benchmarks:
icpc



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Peak Portability Flags

```

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

```

Peak Optimization Flags

C benchmarks:

```

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
               -prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
           -no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
                -opt-streaming-stores always -vec-guard-write
                -opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
             -ansi-alias

```

C++ benchmarks:

```

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
             -no-prec-div -ansi-alias -opt-ra-region-strategy=block
             -Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
           -no-prec-div -ansi-alias -opt-ra-region-strategy=routine
           -Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmarheap

483.xalancbmk: basepeak = yes

```



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.21.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.21.xml>

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For other inquiries, please contact webmaster@spec.org.

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