



# SPEC® CINT2006 Result

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IBM Corporation

SPECint®\_rate2006 = 90.6

IBM System p 520 (4.2 GHz, 4 core)

SPECint\_rate\_base2006 = 82.3

CPU2006 license: 11

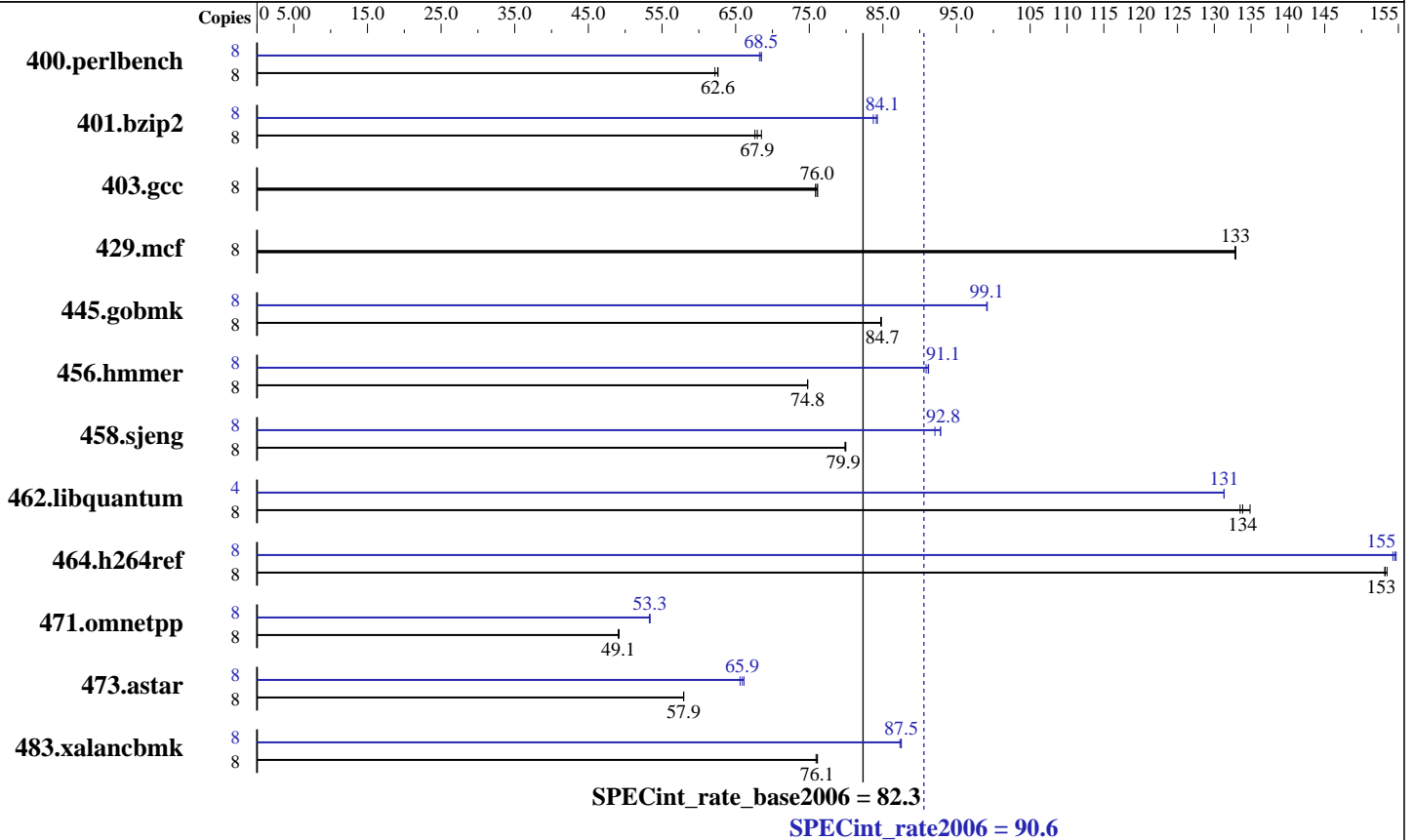
Test date: Jan-2008

Test sponsor: IBM Corporation

Hardware Availability: Feb-2008

Tested by: IBM Corporation

Software Availability: Feb-2008



## Hardware

CPU Name: POWER6  
 CPU Characteristics:  
 CPU MHz: 4200  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2,4 cores  
 Primary Cache: 64 KB I + 64 KB D on chip per core  
 Secondary Cache: 4 MB I+D on chip per core  
 L3 Cache: None  
 Other Cache: None  
 Memory: 32 GB (8x4 GB) DDR2 667 MHz  
 Disk Subsystem: 1x73 GB 1x146 GB SAS 15K RPM  
 Other Hardware: None

## Software

Operating System: IBM AIX V6.1 Updated to SP3  
 Compiler: XL C/C++ Enterprise Edition V9 for AIX Updated with the Oct2007 PTF.  
 Auto Parallel: No  
 File System: AIX/JFS2  
 System State: Multi-user  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: --



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	1257	62.2	<b><u>1249</u></b>	<b><u>62.6</u></b>	1249	62.6	8	1145	68.2	<b><u>1142</u></b>	<b><u>68.5</u></b>	1141	68.5
401.bzip2	8	1142	67.6	1127	68.5	<b><u>1136</u></b>	<b><u>67.9</u></b>	8	916	84.2	922	83.7	<b><u>918</u></b>	<b><u>84.1</u></b>
403.gcc	8	849	75.8	846	76.1	<b><u>847</u></b>	<b><u>76.0</u></b>	8	849	75.8	846	76.1	<b><u>847</u></b>	<b><u>76.0</u></b>
429.mcf	8	549	133	549	133	<b><u>549</u></b>	<b><u>133</u></b>	8	549	133	549	133	<b><u>549</u></b>	<b><u>133</u></b>
445.gobmk	8	990	84.8	990	84.7	<b><u>990</u></b>	<b><u>84.7</u></b>	8	847	99.1	<b><u>847</u></b>	<b><u>99.1</u></b>	846	99.2
456.hmmmer	8	998	74.8	<b><u>998</u></b>	<b><u>74.8</u></b>	998	74.8	8	821	90.9	<b><u>819</u></b>	<b><u>91.1</u></b>	818	91.2
458.sjeng	8	1211	80.0	<b><u>1211</u></b>	<b><u>79.9</u></b>	1212	79.8	8	1051	92.1	1043	92.8	<b><u>1043</u></b>	<b><u>92.8</u></b>
462.libquantum	8	<b><u>1238</u></b>	<b><u>134</u></b>	1241	134	1229	135	4	<b><u>631</u></b>	<b><u>131</u></b>	631	131	631	131
464.h264ref	8	<b><u>1156</u></b>	<b><u>153</u></b>	1154	153	1156	153	8	1148	154	<b><u>1146</u></b>	<b><u>155</u></b>	1144	155
471.omnetpp	8	<b><u>1019</u></b>	<b><u>49.1</u></b>	1019	49.1	1017	49.2	8	938	53.3	937	53.4	<b><u>937</u></b>	<b><u>53.3</u></b>
473.astar	8	<b><u>969</u></b>	<b><u>57.9</u></b>	969	57.9	969	58.0	8	856	65.6	<b><u>852</u></b>	<b><u>65.9</u></b>	849	66.1
483.xalancbmk	8	727	75.9	725	76.1	<b><u>726</u></b>	<b><u>76.1</u></b>	8	632	87.3	<b><u>631</u></b>	<b><u>87.5</u></b>	631	87.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

See flags file of details on following settings.  
all ulimits set to unlimited.

Environment variables set before executing benchmarks:

```
MALLOCOPTIONS=pool
MEMORY_AFFINITY=MCM
XLFRTEOPTS=intrinthds=1
```

System set to "Enhanced" mode when defining partition on HMC.  
bindprocessor command used on submit to bind each copy to a unique processor.

1000 16M large pages defined with vmo command

Remote console disabled in /etc/inittab.

fdpr binary optimization tool used for:

```
400.perlbench 401.bzip2 403.gcc 429.mcf 456.hmmmer
458.sjent 462.libquantum 464.h264ref 473.astar
```

## Base Compiler Invocation

C benchmarks:

```
/usr/vac/bin/xlc -qlanglvl=extc99
```

C++ benchmarks:

```
/usr/vacpp/bin/xlC
```



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## Base Portability Flags

```
400.perlbench: -DSPEC_CPU_AIX
462.libquantum: -DSPEC_CPU_AIX
464.h264ref: -DSPEC_CPU_AIX -qchars=signed
483.xalancbmk: -DSPEC_CPU_AIX
```

## Base Optimization Flags

```
C benchmarks:
-bmaxdata:0x50000000 -O5 -qlargepage -D_ILS_MACROS -qalias=noansi
-qalloca -blpdata

C++ benchmarks:
-bmaxdata:0x20000000 -O5 -qlargepage -D_ILS_MACROS -qrtti=all
-blpdata
```

## Base Other Flags

```
C benchmarks:
-qipa=noobject -qipa=threads -qsuppress=1500-036

C++ benchmarks:
-qipa=noobject -qipa=threads -qsuppress=1500-036
```

## Peak Compiler Invocation

```
C benchmarks:
/usr/vac/bin/xlc -qlanglvl=extc99

C++ benchmarks:
/usr/vacpp/bin/xlc
```

## Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_AIX
462.libquantum: -DSPEC_CPU_AIX
464.h264ref: -DSPEC_CPU_AIX -qchars=signed
483.xalancbmk: -DSPEC_CPU_AIX
```



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## Peak Optimization Flags

C benchmarks:

400.perlbench: -bmaxdata:0x50000000 -qpdf1(pass 1) -qpdf2(pass 2) -O4  
-qlargepage -qenablevmx -qvecnvols -D\_ILS\_MACROS  
-qalias=noansi -qfdpr -blpdata

401.bzip2: -bmaxdata:0x4ffffffc -qpdf1(pass 1) -qpdf2(pass 2) -O5  
-qlargepage -qenablevmx -qvecnvols -D\_ILS\_MACROS -qfdpr  
-blpdata

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -qpdf1(pass 1) -qpdf2(pass 2) -O4 -qlargepage -qenablevmx  
-qvecnvols -D\_ILS\_MACROS -blpdata

456.hmmer: -O5 -qlargepage -D\_ILS\_MACROS -qfdpr -blpdata

458.sjeng: -qpdf1(pass 1) -qpdf2(pass 2) -O5 -qlargepage -qenablevmx  
-qvecnvols -D\_ILS\_MACROS -qfdpr -blpdata

462.libquantum: -qpdf1(pass 1) -qpdf2(pass 2) -O5 -qlargepage -qenablevmx  
-qvecnvols -D\_ILS\_MACROS -q64 -qfdpr -blpdata

464.h264ref: -qpdf1(pass 1) -qpdf2(pass 2) -O5 -q64 -D\_ILS\_MACROS  
-qenablevmx -qvecnvols -qfdpr -bdatapsize:64K  
-bstacksize:64K -btextpsize:64K

C++ benchmarks:

471.omnetpp: -bmaxdata:0x20000000 -qpdf1(pass 1) -qpdf2(pass 2) -O5  
-qlargepage -qenablevmx -qvecnvols -D\_ILS\_MACROS  
-qalign=natural -qrtti=all -qinlglue -blpdata

473.astar: -bmaxdata:0x20000000 -qpdf1(pass 1) -qpdf2(pass 2) -O5  
-qlargepage -D\_ILS\_MACROS -qfdpr -qinlglue  
-qalign=natural -blpdata

483.xalancbmk: -bmaxdata:0x20000000 -qpdf1(pass 1) -qpdf2(pass 2) -O5  
-qlargepage -D\_ILS\_MACROS -qinlglue -D\_\_IBM\_FAST\_VECTOR  
-blpdata

## Peak Other Flags

C benchmarks:

-qipa=noobject -qipa=threads -qsuppress=1500-036

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## Peak Other Flags (Continued)

C++ benchmarks:

-qipa=noobject -qipa=threads -qsuppress=1500-036

The flags file that was used to format this result can be browsed at

[http://www.spec.org/cpu2006/flags/CPU2006\\_flags.20090714.05.html](http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.05.html)

You can also download the XML flags source by saving the following link:

[http://www.spec.org/cpu2006/flags/CPU2006\\_flags.20090714.05.xml](http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.05.xml)

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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