



# SPEC® CINT2006 Result

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Dell Inc.

SPECint®\_rate2006 = 82.3

PowerEdge 1950 III (Intel Xeon X5260, 3.33 GHz)

SPECint\_rate\_base2006 = 69.5

CPU2006 license: 55

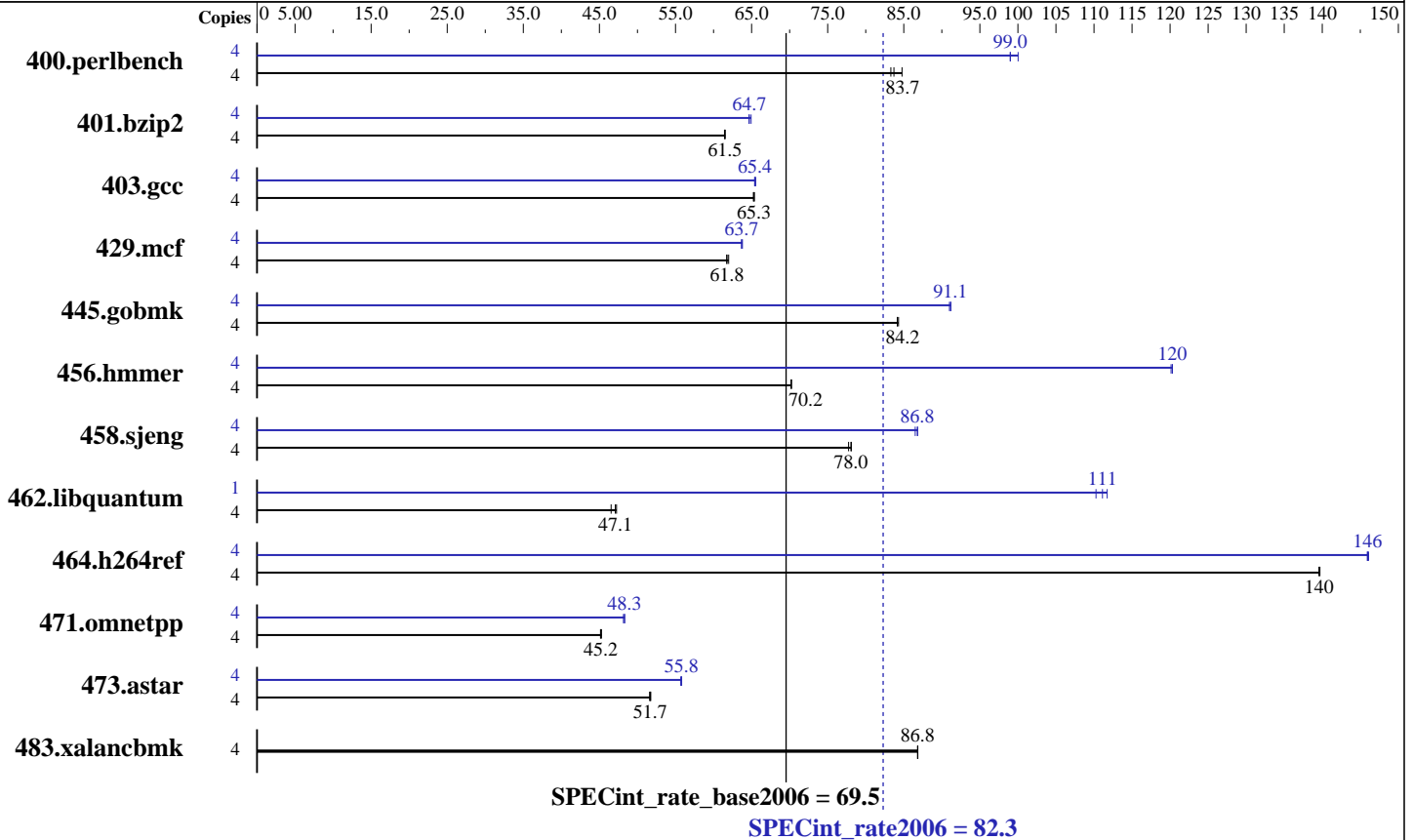
Test date: Jan-2008

Test sponsor: Dell Inc.

Hardware Availability: Jan-2008

Tested by: Dell Inc.

Software Availability: Nov-2007



### Hardware

CPU Name: Intel Xeon X5260  
 CPU Characteristics: 1333 MHz system bus  
 CPU MHz: 3333  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 6 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8x2 GB DDR2-667 ECC CL5 FB-DIMM)  
 Disk Subsystem: 1 x 73 GB SAS 15k RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP1, Kernel 2.6.16.46-0.12-smp  
 Compiler: Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l\_cc\_p\_10.1.008  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Multi-user, run level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1 Binutils 2.17.50.0.15



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	<b>467</b>	<b>83.7</b>	461	84.8	469	83.3	4	<b>395</b>	<b>99.0</b>	391	100	395	99.0
401.bzip2	4	628	61.4	627	61.5	<b>628</b>	<b>61.5</b>	4	<b>597</b>	<b>64.7</b>	594	64.9	597	64.7
403.gcc	4	493	65.4	<b>493</b>	<b>65.3</b>	494	65.2	4	<b>492</b>	<b>65.4</b>	491	65.5	492	65.4
429.mcf	4	<b>591</b>	<b>61.8</b>	591	61.7	589	62.0	4	572	63.8	<b>573</b>	<b>63.7</b>	573	63.7
445.gobmk	4	498	84.3	499	84.1	<b>499</b>	<b>84.2</b>	4	461	91.0	460	91.2	<b>460</b>	<b>91.1</b>
456.hammer	4	531	70.2	532	70.2	<b>532</b>	<b>70.2</b>	4	310	120	<b>310</b>	<b>120</b>	311	120
458.sjeng	4	620	78.1	623	77.7	<b>620</b>	<b>78.0</b>	4	<b>558</b>	<b>86.8</b>	557	86.8	560	86.5
462.libquantum	4	1781	46.5	1754	47.2	<b>1760</b>	<b>47.1</b>	1	<b>186</b>	<b>111</b>	188	110	185	112
464.h264ref	4	634	140	634	140	<b>634</b>	<b>140</b>	4	<b>606</b>	<b>146</b>	607	146	606	146
471.omnetpp	4	554	45.1	<b>553</b>	<b>45.2</b>	552	45.3	4	519	48.2	517	48.3	<b>518</b>	<b>48.3</b>
473.astar	4	<b>543</b>	<b>51.7</b>	544	51.6	543	51.8	4	<b>503</b>	<b>55.8</b>	503	55.8	504	55.7
483.xalancbmk	4	318	86.8	318	86.8	<b>318</b>	<b>86.8</b>	4	318	86.8	318	86.8	<b>318</b>	<b>86.8</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Invocation Notes

OMP\_NUM\_THREADS set to number of cores  
KMP\_STACK\_SIZE set to 64M  
KMP\_AFFINITY set to physical,0

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run  
'/usr/bin/taskset' used to bind processes to CPUs

## Platform Notes

BIOS Settings:  
Adjacent Cache Line Prefetch = Disabled (default Enabled)

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-fast -inline-calloc -opt-malloc-options=3  
  
C++ benchmarks:  
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc  
  
401.bzip2: /opt/intel/cce/10.1.008/bin/icc  
-L/opt/intel/cce/10.1.008/lib  
-I/opt/intel/cce/10.1.008/include  
  
456.hmmer: /opt/intel/cce/10.1.008/bin/icc  
-L/opt/intel/cce/10.1.008/lib  
-I/opt/intel/cce/10.1.008/include  
  
C++ benchmarks:  
icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX

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## Peak Portability Flags (Continued)

483.xalanbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch  
-opt-streaming-stores always -vec-guard-write  
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs  
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs  
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

483.xalanbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.12.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.12.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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