



SPEC[®] CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECfp[®]_rate2006 = 57.2

IBM BladeCenter HS21 XM (Intel Xeon X5260)

SPECfp_rate_base2006 = 51.1

CPU2006 license: 11

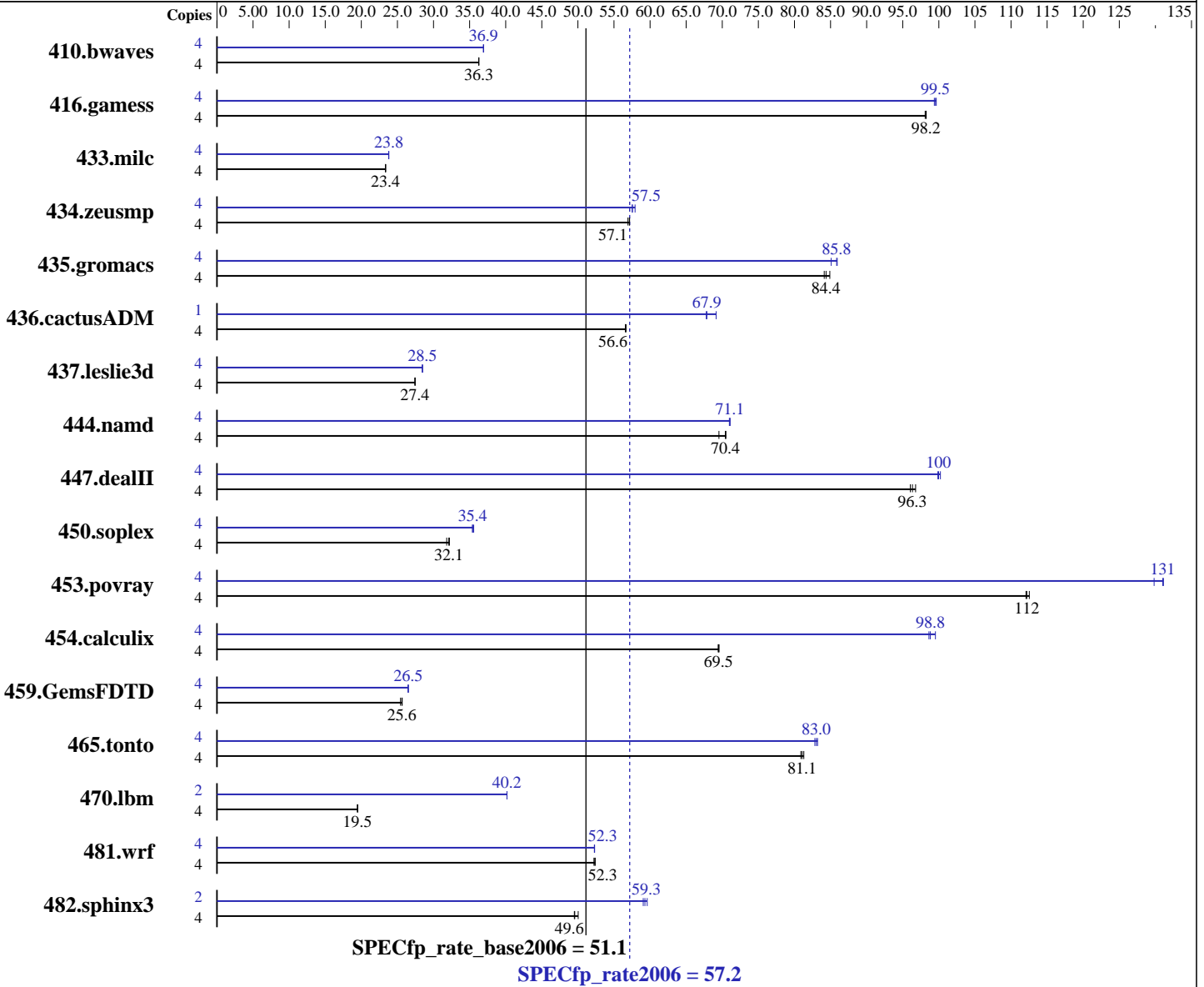
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Jan-2008

Hardware Availability: Feb-2008

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon X5260
 CPU Characteristics: 1333MHz system bus
 CPU MHz: 3325
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip

Continued on next page

Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64), Kernel 2.6.16.21-0.8-smp
 Compiler: Intel C++ and Fortran Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008, l_fc_p_10.1.008
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Multi-user, run level 3
 Base Pointers: 64-bit

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECfp_rate2006 = **57.2**

IBM BladeCenter HS21 XM (Intel Xeon X5260)

SPECfp_rate_base2006 = **51.1**

CPU2006 license: 11

Test date: Jan-2008

Test sponsor: IBM Corporation

Hardware Availability: Feb-2008

Tested by: IBM Corporation

Software Availability: Nov-2007

L3 Cache: None
Other Cache: None
Memory: 16 GB (8 x 2 GB DDR2-5300F ECC)
Disk Subsystem: 1 x 36 GB SAS, 10000 RPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: Binutils 2.17.50.0.15

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	1499	36.3	1499	36.3	1498	36.3	4	1472	36.9	1472	36.9	1472	36.9
416.gamess	4	797	98.3	798	98.1	797	98.2	4	786	99.6	788	99.4	787	99.5
433.milc	4	1572	23.4	1573	23.3	1572	23.4	4	1542	23.8	1543	23.8	1544	23.8
434.zeusmp	4	637	57.1	637	57.2	639	56.9	4	628	57.9	633	57.5	633	57.5
435.gromacs	4	340	84.1	338	84.4	336	84.9	4	332	85.9	336	85.1	333	85.8
436.cactusADM	4	845	56.6	844	56.6	843	56.7	1	176	67.9	176	67.8	173	69.1
437.leslie3d	4	1371	27.4	1370	27.4	1369	27.5	4	1320	28.5	1321	28.5	1322	28.4
444.namd	4	455	70.5	456	70.4	461	69.5	4	452	71.0	451	71.1	451	71.1
447.dealII	4	473	96.8	475	96.3	476	96.0	4	457	100	458	99.9	458	100
450.soplex	4	1048	31.8	1036	32.2	1039	32.1	4	942	35.4	938	35.6	943	35.4
453.povray	4	190	112	190	112	189	113	4	162	131	162	131	164	130
454.calculix	4	474	69.6	475	69.5	475	69.4	4	334	98.8	332	99.5	335	98.6
459.GemsFDTD	4	1654	25.7	1669	25.4	1657	25.6	4	1602	26.5	1602	26.5	1601	26.5
465.tonto	4	486	81.1	484	81.3	487	80.9	4	474	83.0	473	83.2	475	82.8
470.lbm	4	2822	19.5	2822	19.5	2821	19.5	2	684	40.2	684	40.2	684	40.2
481.wrf	4	854	52.3	856	52.2	853	52.4	4	855	52.3	854	52.3	854	52.3
482.sphinx3	4	1558	50.0	1575	49.5	1573	49.6	2	657	59.3	654	59.6	660	59.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

All benchmarks compiled in 64-bit mode except 437.leslie3d, 450.soplex 470.lbm and 482.sphinx3, at peak, are compiled in 32-bit mode
Hardware Sector Prefetch Enabled and Adjacent Sector Prefetch Enabled
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to 64M
taskset utility used to bind CPU(s) to processes

Base Compiler Invocation

C benchmarks:
icc

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECfp_rate2006 = 57.2

IBM BladeCenter HS21 XM (Intel Xeon X5260)

SPECfp_rate_base2006 = 51.1

CPU2006 license: 11

Test date: Jan-2008

Test sponsor: IBM Corporation

Hardware Availability: Feb-2008

Tested by: IBM Corporation

Software Availability: Nov-2007

Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECfp_rate2006 = 57.2

IBM BladeCenter HS21 XM (Intel Xeon X5260)

SPECfp_rate_base2006 = 51.1

CPU2006 license: 11

Test date: Jan-2008

Test sponsor: IBM Corporation

Hardware Availability: Feb-2008

Tested by: IBM Corporation

Software Availability: Nov-2007

Peak Compiler Invocation

C benchmarks (except as noted below):

```
/opt/intel/cc/10.1.008/bin/icc -L/opt/intel/cc/10.1.008/lib
-I/opt/intel/cc/10.1.008/include
```

433.milc: icc

C++ benchmarks (except as noted below):

icpc

```
450.soplex: /opt/intel/cc/10.1.008/bin/icpc -L/opt/intel/cc/10.1.008/lib
-I/opt/intel/cc/10.1.008/include
```

Fortran benchmarks (except as noted below):

ifort

```
437.leslie3d: /opt/intel/fc/10.1.008/bin/ifort -L/opt/intel/fc/10.1.008/lib
-I/opt/intel/fc/10.1.008/include
```

Benchmarks using both Fortran and C:

icc ifort

Peak Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
444.namd: -DSPEC_CPU_LP64
447.deall: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
433.milc: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias
-auto-ilp32
```

```
470.lbm: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-scalar-rep- -prefetch -opt-malloc-options=3
```

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECfp_rate2006 = 57.2

IBM BladeCenter HS21 XM (Intel Xeon X5260)

SPECfp_rate_base2006 = 51.1

CPU2006 license: 11

Test date: Jan-2008

Test sponsor: IBM Corporation

Hardware Availability: Feb-2008

Tested by: IBM Corporation

Software Availability: Nov-2007

Peak Optimization Flags (Continued)

482.sphinx3: -fast -unroll2

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias
-auto-ilp32

447.dealIII: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias -scalar-rep-

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -fast
-opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: -fast -prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0
-ansi-alias -scalar-rep-

434.zeusmp: -prof-gen(pass 1) -prof-use(pass 2) -fast

437.leslie3d: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
-opt-malloc-options=3

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0
-prefetch

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
-auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-prefetch -parallel -auto-ilp32

454.calculix: -fast -unroll-aggressive -auto-ilp32

481.wrf: -fast -auto-ilp32

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090714.11.html>



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECfp_rate2006 = 57.2

IBM BladeCenter HS21 XM (Intel Xeon X5260)

SPECfp_rate_base2006 = 51.1

CPU2006 license: 11

Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Jan-2008

Hardware Availability: Feb-2008

Software Availability: Nov-2007

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090714.11.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 16:37:25 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 6 February 2008.