SPEC® CINT2006 Result
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Intel Corporation

SPECint®_rate2006 = 20.8

Lenovo ThinkPad T60 (Intel Core Duo T2700)
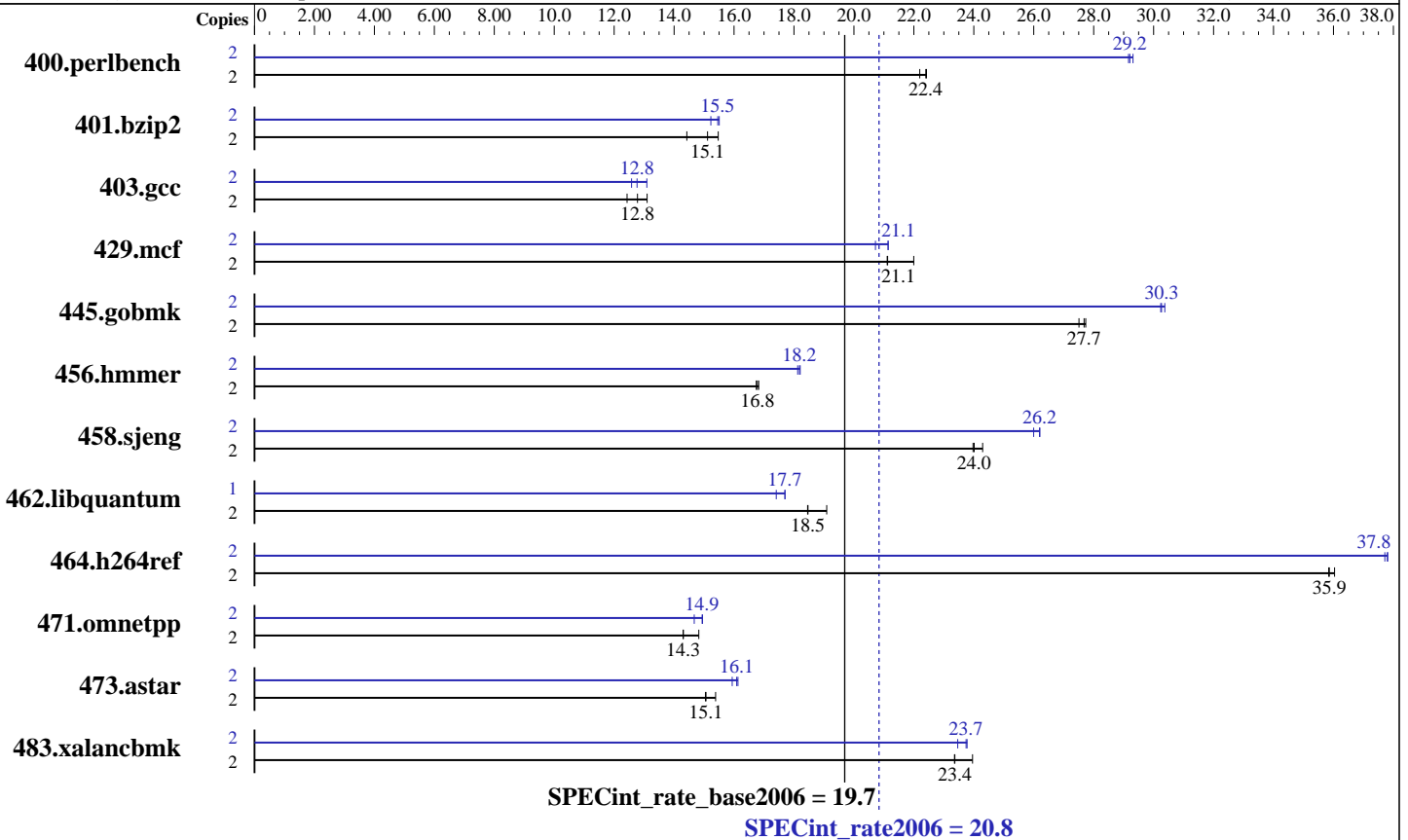
SPECint_rate_base2006 = 19.7

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Dec-2007
Hardware Availability: Jan-2008
Software Availability: Nov-2007

Copies

Benchmark	Copies	Value
400.perlbench	2	29.2
400.perlbench	2	22.4
401.bzip2	2	15.5
401.bzip2	2	15.1
403.gcc	2	12.8
403.gcc	2	12.8
429.mcf	2	21.1
429.mcf	2	21.1
445.gobmk	2	30.3
445.gobmk	2	27.7
456.hmmer	2	18.2
456.hmmer	2	16.8
458.sjeng	2	26.2
458.sjeng	2	24.0
462.libquantum	1	17.7
462.libquantum	2	18.5
464.h264ref	2	37.8
464.h264ref	2	35.9
471.omnetpp	2	14.9
471.omnetpp	2	14.3
473.astar	2	16.1
473.astar	2	15.1
483.xalancbmk	2	23.7
483.xalancbmk	2	23.4

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Hardware

CPU Name:	Intel Core Duo T2700
CPU Characteristics:	
CPU MHz:	2333
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	2 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	2 GB (2x1GB Hynix DDR2-667 CL5)
Disk Subsystem:	Hitachi 100 GB SATA, 7200 RPM
Other Hardware:	None

Software

Operating System:	Windows Vista Ultimate (32-bit)
Compiler:	Intel C++ Compiler for IA32 version 10.1 Build 20070913 Package ID: w_cc_p_10.1.011 Microsoft Visual Studio 2005 SP1 (for libraries)
Auto Parallel:	Yes
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	None
	SmartHeap Library Version 8.1 from http://www.microquill.com/

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Results Table

Benchmark	Base Copies	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Peak Copies	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio
400.perlbench	2	**872**	**22.4**	872	22.4	880	22.2	2	**669**	**29.2**	667	29.3	670	29.2
401.bzip2	2	1338	14.4	1247	15.5	**1277**	**15.1**	2	1268	15.2	1245	15.5	**1249**	**15.5**
403.gcc	2	**1260**	**12.8**	1229	13.1	1295	12.4	2	1280	12.6	**1261**	**12.8**	1230	13.1
429.mcf	2	**863**	**21.1**	829	22.0	864	21.1	2	880	20.7	863	21.1	**863**	**21.1**
445.gobmk	2	763	27.5	756	27.7	**758**	**27.7**	2	**693**	**30.3**	691	30.4	694	30.2
456.hmmer	2	1115	16.7	**1112**	**16.8**	1109	16.8	2	**1026**	**18.2**	1025	18.2	1030	18.1
458.sjeng	2	1009	24.0	996	24.3	**1008**	**24.0**	2	931	26.0	923	26.2	**924**	**26.2**
462.libquantum	2	2246	18.5	2170	19.1	**2244**	**18.5**	1	1190	17.4	**1171**	**17.7**	1171	17.7
464.h264ref	2	1235	35.8	1228	36.0	**1234**	**35.9**	2	1174	37.7	1171	37.8	**1171**	**37.8**
471.omnetpp	2	874	14.3	843	14.8	**874**	**14.3**	2	852	14.7	836	14.9	**837**	**14.9**
473.astar	2	**932**	**15.1**	913	15.4	933	15.0	2	881	15.9	**873**	**16.1**	870	16.1
483.xalancbmk	2	591	23.4	576	24.0	**591**	**23.4**	2	588	23.5	580	23.8	**581**	**23.7**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
The system bus runs at 667 MHz
Binaries were built on Windows Vista (32-bit)
The following VS 2005 SP1 updates were applied: KB926601 and KB932232
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
```

Base Compiler Invocation

C benchmarks:
```
icl -Qvc8 -Qc99
```

C++ benchmarks:
```
icl -Qvc8
```

Base Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword
```

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Base Optimization Flags

C benchmarks:
```
    -QxP -Qipo -O3 -Qprec-div- -Qvec-guard-write /F512000000
```

C++ benchmarks:
```
    -QxP -Qipo -O3 -Qprec-div- -Qcxx_features /F512000000 shlw32m.lib
              -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
        403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks:
```
    icl -Qvc8 -Qc99
```

C++ benchmarks:
```
    icl -Qvc8
```

Peak Portability Flags

```
        403.gcc: -DSPEC_CPU_WIN32
     464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
   483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword
```

Peak Optimization Flags

C benchmarks:

```
    400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
                   -Qprec-div- -Qansi-alias -Qprefetch /F512000000
                   shlw32m.lib             -link /FORCE:MULTIPLE

       401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
                  -Qprec-div- -Qprefetch /F512000000

         403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
                  -Qprec-div- /F512000000
```

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Peak Optimization Flags (Continued)

```
  429.mcf: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
           -Qprec-div- -Qprefetch /F512000000 shlw32m.lib
                    -link /FORCE:MULTIPLE

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -O2 -Qipo
           -Qprec-div- -Qansi-alias /F512000000

456.hmmer: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
           -Qprec-div- -Qunroll2 -Qansi-alias
           -Qopt-multi-version-aggressive /F512000000

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
           -Qprec-div- -Qunroll4 /F512000000

462.libquantum: -QxP -Qipo -O3 -Qprec-div- -Qunroll4 -Ob0 -Qprefetch
           -Qopt-streaming-stores:always -Qparallel
           -Qpar-runtime-control /F512000000

464.h264ref: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
           -Qprec-div- -Qunroll2 -Qansi-alias /F512000000
```

C++ benchmarks:

```
471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
           -Qprec-div- -Qansi-alias -Qopt-ra-region-strategy=block
           -Qcxx_features /F512000000 shlw32m.lib
                    -link /FORCE:MULTIPLE

 473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
           -Qprec-div- -Qansi-alias -Qopt-ra-region-strategy=routine
           -Qcxx_features /F512000000 shlw32m.lib
                    -link /FORCE:MULTIPLE

483.xalancbmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
           -Qprec-div- -Qansi-alias -Qcxx_features /F512000000
           shlw32m.lib              -link /FORCE:MULTIPLE
```

Peak Other Flags

C benchmarks:

```
  403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.09.html

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You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.09.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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