



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Intel Corporation

Alienware Area-51 M15x-R1 (Intel Core 2 Extreme X7900)

SPECint®2006 = 19.8

SPECint_base2006 = 18.3

CPU2006 license: 13

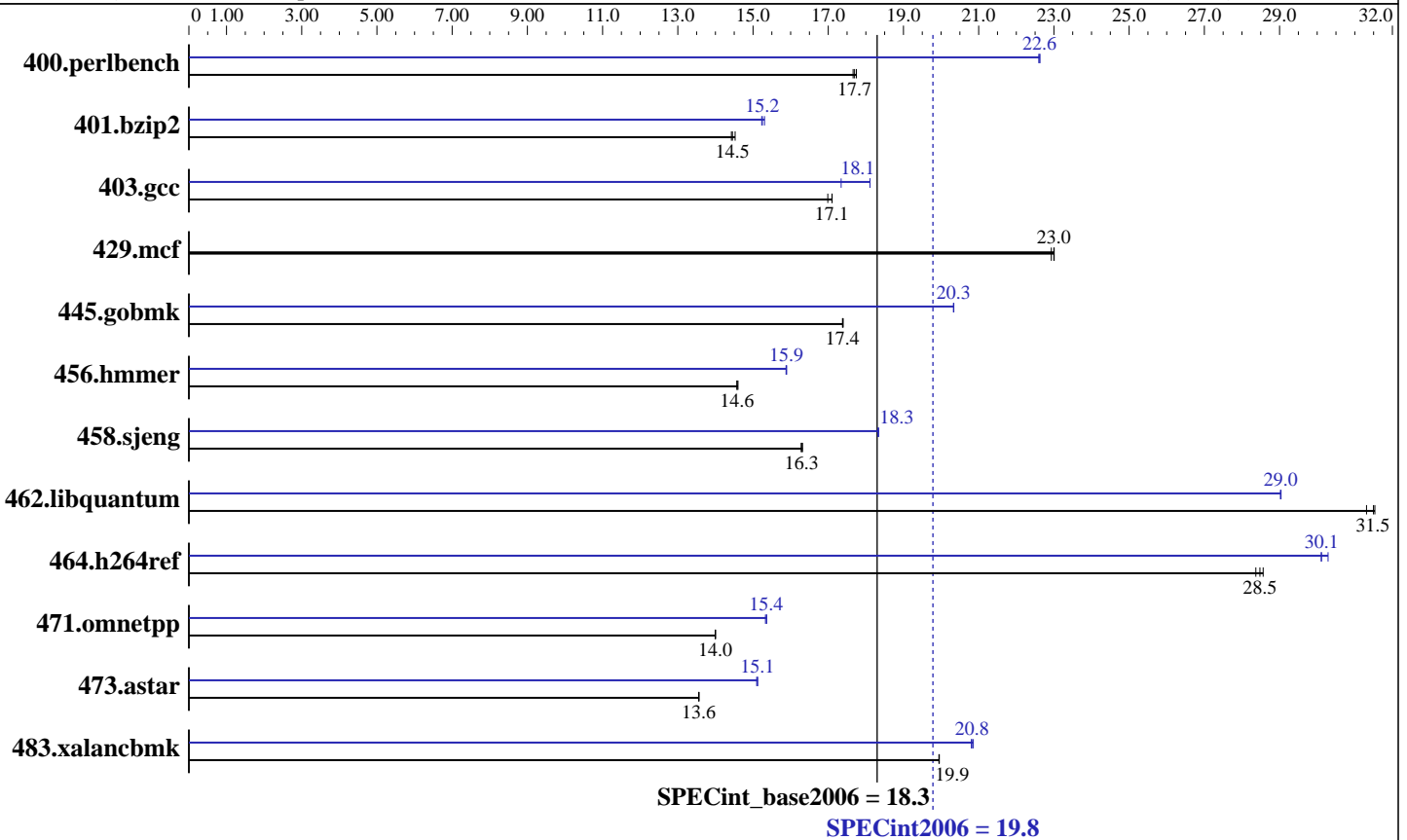
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Dec-2007

Hardware Availability: Jan-2008

Software Availability: Nov-2007



Hardware

CPU Name: Intel Core 2 Extreme X7900
 CPU Characteristics:
 CPU MHz: 2800
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 4 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 2 GB (2x1GB Qimonda DDR2-667 CL5)
 Disk Subsystem: Fujitsu 120GB GB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: Windows Vista Ultimate(32-bit)
 Compiler: Intel C++ Compiler for IA32 version 10.1
 Build 20070913 Package ID: w_cc_p_10.1.011
 Microsoft Visual Studio 2005 SP1 (for libraries)
 Auto Parallel: Yes
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: None
 SmartHeap Library Version 8.1 from
<http://www.microquill.com/>



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Intel Corporation

Alienware Area-51 M15x-R1 (Intel Core 2 Extreme X7900)

SPECint2006 = 19.8

SPECint_base2006 = 18.3

CPU2006 license: 13

Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Dec-2007

Hardware Availability: Jan-2008

Software Availability: Nov-2007

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	553	17.7	551	17.7	<u>552</u>	<u>17.7</u>	432	22.6	<u>432</u>	<u>22.6</u>	432	22.6
401.bzip2	669	14.4	664	14.5	<u>668</u>	<u>14.5</u>	<u>633</u>	<u>15.2</u>	634	15.2	631	15.3
403.gcc	474	17.0	471	17.1	<u>471</u>	<u>17.1</u>	464	17.3	445	18.1	<u>445</u>	<u>18.1</u>
429.mcf	398	22.9	396	23.0	<u>397</u>	<u>23.0</u>	398	22.9	396	23.0	<u>397</u>	<u>23.0</u>
445.gobmk	604	17.4	<u>603</u>	<u>17.4</u>	603	17.4	516	20.3	<u>516</u>	<u>20.3</u>	516	20.3
456.hammer	<u>640</u>	<u>14.6</u>	640	14.6	639	14.6	588	15.9	587	15.9	<u>587</u>	<u>15.9</u>
458.sjeng	<u>743</u>	<u>16.3</u>	743	16.3	742	16.3	<u>660</u>	<u>18.3</u>	660	18.3	660	18.3
462.libquantum	662	31.3	<u>658</u>	<u>31.5</u>	657	31.5	714	29.0	714	29.0	<u>714</u>	<u>29.0</u>
464.h264ref	780	28.4	<u>777</u>	<u>28.5</u>	775	28.6	<u>735</u>	<u>30.1</u>	735	30.1	731	30.3
471.omnetpp	447	14.0	<u>446</u>	<u>14.0</u>	446	14.0	408	15.3	407	15.4	<u>407</u>	<u>15.4</u>
473.astar	<u>518</u>	<u>13.6</u>	518	13.6	518	13.6	465	15.1	<u>464</u>	<u>15.1</u>	464	15.1
483.xalanbmk	346	20.0	<u>346</u>	<u>19.9</u>	346	19.9	332	20.8	331	20.9	<u>331</u>	<u>20.8</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

The system bus runs at 800 MHz
 Binaries were built on Windows Vista32
 The following VS 2005 SP1 updates were applied: KB926601 and KB932232
 OMP_NUM_THREADS set to number of cores
 KMP_AFFINITY set to physical,0

Base Compiler Invocation

C benchmarks:
 icl -Qvc8 -Qc99

C++ benchmarks:
 icl -Qvc8

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
 464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
 483.xalanbmk: -Qoption,cpp,--no_wchar_t_keyword



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Intel Corporation

SPECint2006 = 19.8

Alienware Area-51 M15x-R1 (Intel Core 2 Extreme X7900)

SPECint_base2006 = 18.3

CPU2006 license: 13

Test date: Dec-2007

Test sponsor: Intel Corporation

Hardware Availability: Jan-2008

Tested by: Intel Corporation

Software Availability: Nov-2007

Base Optimization Flags

C benchmarks:

```
-fast -Qparallel -Qpar-runtime-control -Qvec-guard-write /F512000000
libguide40.lib
```

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib libguide40.lib
-link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks:

```
icl -Qvc8 -Qc99
```

C++ benchmarks:

```
icl -Qvc8
```

Peak Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qprefetch -Qparallel -Qpar-runtime-control /F512000000
shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch
/F512000000 libguide40.lib

403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
libguide40.lib
```

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Intel Corporation

SPECint2006 = 19.8

Alienware Area-51 M15x-R1 (Intel Core 2 Extreme X7900)

SPECint_base2006 = 18.3

CPU2006 license: 13

Test date: Dec-2007

Test sponsor: Intel Corporation

Hardware Availability: Jan-2008

Tested by: Intel Corporation

Software Availability: Nov-2007

Peak Optimization Flags (Continued)

429.mcf: basepeak = yes

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo
-Qprec-div- -Qansi-alias /F512000000

456.hmmer: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
-Qansi-alias -Qopt-multi-version-aggressive /F512000000
libguide40.lib

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4
/F512000000 libguide40.lib

462.libquantum: -fast -Qunroll4 -Ob0 -Qprefetch
-Qopt-streaming-stores:always -Qparallel
-Qpar-runtime-control /F512000000 libguide40.lib

464.h264ref: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
-Qansi-alias /F512000000 libguide40.lib

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qopt-ra-region-strategy=block -Qcxx_features /F512000000
shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qopt-ra-region-strategy=routine -Qcxx_features /F512000000
shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

483.xalancbmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qcxx_features /F512000000 shlw32m.lib libguide40.lib
-link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.09.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.09.xml>



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Intel Corporation

Alienware Area-51 M15x-R1 (Intel Core 2 Extreme X7900)

SPECint2006 = 19.8

SPECint_base2006 = 18.3

CPU2006 license: 13

Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Dec-2007

Hardware Availability: Jan-2008

Software Availability: Nov-2007

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 16:02:56 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 23 January 2008.