



# SPEC® CINT2006 Result

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## Intel Corporation

Alienware Area-51 M15x-R1 (Intel Core 2 Extreme X9000)

**SPECint®2006 = 20.7**

**SPECint\_base2006 = 19.0**

CPU2006 license: 13

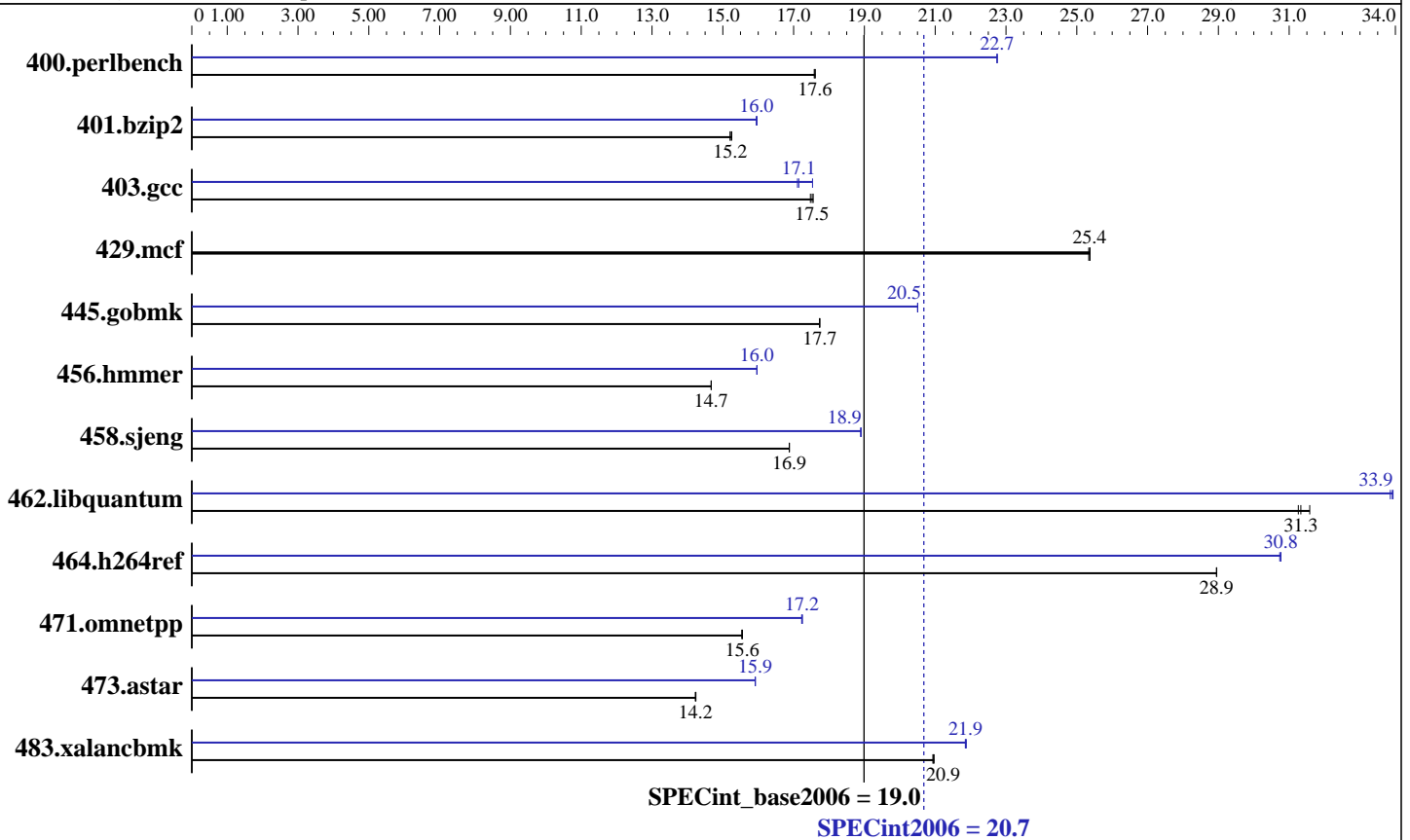
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Dec-2007

Hardware Availability: Jan-2008

Software Availability: Nov-2007



### Hardware

CPU Name: Intel Core 2 Extreme X9000  
 CPU Characteristics:  
 CPU MHz: 2800  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 6 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 2 GB (2x1GB Qimonda DDR2-667 CL5)  
 Disk Subsystem: Fujitsu 120GB GB SATA, 7200 RPM  
 Other Hardware: None

### Software

Operating System: Windows Vista Ultimate(32-bit)  
 Compiler: Intel C++ Compiler for IA32 version 10.1  
 Build 20070913 Package ID: w\_cc\_p\_10.1.011  
 Microsoft Visual Studio 2005 SP1 (for libraries)  
 Auto Parallel: Yes  
 File System: NTFS  
 System State: Default  
 Base Pointers: 32-bit  
 Peak Pointers: 32-bit  
 Other Software: None  
 SmartHeap Library Version 8.1 from  
<http://www.microquill.com/>



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	556	17.6	<b><u>555</u></b>	<b><u>17.6</u></b>	555	17.6	429	22.8	430	22.7	<b><u>429</u></b>	<b><u>22.7</u></b>
401.bzip2	<b><u>634</u></b>	<b><u>15.2</u></b>	633	15.3	635	15.2	<b><u>605</u></b>	<b><u>16.0</u></b>	604	16.0	605	15.9
403.gcc	461	17.5	<b><u>459</u></b>	<b><u>17.5</u></b>	459	17.6	459	17.5	<b><u>469</u></b>	<b><u>17.1</u></b>	471	17.1
429.mcf	359	25.4	<b><u>360</u></b>	<b><u>25.4</u></b>	360	25.3	359	25.4	<b><u>360</u></b>	<b><u>25.4</u></b>	360	25.3
445.gobmk	<b><u>591</u></b>	<b><u>17.7</u></b>	591	17.7	592	17.7	512	20.5	<b><u>512</u></b>	<b><u>20.5</u></b>	512	20.5
456.hammer	636	14.7	636	14.7	<b><u>636</u></b>	<b><u>14.7</u></b>	584	16.0	584	16.0	<b><u>584</u></b>	<b><u>16.0</u></b>
458.sjeng	717	16.9	717	16.9	<b><u>717</u></b>	<b><u>16.9</u></b>	640	18.9	640	18.9	<b><u>640</u></b>	<b><u>18.9</u></b>
462.libquantum	656	31.6	663	31.3	<b><u>661</u></b>	<b><u>31.3</u></b>	611	33.9	<b><u>611</u></b>	<b><u>33.9</u></b>	612	33.9
464.h264ref	764	29.0	765	28.9	<b><u>764</u></b>	<b><u>28.9</u></b>	720	30.7	<b><u>719</u></b>	<b><u>30.8</u></b>	719	30.8
471.omnetpp	402	15.5	<b><u>402</u></b>	<b><u>15.6</u></b>	402	15.6	363	17.2	<b><u>363</u></b>	<b><u>17.2</u></b>	362	17.2
473.astar	<b><u>493</u></b>	<b><u>14.2</u></b>	494	14.2	493	14.2	441	15.9	441	15.9	<b><u>441</u></b>	<b><u>15.9</u></b>
483.xalanbmk	<b><u>329</u></b>	<b><u>20.9</u></b>	330	20.9	329	21.0	316	21.9	315	21.9	<b><u>316</u></b>	<b><u>21.9</u></b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

The system bus runs at 800 MHz  
 Binaries were built on Windows Vista32  
 The following VS 2005 SP1 updates were applied: KB926601 and KB932232  
 OMP\_NUM\_THREADS set to number of cores  
 KMP\_AFFINITY set to physical,0

## Base Compiler Invocation

C benchmarks:  
 icl -Qvc8 -Qc99

C++ benchmarks:  
 icl -Qvc8

## Base Portability Flags

403.gcc: -DSPEC\_CPU\_WIN32  
 464.h264ref: -DSPEC\_CPU\_NO\_INTTYPES -DWIN32  
 483.xalanbmk: -Qoption,cpp,--no\_wchar\_t\_keyword



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## Base Optimization Flags

C benchmarks:

```
-fast -Qparallel -Qpar-runtime-control -Qvec-guard-write /F512000000
libguide40.lib
```

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib libguide40.lib
-link /FORCE:MULTIPLE
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

## Peak Compiler Invocation

C benchmarks:

```
icl -Qvc8 -Qc99
```

C++ benchmarks:

```
icl -Qvc8
```

## Peak Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword
```

## Peak Optimization Flags

C benchmarks:

```
400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qprefetch -Qparallel -Qpar-runtime-control /F512000000
shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch
/F512000000 libguide40.lib

403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
libguide40.lib
```

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## Peak Optimization Flags (Continued)

429.mcf: basepeak = yes

445.gobmk: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -QxT -O2 -Qipo  
-Qprec-div- -Qansi-alias /F512000000

456.hmmer: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qunroll2  
-Qansi-alias -Qopt-multi-version-aggressive /F512000000  
libguide40.lib

458.sjeng: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qunroll4  
/F512000000 libguide40.lib

462.libquantum: -fast -Qunroll4 -Ob0 -Qprefetch  
-Qopt-streaming-stores:always -Qparallel  
-Qpar-runtime-control /F512000000 libguide40.lib

464.h264ref: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qunroll2  
-Qansi-alias /F512000000 libguide40.lib

C++ benchmarks:

471.omnetpp: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qansi-alias  
-Qopt-ra-region-strategy=block -Qcxx\_features /F512000000  
shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

473.astar: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qansi-alias  
-Qopt-ra-region-strategy=routine -Qcxx\_features /F512000000  
shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

483.xalancbmk: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qansi-alias  
-Qcxx\_features /F512000000 shlw32m.lib libguide40.lib  
-link /FORCE:MULTIPLE

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.09.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.09.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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